## Tunnel Field-Effect Transistors (TFET)

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## Modelling and Simulation

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## Preface

## Overview

Just as a human body is made up of millions of biological cells, an integrated circuit is made up of millions of transistors. Transistors are the basic building blocks of all modern electronic gadgets. Ever since the advent of CMOS circuits, the dimensions of the transistor have been continuously scaled down in order to pack more logic on to a silicon wafer and also to reduce power consumption in the circuits. In recent years, with mobile devices becoming popular, the search for low power devices with steep switching characteristics has become important. Highly scaled MOSFETs are rendered unsuitable for low power applications due to a thermal limit on their switching. Hence, the Tunnel Field Effect Transistor (TFET) is being explored extensively for low power applications. A TFET has a steep switching characteristic as it works on the phenomena of band-to-band tunnelling. Over the past few years, TFETs have been heavily researched by various notable groups in the field of semiconductor devices across the globe.

This book provides a comprehensive guide for those who are beginning their study on TFETs and also as a guide for those who wish to design integrated circuits based on TFETs. The book covers the essential physics behind the functioning of the TFETs and also the device modelling of TFETs, for the purpose of circuit design and simulation. It begins with studying the basic principles of quantum mechanics and then builds up to the physics behind the quantum mechanical phenomena of band-to-band tunnelling. This is followed by studying the basic functioning of TFETs and their different structural configurations. After explaining the functioning of TFETs, the book describes different approaches used by researchers for developing the drain current models for TFETs. Finally, to help new researchers in the field of TFETs, the book describes the process of carrying out numerical simulations of TFETs using the TCAD tool Silvaco ATLAS. Numerical simulations are helpful tools for studying the behaviour of any semiconductor device without getting into the complex process of fabrication and characterisation.

## Key feature in relation to existing literature

This book is the first comprehensive literature on TFETs, which are very popular transistors and have been extensively studied in recent years; they are going to be important building blocks for low power solid state circuits in the future. It is a one-stop volume for studying TFETs for someone who has a basic knowledge of MOSFET physics. It covers the physics behind the phenomena of tunnelling as well as the device physics of TFETs. It also has a unique feature of describing device simulation along with device physics so as to enable readers to do further research on TFETs.

The presentation of the book is clear and accurate and is written in simple language. The book endeavours to explain different phenomena in the TFETs using simple and logical explanations so as to enable the reader to get a real feel for the functioning of the device. Also, each and every aspect of the TFET has been compared to that of the MOSFET so that the facts presented in the book make more sense to the entire semiconductor device fraternity and help in the integration of the TFET with the prevailing technology in the industry. The book also attempts to cover all the recent research articles published on TFETs so as to make sure that, along with covering the basics, it also covers state of the art work on TFETs.

## 1

## Quantum mechanics

### 1.1 Introduction to quantum mechanics

Before attempting to investigate the workings of a tunnelling field-effect transistor, it is essential to be familiar with the concept of tunnelling. Tunnelling is a quantum phenomenon, with no counterpart in the everyday physics one encounters, or the physics that one applies while dealing with devices a few hundred nanometres in length. The initial two chapters will, therefore, help us develop an understanding of quantum phenomena. In this chapter, we will present an introduction to the field of quantum mechanics and the next chapter will discuss the phenomenon of tunnelling in detail.

The chapter begins with a description of a landmark experiment that conclusively proved the wave nature of particles, after which we will study the concept of wavefunctions and how to use Schrodinger's equation to obtain them. A few basic problems will be presented so that the readers may familiarise themselves with basic quantum concepts.

### 1.1.1 The double slit experiment

There are many experiments that led to the conception of quantum mechanics blackbody radiation, the Stern Gerlach experiment, the photoelectric effect, the line spectrum, etc. However, for our purposes we will concentrate on one of the landmark experiments, that is the double slit experiment, which demonstrated the fundamental quantum nature (i.e. both wave and particle) of electrons.

[^0]You would have read that only waves can undergo superposition, and not particles. Superposition is the fundamental principle behind the occurrence of interference - therefore, if something exhibits interference, it must have a wave nature. The double slit experiment is famously associated with Thomas Young, who used it for the first time in the early nineteenth century to prove the wave nature of light. Before this experiment was performed, light had been associated with a particle nature (since the times of Newton), and the fact that it underwent interference was conclusive proof of its wave nature.

However, the behaviour of light that led Newton and others to believe that it had a particle nature could not be reconciled with this newly formed wave picture. It took another century of research and experiments to establish a rather astonishing result regarding the behaviour of light - that it displays both particle and wave natures. The particle nature leads to phenomena such as the photoelectric effect and rectilinear propagation of light in ray optics; the wave nature explained the interference and diffraction of light.

While this dual nature (that is both particle and wave natures) of light was being worked out, many people were, independently, studying the behaviour of subatomic particles. Phenomenon like the discrete line spectrum of hydrogen, the observed distribution of blackbody radiation, etc., could not be explained by any established theory. Theoretical physicists were in a quandary. At this point, de Broglie hypothesised that, just like light, particles possess a dual nature as well. When de Broglie made this hypothesis, there was little evidence to support his claim. A few years later, Davisson and Germer experimentally observed that electrons underwent diffraction just as light did. These were landmark moments in the history of physics - de Broglie received the Nobel Prize in physics (the second time it was awarded for a PhD thesis) and, later, so did Davisson and Germer. While the Davisson-Germer experiment was the first to establish the dual nature of matter, the double slit interference experiment is far easier to conceptually grasp and visualise, which is why we will use it to embark on our study of quantum phenomena.

The setup of an electron interferometer used in the double slit experiment is conceptually quite similar to that of a light interferometer (Figure 1.1). A parallel beam of electrons is incident on a screen with two slits. The electrons that pass through the slits impinge upon the optical screen, where their incidence is captured by a visible spot. First, let us think of these electrons as if they were the kind of particles we observe in our daily lives (classical particles) and see how they should behave. All the electrons in the initial beam have the same speed and direction of motion and they are heading towards the screen with two slits. All the electrons that hit this screen are blocked, except for the ones passing right through the slits. These electrons that passed through the slits should have no reason to change either their speed or the direction of their motion. They do not "know" that there
(a)

(b)


Figure 1.1 (a) Classically predicted electron pattern. It can be seen that interference fringes are experimentally observed, as opposed to the classically predicted pattern. This establishes the wave-like behaviour of electrons, (b) experimentally observed electron pattern.
was a screen in the first place - they pass through unaffected. They subsequently keep heading straight and hit the final screen as illustrated in Figure 1.1(a). Two narrow bands are formed on the screen, corresponding to the two thin beams of electrons that passed unaffected through the two small slits.

Now let us take a look at what was actually observed in the experiment. There was an interference pattern on the screen, as shown in Figure 1.1(b), a pattern uncannily similar to what is observed when we perform the same experiment with light instead of electrons. At this juncture, you might hypothesise this behaviour to result from some sort of statistical phenomenon due to the large number of electrons. However, the experiment is far from finished, and further strangeness lies ahead.

Let us now adjust the electron source so that instead of a beam of electrons it sends a single electron at a time. This time, we find something even more extraordinary - after a lot of electrons have hit the screen, the same interference pattern builds up as in the case of a beam of many electrons. There is no way this electron "knows" that it has been preceded by, or it will be followed by, another electron. What, then, could be happening? The answer is even more puzzling than the question, and will take you quite a while to come to terms with - each and every electron is undergoing interference with itself. This is what leads to the final conclusion that not just aggregates of particles but each and every particle exhibits a wave nature. To make this point clear, let us modify the experiment such that we are able to find out through which slit each electron passes. Independent of how we find out which slit each electron passes through, we get exactly the same result, that is the interference pattern vanishes and we get the pattern shown in Figure 1.1(a), as predicted by classical mechanics. Think about this very carefully, because this
point merits serious investigation. For a wave to show interference, there need to be two sources - the two slits in this case. Thus, for a single electron to show interference, it must be passing through both slits. However, this is not possible! At the very least, we cannot imagine such a situation. It is only reasonable to assume that the electron either goes through one slit or the other, but the moment we impose such a restriction on the electron, we are thinking of it as a classical particle. By just knowing which slit the electron is going through, and thereby imposing the condition that it will pass through either one slit or the other, we are restricting it to behave like a classical particle. While the mathematical foundations will be laid later in this chapter, for now the reader should try and grasp the underlying concept - the quantum electron passes through both the slits; it is a superposition of these two states (corresponding to passing through the upper or lower slit). You may think that the electron actually passes through either of the two slits and due to limitations of our experimental techniques, we do not know which slit it passes through. This is not the case - the electron is indeed passing through both the slits. This counterintuitive phenomenon is at the very root of quantum mechanics and it will take some time for us to be familiar with this kind of approach. You cannot ask of the quantum electron (or any general quantum particle), "Which slit does it pass through?" The question in itself is wrong. It passes through both. It should be noted that this wave nature of a particle becomes appreciable only at very small sizes, such as a few nanometres.

### 1.1.2 Basic concepts of quantum mechanics

### 1.1.2.1 Wavefunctions

The behaviour of classical particles can be fully explained by describing how their position changes with time. This information would be sufficient to give us the trajectory, the velocity, the momentum and the acceleration of the particle. However, what of the quantum particle? Surely, the electron that passed through both slits of the double slit experiment cannot be assigned a precise location. This leads us to the realisation that we need some new method to describe the quantum particle. The rest of this chapter is devoted to formulating a mathematical picture that is able to capture the unusual behaviour of quantum particles.

The search for this new method of description was helped by the knowledge that the quantum behaviour of particles closely resembled the behaviour displayed by waves. Waves of many kinds - electromagnetic waves, sound waves, etc. - had been extensively studied, and all these waves were described by wave equations. These equations described the behaviour of a wave at every point in space, and at all times. For example, in the case of sound waves, the wave equation described the displacement $(\Delta \vec{r})$ of each particle as a function of time:

$$
\begin{equation*}
\Delta \vec{r}=\vec{\psi}(x, y, z, t) \tag{1.1}
\end{equation*}
$$

Similarly, for an electromagnetic wave, the wave equations described the electric $(\vec{E})$ or magnetic $(\vec{B})$ field at each and every point as a function of time:

$$
\begin{align*}
\vec{E} & =\overrightarrow{\psi_{E}}(x, y, z, t)  \tag{1.2}\\
\vec{B} & =\overrightarrow{\psi_{B}}(x, y, z, t) \tag{1.3}
\end{align*}
$$

Taking the cue from these equations, physicists assigned a similar wave equation to the quantum particle. This equation was called the wavefunction of the quantum particle and was usually denoted by the Greek symbol $\psi(\mathrm{psi})$. Just like in the case of classical waves, this wavefunction contained all the information about the particle - its current state and the variation of its behaviour with time. It is important to note that while the previously described wave equations (1.1) to (1.3) were real functions, the wavefunction of a quantum particle is a complex function.

### 1.1.2.2 Born interpretation

While the wavefunction-based formulation of quantum mechanics was proposed by Erwin Schrodinger quite early, he was at a loss to ascribe any physical meaning to it. The theory he built up described what sort of mathematical operations one needed to perform on the wavefunction to get information relating to its various properties, such as its position, its momentum, its energy, etc. However, what this wavefunction itself meant was a mystery, especially because it was a complex function. Many interpretations were proposed as to the meaning of this wavefunction, but the one that is most widely accepted was proposed by Max Born. It is known as the "Born interpretation of quantum mechanics" and is one of the fundamental principles of quantum mechanics. According to this interpretation, the wavefunction $\psi$ is the "probability amplitude" of the quantum particle, the square of whose magnitude gives us the probability density $\rho$ of finding that particle at any point:

$$
\begin{equation*}
\rho=|\psi|^{2}=\psi^{*} \psi \tag{1.4}
\end{equation*}
$$

where $\psi *$ is the complex conjugate of $\psi$. Using this interpretation, the probability $P$ of finding the particle in a volume $V$ at any time $t$ would be

$$
\begin{equation*}
P(V, t)=\iiint_{V} \rho d x d y d z \tag{1.5}
\end{equation*}
$$

Since the probability of finding the particle in the entire space should always be unity, we can say that

$$
\begin{equation*}
\iiint_{-\infty}^{\infty} \psi^{*} \psi d x d y d z=1 \tag{1.6}
\end{equation*}
$$

A wavefunction that displays this property is called a "normalised" wavefunction.

It is very important to realise that the probabilistic behaviour that follows from the Born interpretation is different from the probabilistic behaviour encountered in statistical mechanics. For example, consider an ensemble of particles in a chamber each occupying a particular position. This allows us to calculate the probability of finding a particle at any position. If there were only a single classical particle in this chamber, we could always precisely identify its position. However, in quantum mechanics, every single particle is "spread out" in space, and its position is uncertain. Even if there is only a single electron, we cannot say, "The electron is at this particular point". We can only talk about the probability of finding the electron at any given point once we measure its position. Thus, the Born interpretation provided a physical meaning to the wavefunction that was compatible with the fundamentally probabilistic behaviour of a quantum particle, and gave a mathematical approach to calculate the probability of finding a quantum particle at any region in space.

### 1.1.2 3 Measurement

Measurement is a fundamental process in our lives, yet it is so much a part of our instincts that we barely pay any attention to it. However, if you think carefully, most of the information you get is by the process of measurement. When you look at a tree, your eyes measure the frequency and amplitude of the incoming electromagnetic waves, giving you information regarding the colour and brightness of the tree. Subsequently, your eyes measure the angular difference between the signals received by the two eyes, and calculations by your brain tell you how far away this tree is. You may hear a bird chirping on this tree - once again, due to your ears measuring the frequency and location of the pressure waves (sound) impinging upon them. Similarly, any information we get about a quantum particle is by the process of measurement - measuring the position, energy, momentum, etc.

However, there is a very fundamental difference between measurement in classical physics and quantum physics. While the state of a classical particle is independent of measurements performed on it, in quantum mechanics, the state of the quantum particle is intricately linked to measurements performed on it. We shall go back to the double slit experiment to illustrate this point. When
we measured which slit the electron passed through, that is when we measured its position, it stopped showing interference. The electron, before measurement, exhibited interference. After we carried out the measurement, it no longer showed interference. This shows that measurement changed the state of the electron. In general, measurement changes the state of a quantum particle, and its final state (after measurement) depends both on its initial state and the kind of measurement being performed. Do not be worried if the picture is not completely clear yet - to fully understand the process of measurement, we will have to know about operators and eigenvalues, which we will do in the next two sections.

### 1.1.2.4 Operators

The Born interpretation told us that we can obtain the probability of finding a quantum particle at any given point if we know its wavefunction. However, the wavefunction contains far more information than this. If you remember, the wavefunction was supposed to contain all the information about the quantum particle. How, then, do we extract this information from the wavefunction?

Since the wavefunction is a mathematical function, it is clear that we will be performing certain mathematical operations on it to get the information we desire. This mathematical operation must be different, depending on the specific kind of information - energy, momentum, position, etc. - we need to obtain. This, indeed, is the case.

Corresponding to every physically observable parameter (also called observables) of a quantum particle, such as position, momentum, energy, we have mathematical operators. The operators for certain common observables are listed below in Table 1.1, where $l$ (iota) is the square root of negative unity and $\hbar$ (h-cross or h-bar) is the reduced Planck's constant.

To understand the use of these operators, let us imagine an experiment where we have a large number of quantum particles with the same wavefunction $\psi$. We wish to measure a particular observable, the mathematical operator corresponding to which is $O$. The outcome of each measurement is $o$. As the behaviour of

Table 1.1 Quantum mechanical operators corresponding to physical observables.

| Observable | Operator |
| :--- | :---: |
| Position $(x)$ | $x$ |
| Momentum $(\vec{p})$ | $-i \hbar \vec{\nabla}$ |
| Energy $(E)$ | $i \hbar \frac{\partial}{\partial t}$ |

quantum particles is probabilistic, measuring $O$ for every particle will give a different outcome $o$. Looking back to our example of the double slit experiment, all the incoming electrons were exactly similar. However, when we start measuring which slit they pass through, sometimes we find that an electron passes through the upper slit and at other times through the lower slit. We can, therefore, only discuss the expectation value $\langle o\rangle$ after taking an average of all the measurements. This expectation value of the observable $o$ is given as

$$
\begin{equation*}
\langle o\rangle=\frac{\iiint_{-\infty}^{\infty} \psi^{*} O \psi d x d y d z}{\iiint_{-\infty}^{\infty} \psi^{*} \psi d x d y d z} \tag{1.7}
\end{equation*}
$$

The above equation tells us about the expectation value when we perform a large number of measurements, all on particles with the same wavefunction $\psi$. However, if we have only one particle, it would be useful to know the probability of obtaining a particular result. For us to know this, we must find the eigenfunctions of the operator in question.

### 1.1.2.5 Eigenfunctions

Let us recollect from the section on measurement (Section 1.1.2.3) that the state of a quantum particle changes upon measurement, and the final state is dependent on both the initial state and the kind of measurement being performed. However, there are certain very special states corresponding to every observable that do not change when it is measured. These special states are the eigenfunctions of that observable. If, for an operator $O$, the wavefunction $\psi_{o}$ behaves as

$$
\begin{equation*}
O \psi_{o}=\lambda \psi_{o} \tag{1.8}
\end{equation*}
$$

where $\lambda$ is a constant, then $\psi_{o}$ is an eigenfunction (also referred to as an eigenstate or an eigenvector) of the operator $O$ and $\lambda$ is the corresponding eigenvalue. Suppose that we measure the observable corresponding to the operator $O$ on a particle having the wavefunction $\psi_{o}$. We will find the value of this observable to be $\lambda$. This can be proven by substituting the value of $O \psi_{o}$ from Equation (1.8) into Equation (1.7) that gave us the expectation value corresponding to any operator. Moreover, the wavefunction $\psi_{o}$ will remain unchanged. Therefore, for a particle whose wavefunction is an eigenfunction of an observable, we can, with absolute certainty, state the result of measurement. To understand this, let us consider the energy operator (Table 1.1) as an example. Let us assume $\psi_{E_{i}}(i=0,1,2, \ldots)$ to be the eigenfunctions of the energy operator, having eigenvalues $E_{i}$ :

$$
\begin{equation*}
i \hbar \frac{\partial \psi_{E_{i}}}{\partial t}=E_{i} \psi_{E_{i}} \tag{1.9}
\end{equation*}
$$

If we take a particle with wavefunction $\psi_{E_{i}}$, we know that its energy is $E_{i}$. There is no probability involved in this.

Furthermore, even if we have a wavefunction that is not an eigenfunction of the operator in question, the result of every measurement can only be one of the eigenvalues of the operator. Let us again take the energy operator as an example. Suppose that we measure the energy of a particle having a wavefunction $\Psi$ that is not one of the eigenfunctions $\psi_{E_{i}}$ of the operator. The result will always be one of the eigenvalues $E_{i}$. Note that every measurement will result in a different energy being observed each time. You will now say that once you have measured the energy, and it is found to be a particular $E_{i}$, you know the energy of the particle to be $E_{i}$. However, it was stated earlier in this section that we can only know (with absolute certainty) the energy of the eigenfunctions of an observable. Yet $\Psi$ is not an eigenfunction of the energy operator. What happens is that, after measurement, the wavefunction $\Psi$ "collapses" into the wavefunction $\psi_{E_{i}}$ corresponding to the observed energy $E_{i}$. Remember that measurement changes the state of a quantum particle. Now we can say that measuring an observable leads us to observe one of the eigenvalues of that observable, and the state of the quantum particle being measured changes to the corresponding eigenfunction.

We now face the problem of finding the probability of this "collapse" into a particular eigenfunction. Linear algebra provides us with a very handy solution to this problem. Any general wavefunction can be written in terms of the eigenfunctions of an operator. Let us clarify this point. Every operator has a set of eigenfunctions. If we use a linear combination of all these eigenfunctions, we get a set of states that includes every possible state that the quantum particle can have. That is, every wavefunction can be decomposed into a linear superposition of the eigenfunctions of any given operator. Let us once again go back to the energy operator and its eigenfunctions that we discussed in Equation (1.9). Any general wavefunction $\Psi$ can be written in terms of the eigenfunctions $\psi_{E_{i}}$ of the energy operator as

$$
\begin{equation*}
\Psi=\Sigma a_{i} \psi_{E_{i}} \tag{1.10}
\end{equation*}
$$

where $a_{i}$ are coefficients corresponding to every wavefunction $\psi_{E_{i}}$ and are complex numbers.

Unlike in the case of the eigenfunctions $\psi_{E_{i}}$, we cannot discuss the energy of this general particle as having wavefunction $\Psi$. It is a linear superposition of states $\psi_{E_{i}}$ of different energies $E_{i}$. When the energy of this particle is measured, one obtains any one of the energies $E_{i}$, and the particle is found to be in the state $\psi_{E_{i}}$ after the measurement. However, we cannot say that the particle had energy $E_{i}$ because the measurement may very well have led to the observation of a different energy $E_{j}$. Now the state of the particle changes after measurement from
$\Psi$ to $\psi_{E_{i}}$. The probability $P\left(\psi_{E_{i}}\right)$ that the wavefunction $\Psi$ collapses into a particular eigenfunction $\psi_{E_{i}}$ can be written as

$$
\begin{equation*}
P\left(\psi_{E_{i}}\right)=\left|a_{i}\right|^{2} \tag{1.11}
\end{equation*}
$$

where $a_{i}$ is the coefficient corresponding the eigenfunction $\psi_{E_{i}}$ in the linear superposition shown in Equation (1.10).

### 1.1.3 Schrodinger's equation

### 1.1.3.1 Formulation of the equation

We have now understood what wavefunctions mean and how they behave when measured, yet we do not know how to find them for a particular physical situation, like an electron in a hydrogen atom. There must be some equations that have to be solved to give us these wavefunctions. Just as Maxwell's equations (when solved under an appropriate set of boundary conditions) give the equations for electromagnetic waves, an equation is needed that can be used to find the wavefunction of a quantum particle. This equation is called Schrodinger's equation. It is a quantum formulation of the statement that

$$
\begin{equation*}
\text { Total energy = kinetic energy }+ \text { potential energy } \tag{1.12}
\end{equation*}
$$

By using the operators listed in Section 1.1.2.4 (Table 1.1), we can write:

$$
\begin{gather*}
\text { Kinetic energy }=\frac{p^{2}}{2 m}=-\frac{\hbar^{2}}{2 m} \nabla^{2}  \tag{1.13a}\\
\text { Potential energy }=V(\vec{r}, t)  \tag{1.13b}\\
\text { Total energy }=i \hbar \frac{\partial}{\partial t} \tag{1.13c}
\end{gather*}
$$

which, when substituted into Equation (1.12), gives Schrodinger's equation:

$$
\begin{equation*}
-\frac{\hbar^{2}}{2 m} \nabla^{2}+V(\vec{r}, t)=i \hbar \frac{\partial}{\partial t} \tag{1.14}
\end{equation*}
$$

The above equation is in the form of operators of the individual energies. It needs to be operated upon the wavefunction $\psi$, giving the final form of the equation as

$$
\begin{equation*}
-\frac{\hbar^{2}}{2 m} \nabla^{2} \psi+V \psi=i \hbar \frac{\partial \psi}{\partial t} \tag{1.15}
\end{equation*}
$$

The above equation is a partial differential equation, which has to be solved for $\psi$. The form of the potential $V(\vec{r}, t)$ and the boundary conditions will be different for different physical problems (e.g. a particle confined in a one-dimensional well, the tunnelling problem, the hydrogen atom, etc.), thus leading to different wavefunctions.

In Equation (1.15), we have introduced the time-dependent form of Schrodinger's equation. However, in most cases, we would be solving for energy eigenfunctions, which are stationary states and do not change with time. Therefore, the right-hand side of Equation (1.15) changes to $E \psi$, giving

$$
\begin{equation*}
-\frac{\hbar^{2}}{2 m} \nabla^{2} \psi+V \psi=E \psi \tag{1.16}
\end{equation*}
$$

This is the time-independent form of Schrodinger's equation, and it will be the building block for solving most elementary and slightly complex quantum problems. Additionally, most of the problems that are dealt with in this text are one dimensional. In this case, the time-independent Schrodinger equation (1.16) further simplifies to

$$
\begin{equation*}
-\frac{\hbar^{2}}{2 m} \frac{\partial^{2} \psi}{\partial x^{2}}+V \psi=E \psi \tag{1.17}
\end{equation*}
$$

Using Schrodinger's equation, you can get some very important mathematical conditions that every wavefunction must obey [1]. These are:

1. The wavefunction must be continuous at each point in space.
2. The first derivative of the wavefunction must be continuous at each point in space, unless the potential $V$ at the boundary in question is infinite.

### 1.1.3.2 Probability current

Our final aim is to model tunnel field-effect transistors (TFETs) for predicting their electric currents. We must keep this perspective in mind when we look at the quantum mechanical techniques that we are discussing. We know that electric current is a measure of the rate of flow of charge. These charges are either electrons or holes and their behaviour is best predicted by quantum mechanics. Therefore, we need to find a link between the electric current and the quantum mechanical behaviour of charge carriers.

Let us consider a single electron present inside a conducting wire. If we were to study the behaviour of this electron purely in terms of classical physics, we could find its position and velocity, and use this information to calculate the current in


Figure 1.2 Probability density of a quantum particle moving from left to right, plotted at different points of time $\left(t_{2}>t_{1}>t_{0}\right)$.
the wire. However, for an electron obeying the laws of quantum mechanics, we cannot specify its position - we can only find the probability of its presence at any point. From this point of view, what happens when an electron moves in a particular direction? The probability density $l \psi^{2}$ lof a quantum particle moving from left to right is plotted at various points of time in Figure 1.2. In terms of quantum mechanics, we might say that the probability of finding this particle is changing. In technical terms, there is now a probability current, which is the rate of flow of the probability of the quantum particle's presence. To calculate the electric current resulting from the flow of quantum particles, we need to find this probability current.

We know that the differential form of the continuity equation for charge is

$$
\begin{equation*}
\vec{\nabla} \cdot \vec{J}=-\frac{\partial \rho}{\partial t} \tag{1.18}
\end{equation*}
$$

where $\vec{J}$ is the current density and $\rho$ is the charge density. In the case of a quantum particle, the same continuity equation holds, but with $J$ being the probability current and $\rho=\psi^{*} \psi$ being the probability density. We can use the left-hand side of the time-independent Schrodinger Equation (1.16) to give us the values of $\psi$ and $\psi *$. When these values of $\psi$ and $\psi *$ are substituted in Equation (1.18), we can write the probability current $\vec{J}$ as [1]

$$
\begin{equation*}
\vec{J}=\frac{i \hbar}{2 m}\left(\psi \vec{\nabla} \psi^{*}-\psi^{*} \vec{\nabla} \psi\right) \tag{1.19}
\end{equation*}
$$

### 1.2 Basic quantum physics problems

In this section, we will familiarise ourselves with certain basic problems of quantum mechanics. This would enable us to appreciate the concepts introduced that act as a bridge between the theoretical framework and the practical problem of tunnelling.

### 1.2.1 Free particle

The simplest quantum particle is a free particle - a particle that is completely unconstrained by any external potentials. This free particle is a basic building block for studying more complex quantum mechanical problems, just as we use the sine wave as a basic building block for representing complicated waves (by using Fourier series).

### 1.2.1.1 Wavefunction

As a free particle is unconstrained by any external potential, the term $V(\vec{r})$ in the time-independent Schrodinger equation (1.16) is zero at all points in space. Therefore, for a free particle, we can write

$$
\begin{equation*}
-\frac{\hbar^{2}}{2 m} \frac{\partial^{2} \psi}{\partial x^{2}}=E \psi \tag{1.20}
\end{equation*}
$$

The solutions of Equation (1.20) are

$$
\begin{align*}
& \psi_{ \pm}=A e^{ \pm k x}  \tag{1.21}\\
& k=\sqrt{\frac{2 m E}{\hbar^{2}}} \tag{1.22}
\end{align*}
$$

where $k$ is referred to as the wave vector of the free particle and $A$ is a complex constant. The general solution would be a linear superposition of $\psi_{+}$and $\psi_{-}$:

$$
\begin{equation*}
\Psi=A^{\prime} e^{\imath k x}+B^{\prime} e^{-l k x} \tag{1.23}
\end{equation*}
$$

where $A^{\prime}$ and $B^{\prime}$ are complex constants.
We find that there are two eigenvectors $\psi_{+}$and $\psi_{-}$corresponding to a particular energy $E$ - one with a plus sign and another with a minus sign in the exponential. Let us see what happens when we apply the momentum operator $p_{x}$ (Table 1.1) to these wavefunctions:

$$
\begin{equation*}
p_{x} \psi_{ \pm}=-i \hbar \frac{\partial \psi_{ \pm}}{\partial x}= \pm \hbar k \psi_{ \pm} \tag{1.24}
\end{equation*}
$$

We find that the wavefunctions $\psi_{ \pm}$are eigenfunctions of the momentum operator, with eigenvalues $\pm \hbar k$. This means that the particle corresponding to $\psi_{+}$has momentum in the positive $x$-direction, while $\psi_{-}$has momentum in the negative $x$-direction. Thus, $\psi_{+}$is a free particle moving forward with a momentum $\hbar k$ and $\psi_{-}$is a free particle moving backward with a momentum $\hbar k$.

Another important observation here is that the probability $\rho$ (Equation (1.4)) of finding the particle at any location is uniform - the particle is fully dispersed in space. Therefore, we have

$$
\begin{equation*}
\rho=\psi^{*} \psi=|A|^{2} \tag{1.25}
\end{equation*}
$$

The fact that the particle is fully dispersed in space follows from Heisenberg's uncertainty principle. The momentum of either of the two wavefunctions $\psi_{ \pm}$is known with complete certainty. Therefore, the uncertainty in the position of the particle is infinite.

### 1.2.1.2 Probability current

Using Equation (1.19), we can write the probability current for a free particle propagating along the positive $x$-axis as

$$
\begin{equation*}
J=\frac{\hbar k}{m}|A|^{2} \tag{1.26}
\end{equation*}
$$

Since $|A|^{2}$ is the probability density (Equation (1.25)) and $\hbar k$ is the momentum (Equation (1.24)) of the particle, the above equation can be rearranged to give

$$
\begin{equation*}
J=\frac{p}{m} \rho=v \rho \tag{1.27}
\end{equation*}
$$

where $v=p / m$ is the velocity of the particle. We can now write the electric current density $j$ as

$$
\begin{equation*}
j=q J=q v \rho \tag{1.28}
\end{equation*}
$$

In this section, we have discussed the behaviour of a completely unconstrained "free" particle. Let us now investigate the behaviour of a quantum particle that is bound to remain between two points - a particle in a box.

### 1.2.2 Particle in a one-dimensional box

The next problem we will deal with is a particle confined to move along an axis between two points. It cannot go beyond those two specified points. It is, therefore, referred to as a particle in a one-dimensional box. It is confined to remain within the


Figure 1.3 Particle in a one-dimensional box.
"box" (also referred to as an infinite potential well) with edges at $x=0$ and $x=L$, as shown in Figure 1.3. The particle is completely free to move within these limits, implying that there is no external potential (i.e. $V=0$ ) in the region $x \epsilon(0, L)$. Outside these limits, there is an infinite potential barrier (i.e. $V=\infty$ ). Physically, this form of external potential ensures that the particle stays within the "box". Since Schrodinger's equation assumes different forms in different regions, its solution - the wavefunction - will also have different forms in different regions. We will, therefore, find the wavefunction separately for each region and then join these separate solutions to give the final solution. It is important to be familiar with this procedure since it will be used in this book, not just for solving Schrodinger's equation but as a very important tool in the modelling of TFETs.

Now we know that the particle cannot be present in the regions $x \leq 0$ and $x \geq L$. Thus, in these regions, the probability of finding the electron is zero:

$$
\begin{equation*}
\rho=\psi^{*} \psi=0 \tag{1.29}
\end{equation*}
$$

This is only possible if $\psi=0$ in these regions.
Let us now find the solution of Schrodinger's equation within the box, where the form of Schrodinger's equation is the same as that of the free particle (Equation (1.20)). However, we need to keep in mind the restrictions applied to a wavefunction, stated at the end of Section 1.1.3.1. The first condition is that the wavefunction must be continuous. Since the wavefunction is zero in the regions $x \leq 0$ and $x \geq L$, we get the following boundary conditions at the edges of our box:

$$
\begin{align*}
& \psi(0)=0  \tag{1.30a}\\
& \psi(L)=0 \tag{1.30b}
\end{align*}
$$

The general solution of Equation (1.20) shown in Equation (1.23) can be written in the form of trigonometric functions as

$$
\begin{equation*}
\psi(x)=A \sin k x+B \cos k x \tag{1.31}
\end{equation*}
$$

where $A$ and $B$ are complex constants and the wave vector $k$ is

$$
\begin{equation*}
k=\sqrt{\frac{2 m E}{\hbar^{2}}} \tag{1.32}
\end{equation*}
$$

Applying the boundary conditions (1.30), we get

$$
\begin{gather*}
B=0  \tag{1.33}\\
k_{n} L=n \pi \tag{1.34}
\end{gather*}
$$

The subscript $n$ has been added to $k$ because Equation (1.34) shows that $k$ is quantised and takes only certain fixed values. Using Equation (1.34) in (1.32) gives the possible values of energy $E_{n}$ that the particle can have:

$$
\begin{equation*}
E_{n}=\frac{n^{2} h^{2}}{8 m L^{2}} \tag{1.35}
\end{equation*}
$$

and the wavefunction $\psi_{n}$ corresponding the energy $E_{n}$ is

$$
\begin{equation*}
\psi_{n}=A \sin \frac{n \pi x}{L} \tag{1.36}
\end{equation*}
$$

The constant $A$ is given by the normalisation condition (1.6):

$$
\begin{equation*}
A=\frac{1}{\sqrt{2 L}} \tag{1.37}
\end{equation*}
$$

The above wavefunctions (1.36) correspond to energy eigenstates, as we have solved the time-independent Schrodinger equation. A general wavefunction for a particle in a one-dimensional box would, therefore, be a superposition of these energy eigenstates:

$$
\begin{equation*}
\Psi=\Sigma a_{n} \psi_{n} \tag{1.38}
\end{equation*}
$$

where $a_{n}$ is the coefficient corresponding to the wavefunction $\psi_{n}$ and is a complex number.

If we plot the wavefunctions of energy eigenstates for a particle in a onedimensional box (Figure 1.4), we observe the same pattern as standing waves in a string.


Figure 1.4 Energy eigenfunctions of a particle in a one-dimensional box.

Going back to classical physics, remember that a standing wave can be written as a superposition of two travelling waves. Therefore, we can represent the energy eigenstate of a particle in a one-dimensional box as a superposition of two free particle wavefunctions going in opposite directions:

$$
\begin{equation*}
\psi_{n}=\frac{1}{\sqrt{2 L}} \sin k_{n} x=\frac{l}{\sqrt{2 L}}\left(e^{-\imath k_{n} x}-e^{\imath k_{n} x}\right) \tag{1.39}
\end{equation*}
$$

The first and second exponential terms in Equation (1.39) represent a free particle travelling in the negative and positive $x$-directions with momentum $\hbar k_{n}$, respectively. Therefore, if we take the expectation value (1.7) of their superposition, these momenta cancel each other and the expectation value of the momentum for $\psi_{n}$ is zero.

## Reference

[1] D. J. Griffiths, Introduction to Quantum Mechanics, 2nd edn, Pearson Education, 2005.

## 2

## Basics of tunnelling

### 2.1 Understanding tunnelling

In the previous chapter, we developed the necessary theoretical framework to deal with the problem of quantum mechanical tunnelling. Tunnelling plays an important role in many physical phenomena and devices - nuclear fusion, alpha decay, the scanning tunnelling microscope, the tunnel diode, tunnelling FETs, etc. The focus of this book is the modelling of tunnelling field-effect transistors. As is evident from the name, tunnelling is at the very basis of what makes TFETs work.

Let us first develop a qualitative idea of tunnelling, after which we will examine its quantitative details.

### 2.1.1 Qualitative description

Tunnelling is a quantum phenomenon, in which a particle is able to cross a potential barrier even though it does not have the energy to overcome this barrier. Such behaviour is not observed in the case of classical particles. Therefore, any classical analogy used to explain the phenomenon of quantum mechanical tunnelling would necessarily be inaccurate. Instead of taking such a classical analogy, it would be more fruitful to picture the quantum particle as a wave and form a link between this quantum mechanical behaviour and the behaviour displayed by waves.

Let us consider the problem of an electromagnetic wave incident on a conductor. The equations that describe this phenomenon have the same form as

[^1](a)

(b)


Figure 2.1 (a) Emerging electromagnetic wave has negligible amplitude. (b) Emerging electromagnetic wave's amplitude is attenuated, but not to negligible levels.
the equations that describe quantum mechanical tunnelling. When an electromagnetic wave is incident on a conductor, the free electrons of the conductor move. This causes an energy transfer - the wave loses energy and the electrons gain energy. Consequently, the amplitude of the wave decays exponentially within the material and is soon negligible, as shown in Figure 2.1(a). However, if the conductor is very thin, the amplitude of the wave does not decay to negligible levels before reaching the other edge of the conductor. In this case, we find electromagnetic waves of a very small amplitude on the other side of the conductor, as shown in Figure 2.1(b). In the previous chapter, we represented quantum particles by wavefunctions whose amplitudes correspond to the probability of the particle's presence. If we substitute the electromagnetic waves by such quantum particles and the conductor by a potential barrier, we would get a strikingly similar situation. A quantum mechanical particle is incident upon a potential barrier that it does not have enough energy to cross, yet there is a non-zero probability of the particle being found on the other side of this barrier. If we observe the particle


Figure 2.2 Tunnelling through a rectangular barrier.
on the other side, we say that it has "tunnelled" through the barrier, and this phenomenon is known as quantum mechanical tunnelling.

With this qualitative idea about tunnelling, let us find the probability of an electron tunnelling through a potential barrier. In the case of an electron, the potential barrier is the external potential $V(x)$ term of Schrodinger's equation. This function can take various forms. Let us first deal with the relatively simple problem of tunnelling through a rectangular potential barrier, after which we shall develop a method to obtain the probability of tunnelling through any potential barrier.

### 2.1.2 Rectangular barrier

The simplest tunnelling problem is that of a rectangular potential barrier, for which the external potential term $V(x)$ in Schrodinger's equation is

$$
V(x)=\left\{\begin{array}{l}
0, x \leq 0  \tag{2.1}\\
V, x \in(0, L) \\
0, x \geq L
\end{array}\right.
$$

As we did in the case of a particle in a one-dimensional box (Section 1.2.2), we divide the space into three regions as shown in Figure 2.2. Region $\mathrm{I}(x \leq 0)$ is where the incoming particle impinges upon the barrier. Region II $(x \in(0, L))$ is the barrier itself. Region III $(x \geq L)$ is where the particle emerges after tunnelling through the barrier.

Within Region I, the potential $V=0$; hence Schrodinger's equation reduces to that of a free particle (1.20). The incoming particle moving towards the right in

Region I, therefore, has a wavefunction $\psi_{I, i}=A e^{i k x}$. Notice that we chose the $\psi_{+}$solution of a free particle (1.21) to represent the incoming particle since it is moving along the positive $x$-axis and therefore has a momentum in the positive $x$-direction.

At this point, we need to start looking at the particle as if it were a wave. When a wave hits the edge of a barrier, a part of it is reflected and a part of it is transmitted. Therefore, this incoming wave-particle is partially reflected, resulting in another free particle moving towards the left in Region I having a wavefunction $\psi_{I, r}=B e^{-l k x}$. Once again, notice that we chose the $\psi_{-}$solution from Equation (1.21) because the reflected particle is now moving to the left and thus has momentum in the negative $x$-direction.

Superposing the wavefunctions of the incoming and the reflected particle, the resultant wavefunction in Region I is

$$
\begin{equation*}
\psi_{I}=\psi_{I, i}+\psi_{I, r}=A e^{i k x}+B e^{-\imath k x} \tag{2.2}
\end{equation*}
$$

This wavefunction $\psi_{I}$ is, of course, the general solution (Equation (1.23)) to Schrodinger's equation for a free particle. We chose to arrive at the same solution in a circuitous way so that we could assign a physical meaning to the eigenfunctions $\psi_{+}$and $\psi_{-}$, which represent the incoming and the reflected particles, respectively.

Let us now write Schrodinger's equation in Region II:

$$
\begin{equation*}
\frac{\hbar^{2}}{2 m} \frac{\partial^{2} \psi_{I I}}{\partial x^{2}}=(V-E) \psi_{I I} \tag{2.3}
\end{equation*}
$$

where $\psi_{I I}$ is the wavefunction of the particle in Region II. If the energy $E$ of the particle is more than the barrier height $V$ (i.e. $V<E$ ) we know that the particle can cross over the barrier. However, suppose that the energy $E$ of the particle is less than what is needed to cross over the barrier height $V(V>E)$, and the particle still moves from Region I to Region III. This phenomenon is called quantum mechanical tunnelling and, in this case, the solution for Equation (2.3) is

$$
\begin{gather*}
\psi_{I I}=C e^{ \pm k^{\prime} x}  \tag{2.4}\\
k^{\prime}=\sqrt{\frac{2 m(V-E)}{\hbar^{2}}} \tag{2.5}
\end{gather*}
$$

The general solution in Region II, therefore, is

$$
\begin{equation*}
\psi_{I I}=C e^{k^{\prime} x}+D e^{-k^{\prime} x} \tag{2.6}
\end{equation*}
$$

where $C$ and $D$ are complex constants.

In Region III, the potential $V=0$ and the transmitted particle is moving towards the right and has a positive momentum. Therefore, we can use the free particle solution (1.23) of Schrodinger's equation, without the negative momentum $e^{-l k x}$ term, to write the wavefunction $\psi_{I I I}$ in Region III:

$$
\begin{equation*}
\psi_{I I I}=F e^{\imath k x} \tag{2.7}
\end{equation*}
$$

where $F$ is a complex constant.
Since the wavefunctions in Regions I, II and III are known, the combined wavefunction $\Psi$ can be written as

$$
\Psi=\left\{\begin{array}{lr}
\psi_{I, i}+\psi_{I, r}, & x \leq 0  \tag{2.8}\\
\psi_{I I}, & x \in(0, L) \\
\psi_{I I I}, & x \geq L
\end{array}\right.
$$

We only need to solve for the constants $A, B, C, D$, and $F$ to get the final wavefunction $\Psi$. Once again, we use the physical conditions of continuity and differentiability of the wavefunction at all points to obtain these constants. Within each region, the wavefunctions are continuous and differentiable. We need to impose these boundary conditions only at the points where the wavefunction $\Psi$ changes its form, that is at $x=0$ and $x=L$.

Imposing continuity and differentiability, respectively, of the wavefunction $\Psi$ at $x=0$, we get

$$
\begin{gather*}
A+B=C+D  \tag{2.9}\\
l k(A-B)=k^{\prime}(C-D) \tag{2.10}
\end{gather*}
$$

Similarly, at $x=L$, we get

$$
\begin{gather*}
C e^{k^{\prime} L}+D e^{k^{\prime} L}=F e^{i k L}  \tag{2.11}\\
k^{\prime}\left(C e^{k^{\prime} L}-D e^{-k^{\prime} L}\right)=\imath k F e^{i k L} \tag{2.12}
\end{gather*}
$$

Notice that we have five variables but only four linear equations. To find the tunnelling probability, we do not need to find the value of all the five variables but only the ratio of two variables. The probability density of the transmitted particle (in Region III) is $\rho_{I I I}=|F|^{2}$ and the probability density of the incoming particle (in Region I) is $\rho_{I, i}=|A|^{2}$. The tunnelling (or transmission) probability is, therefore, given by

$$
\begin{equation*}
T=\frac{|F|^{2}}{|A|^{2}} \tag{2.13}
\end{equation*}
$$

Using Equations (2.9) to (2.12) to solve for the tunnelling probability given in (2.13), we get

$$
\begin{equation*}
T=\frac{1}{1+\frac{V^{2} \sinh ^{2} k^{\prime} L}{4 E(V-E)}} \tag{2.14}
\end{equation*}
$$

This formula (2.14) is useful because some potential barriers may be approximated as rectangular barriers. However, many practical tunnelling problems cannot be approximated as tunnelling through a rectangular potential barrier. Therefore, for practical tunnelling problems, we need to develop a method that can give us the tunnelling probability for any type of potential barrier. Such a method is described in the next section.

### 2.2 WKB approximation

For a rectangular potential barrier (Section 2.1.2), the external potential $V(x)$ in Schrodinger's equation was constant, thereby simplifying the process of obtaining the solution. However, finding the tunnelling probability through a general potential barrier involves solving Schrodinger's equation where the external potential $V$ $(x)$ can have any form. This is mathematically challenging and therefore we need to apply approximations to simplify the problem. One such approximation is the WKB approximation, which is widely used to find the tunnelling probability [1]. It was developed by Wentzel, Kramers and Brillouin, and is a mathematical approach to find approximate solutions to certain types of differential equations. We will now discuss this approach by applying it to the problem of quantum mechanical tunnelling.

Let us begin by writing Schrodinger's equation for a general potential barrier $V(x)$ :

$$
\begin{equation*}
-\frac{\hbar^{2}}{2 m} \frac{\partial^{2} \psi}{\partial x^{2}}+V(x) \psi=E \psi \tag{2.15}
\end{equation*}
$$

Equation (2.15) is a partial differential equation and we will use the WKB approximation to solve it. There is little physics involved in the following derivation, but this method gives a very good idea about the kinds of approximations we use to solve complicated equations. Remember that in the case of tunnelling, the barrier height should be greater than the particle energy, that is $V(x) \geq E$.

Let us rewrite Equation (2.15) as follows:

$$
\begin{equation*}
\frac{\partial^{2} \psi}{\partial x^{2}}=k^{\prime}(x)^{2} \psi \tag{2.16}
\end{equation*}
$$

where

$$
\begin{equation*}
k^{\prime}(x)=\sqrt{\frac{2 m(V(x)-E)}{\hbar^{2}}} \tag{2.17}
\end{equation*}
$$

Since $\psi(x)$ is a complex function, it may be written in terms of its amplitude $A(x)$ and phase $\phi(x)$, which are both real functions of $x$ :

$$
\begin{equation*}
\psi(x)=A(x) e^{\imath \phi(x)} \tag{2.18}
\end{equation*}
$$

Substituting (2.18) into (2.16), we get

$$
\begin{equation*}
\frac{\partial^{2} A}{\partial x^{2}}+2 l \frac{\partial A}{\partial x} \frac{\partial \phi}{\partial x}+l A \frac{\partial^{2} \phi}{\partial x^{2}}-A\left(\frac{\partial \phi}{\partial x}\right)^{2}=k^{\prime 2} A \tag{2.19}
\end{equation*}
$$

Equating the real and imaginary parts of Equation (2.19) gives us

$$
\begin{align*}
& \frac{\partial^{2} A}{\partial x^{2}}-A\left(\frac{\partial \phi}{\partial x}\right)^{2}=k^{\prime 2} A  \tag{2.20}\\
& 2 l \frac{\partial A}{\partial x} \frac{\partial \phi}{\partial x}+\imath A \frac{\partial^{2} \phi}{\partial x^{2}}=0 \tag{2.21}
\end{align*}
$$

Note that Equation (2.21) is equivalent to

$$
\begin{equation*}
\frac{\partial}{\partial x}\left(A^{2} \frac{\partial \phi}{\partial x}\right)=0 \tag{2.22}
\end{equation*}
$$

Therefore, the solution of Equation (2.22) gives us a relation between the amplitude $A(x)$ and phase $\phi(x)$ of the wavefunction $\psi(x)$ :

$$
\begin{equation*}
A=\frac{C}{\sqrt{|\partial \phi / \partial x|}} \tag{2.23}
\end{equation*}
$$

where $C$ is a real constant.
Till this point, the solution is exact and no approximations have been made. However, solving (2.20) without an approximation would be a challenging task. Therefore, we assume that the amplitude of the wavefunction varies slowly with $x$. Due to this assumption, $\partial^{2} A / \partial x^{2} \sim 0$, reducing Equation (2.20) to

$$
\begin{equation*}
\left(\frac{\partial \phi}{\partial x}\right)^{2}=-k^{\prime 2} \tag{2.24}
\end{equation*}
$$

The phase $\phi(x)$ can now be written as

$$
\begin{equation*}
\phi(x)= \pm l \int\left|k^{\prime}(x)\right| d x \tag{2.25}
\end{equation*}
$$

Using Equations (2.18) and (2.23) to (2.25), the wavefunction can be given as

$$
\begin{equation*}
\psi(x)=\frac{C}{\sqrt{k^{\prime}(x)}} e^{ \pm \int\left|k^{\prime}(x)\right| d x} \tag{2.26}
\end{equation*}
$$

A general solution for the wavefunction can be represented by a linear superposition of the positive and negative exponential terms shown in Equation (2.26).

At this point, the mathematical part of the solution is complete and we will now consider the physics involved in finding the tunnelling probability. The wavefunction $\psi(x)$ in Equation (2.26) is the solution of Schrodinger's equation within the potential barrier region. There will still be incoming, reflected and transmitted waves of the same form as seen in the section on the rectangular barrier (Section 2.1.2). Using the notations defined in that section, the wavefunction in the barrier region $\psi_{I I}$ can be written as

$$
\begin{equation*}
\psi_{I I}=\frac{C}{\sqrt{k^{\prime}(x)}} e^{\int_{0}^{x}\left|k^{\prime}(x)\right| d x}+\frac{D}{\sqrt{k^{\prime}(x)}} e^{-\int_{0}^{x}\left|k^{\prime}(x)\right| d x} \tag{2.27}
\end{equation*}
$$

As we go further into the barrier, the probability of the particle being found should decrease. Since the wavefunction is the probability amplitude (Section 1.1.2.2), this means that $\psi_{I I}$ should decrease as we go further into the barrier region. However, the first term in Equation (2.27) is an exponential that increases with $x$. Therefore, to make sure our wavefunction displays this behaviour, the coefficient $C$ of the first term in the above equation should be very small. This is especially true if either the barrier width or the barrier height is very large, which is usually true in most practical situations. By neglecting the first term and imposing the boundary conditions as described in Equations (2.9) to (2.12), we get the tunnelling probability $T$ as

$$
\begin{equation*}
T=\frac{|F|^{2}}{|A|^{2}}=e^{-2 \gamma} \tag{2.28}
\end{equation*}
$$

where

$$
\begin{equation*}
\frac{|F|}{|A|} \sim e^{-\int_{0}^{L}\left|k^{\prime}(x)\right| d x} \tag{2.29}
\end{equation*}
$$

and

$$
\begin{equation*}
\gamma=\int_{0}^{L}\left|\sqrt{\frac{2 m(V(x)-E)}{\hbar^{2}}}\right| d x \tag{2.30}
\end{equation*}
$$

We can now find the tunnelling probability through a general potential barrier independent of any material or band structure properties. In the following section, let us now see how the current due to tunnelling can be estimated considering the properties of the material.

### 2.3 Landauer's tunnelling formula

In the previous sections, we did not consider the tunnelling electrons as part of any material or band structure. Therefore, we need to connect the concepts of quantum mechanics with the band structure of the material, so that we can obtain a model for the current in a system. This is done by using the Landauer's tunnelling formula. Let us look at a simple derivation of Landauer's tunnelling formula required for developing TFET models.

All bulk crystalline materials have continuous energy bands - a continuum of available energy levels that may be occupied by an electron. The Fermi-Dirac statistics gives us this probability $f(E)$ of occupancy of a level at energy $E$ as

$$
\begin{equation*}
f(E)=\frac{1}{1+e^{\left(E-E_{F}\right) / k T}} \tag{2.31}
\end{equation*}
$$

where $k$ is Boltzmann's constant, $E_{F}$ is the Fermi energy of the material and $T$ is the temperature. We can observe from Equation (2.31) that a higher energy state would have a lower probability of being occupied, as electrons prefer to stay in a lower energy state. Of course, due to thermal excitations, some electrons would be found in higher energy states as well, and the number of such electrons would increase as we increase the temperature. Let us initially assume the temperature to be $0 K$, so that all states below the Fermi level $E_{F}$ are occupied, while all states above it are empty. Let us take a look at Figure 2.3(a), which shows two materials divided by a potential barrier. If this potential barrier were not present, electrons in one region would be free to move to the other. However, since the potential barrier is present, electrons need to tunnel through it to get to the other side.

Note that electrons would be tunnelling from both the sides, Region A to Region B and vice versa. We will first find the current $I_{A \rightarrow B}$ corresponding to electrons flowing from Region A to Region B. A similar formula will give us the current $I_{B \rightarrow A}$ corresponding to electrons flowing in the opposite direction. The net current $I_{\text {net }}$ would be the difference between these two currents.


Figure 2.3 (a) Regions divided by a potential barrier with no external bias. No empty states exist in Region B for electrons to tunnel. (b) Regions divided by a potential barrier with external bias. Empty states exist in Region B, to which electrons can now tunnel.

Since electrons are fermions [2], no two electrons can have the same quantum state and an energy level can only hold two electrons - one of spin-up and one of spin-down. Therefore, movement of electrons from Region A to B (and vice versa) can take place only if there are empty states available in the other region. Initially, when no bias is applied (Figure 2.3(a)), there are no empty states on either side of the barrier. Therefore, electrons cannot tunnel through the barrier. However, when a small bias $\Delta V$ is applied, the Fermi energy of Region B is lowered by $\Delta V$, as shown in Figure 2.3(b). The electrons having energies between $E-\Delta V$ and $E$ can now move from Region A into the corresponding unoccupied energy levels in Region B, and thus contribute to the tunnelling current.

Let us consider Region A to be a one-dimensional potential well of length $L$ (Section 1.2.2). Writing the wavefunction in this "potential well" in the form of two travelling waves (as shown in Equation (1.39)), we get

$$
\begin{equation*}
\psi_{A}=\frac{l}{2 L}\left(e^{-\iota k_{n} x}-e^{\imath k_{n} x}\right) \tag{2.32}
\end{equation*}
$$

where the incoming wave corresponds to $e^{i k_{n} x}$. Therefore, the probability density corresponding to the incoming wave is

$$
\begin{equation*}
\rho=1 / 2 L \tag{2.33}
\end{equation*}
$$

For a one-dimensional potential well, the difference in the wave vector $k_{n}$ between two successive states is

$$
\begin{equation*}
\Delta k_{n}=\frac{\pi}{L} \tag{2.34}
\end{equation*}
$$

The total current $I_{\text {in }}$ going into the tunnelling barrier is the summation of the individual contributions from each energy level between $E$ and $E-\Delta V$. These individual contributions can be written by using the equation for current due to a free particle (Equation (1.28)). Their summation would, therefore, give $I_{i n}$ as

$$
\begin{equation*}
I_{i n}=2 \sum_{E-\Delta V}^{E} q v \rho \tag{2.35}
\end{equation*}
$$

where the factor of 2 comes from the double degeneracy of each energy level due to electron spin.

The current that comes out of the tunnelling barrier into Region B would be the incoming current $I_{i n}$ multiplied by the tunnelling probability $T$. The tunnelling probability is dependent on the energy of the incoming electron. For the $n$th state, having a wave vector $k_{n}$, the tunnelling probability is $T\left(k_{n}\right)$. Therefore, the current transmitted from Region A to Region B is:

$$
\begin{equation*}
I_{A \rightarrow B}=2 \sum_{E-\Delta V}^{E} T\left(k_{n}\right) q v \rho \tag{2.36}
\end{equation*}
$$

Multiplying and dividing by $\pi / L$ and using Equation (2.34) gives

$$
\begin{equation*}
I_{A \rightarrow B}=2 q \rho \frac{L}{\pi} \sum_{E-\Delta V}^{E} T v \frac{\pi}{L}=2 q \rho \frac{L}{\pi} \sum_{E-\Delta V}^{E} T v \Delta k_{n} \tag{2.37}
\end{equation*}
$$

Substituting the value of probability density $\rho$ from Equation (2.33),

$$
\begin{equation*}
I_{A \rightarrow B}=\frac{q}{\pi} \sum_{E-\Delta V}^{E} T v \Delta k_{n} \tag{2.38}
\end{equation*}
$$

However, as $L \rightarrow \infty, \Delta k_{n} \rightarrow 0$. Therefore:

$$
\begin{equation*}
I_{A \rightarrow B}=\frac{q}{\pi} \int_{E-\Delta V}^{E} T v d k \tag{2.39}
\end{equation*}
$$

So far we have considered the temperature to be 0 K . Let us now assume the temperature to be greater than 0 K . Due to thermal excitations, electrons can now
occupy energy levels above the Fermi level $E_{F}$. The probability that an energy level is occupied by an electron is given by the Fermi-Dirac statistics (Equation (2.31)). If the probability of occupancy of each energy level in Region A is given as $f_{A}$, then the current from Region A to Region B becomes

$$
\begin{equation*}
I_{A \rightarrow B}=\frac{q}{\pi} \int_{E-\Delta V}^{E} T v f_{A} d k \tag{2.40}
\end{equation*}
$$

We will now use the relation for group velocity $v$ to change the variable of integration from wave vector $k$ to energy $E$ :

$$
\begin{equation*}
v=\frac{1}{\hbar} \frac{\partial E}{\partial k} \tag{2.41}
\end{equation*}
$$

This equation is similar to the group velocity in the case of waves. When we substitute Equation (2.41) into (2.40), we get the expression for the electron current going from Region A to Region B:

$$
\begin{equation*}
I_{A \rightarrow B}=\frac{2 q}{\hbar} \int_{E-\Delta V}^{E} f_{A}(E) T(E) d E \tag{2.42}
\end{equation*}
$$

Similarly, the current going from Region B to Region A is

$$
\begin{equation*}
I_{B \rightarrow A}=\frac{2 q}{\hbar} \int_{E-\Delta V}^{E} f_{B}(E) T(E) d E \tag{2.43}
\end{equation*}
$$

Equations (2.43) and (2.44) can be combined to give the final expression for Landauer's formula:

$$
\begin{equation*}
I_{n e t}=I_{A \rightarrow B}-I_{B \rightarrow A}=\frac{2 q}{\hbar} \int_{E-\Delta V}^{E}\left(f_{A}(E)-f_{B}(E)\right) T(E) d E \tag{2.44}
\end{equation*}
$$

The above formula is extensively used in TFET models to calculate the current.

### 2.4 Advanced tunnelling models

In semiconductors, two different kinds of models are used to calculate the current resulting from tunnelling - local and non-local models. Non-local models treat tunnelling as a process that occurs in spatial coordinates, where electrons tunnel from one point in space to another, as shown in Figure 2.3(b). Therefore, the WKB approximation and Landauer's tunnelling formula that we have considered earlier can be classified as non-local models. Local models, on the other hand, treat
tunnelling as a phenomenon taking place from one energy band to another in the $E-k$ space of the material.

### 2.4.1 Non-local tunnelling models

Since the non-local models approach tunnelling from a spatial perspective, we need to know the spatial dependence of all the parameters in Schrodinger's equation to obtain the tunnelling probability. If an external bias is applied to a semiconductor, the shape of the potential barrier $V(x)$ is complicated. The band structure of the semiconductor also needs to be incorporated into the potential $V(x)$ in Schrodinger's equation. Moreover, the potential in the semiconductor depends on the current (Ohm's law), while the current in turn depends on the shape of the potential barrier (Schrodinger's equation). All these complicated spatial dependencies make it difficult to solve Schrodinger's equation analytically. Therefore, numerical approaches need to be applied to obtain a solution for calculating the tunnelling probability. However, because all these complex dependencies are incorporated, non-local models provide an accurate estimation of the current in the device although they are difficult to solve analytically. As non-local models cannot be used to obtain analytical models, they are used in device simulation. On the other hand, analytical models are essential for use in circuit simulation and to understand the functioning of devices. To develop analytical models, we use local tunnelling models.

### 2.4.2 Local tunnelling models

To understand local models, let us consider the problem from an energy band perspective. Semiconductors have various energy bands, which result from the interaction of the individual fields created by the atoms in its lattice. These energy bands are usually represented in an $E-k$ diagram, which shows the allowed values of energy $E$ that the electron can take. The $E-k$ diagram of an intrinsic direct bandgap semiconductor is shown in Figure 2.4(a). We can observe the presence of a valence band and a conduction band, separated by the bandgap - a potential barrier. Since we have considered an intrinsic material, there are very few electrons in the conduction band. In the presence of an external electric field, these few electrons will flow to generate a very low current. However, if this electric field is sufficiently large, it is possible for electrons to tunnel from the valence band to the conduction band without a change in energy, as shown in Figure 2.4(b). In other words, the electric field causes the bands in the semiconductor to be modified in such a way that the electrons in the valence band can tunnel through the potential barrier (the bandgap) and reach the conduction band. Now that there are sufficient electrons in the conduction band due to tunnelling, an appreciable current can flow in the material.


Figure 2.4 (a) Energy bands of a semiconductor under zero bias, with inset $E-k$ diagrams. (b) Energy bands of a semiconductor with an electric field, with inset $E-k$ diagrams. Non-local occurs, with the electron transitioning from the valence band maxima to the conduction band minima in the E-k diagram.

An important approximation made while deriving local models is that the electric field is assumed to be constant, which is rarely true in a real device. Because of the assumption of a constant electric field, local models predict a single tunnelling rate throughout the device. In case the electric field is position-dependent, we estimate the tunnelling rate using the local electric field at every point in the device. The tunnelling rate at each point is then integrated throughout the device to give the total number of electrons that have tunnelled from the valence band to the conduction band. Since no generation or recombination is assumed, the total current is dependent only on the rate at which the electrons tunnel from the valence band to the conduction band. Therefore, in local models, it is assumed that each electron that reaches the conduction band is swept away to form a part of the total current. Local models are extensively used in analytical modelling of TFETs as they give an analytical expression for the tunnelling rate at each point.

We will now discuss two commonly used local models for calculating the tunnelling rate in semiconductors.

### 2.4.2.1 Kane's model

Developed by E.O. Kane in 1959 [3], this is one of the oldest and most widely used models for calculating the band-to-band tunnelling rate in TFET models. The derivation of Kane's model is very complicated. However, a brief summary of the procedure followed in the paper is presented below.

As mentioned in the preceding section, we will be approaching tunnelling as a phenomenon occurring in the $E-k$ diagram rather than in the spatial coordinates. We would, therefore, need to reframe Schrodinger's equation accordingly. In this new representation of the Schrodinger equation, there must be no spatial terms. The terms that depend on space in Schrodinger's equation are: (i) the derivatives that are with respect to spatial coordinates and (ii) the spatial dependence of the external potential $V(x)$. These spatial terms must now be reframed as functions of either energy $E$ or momentum $k$. This reformulation of Schrodinger's equation is done by using Bloch functions. Therefore, let us go through a brief introduction to Bloch functions.

Semiconductors are crystalline solids, having a periodic structure. Due to such a structure, the atoms of the crystal produce a periodic potential $V_{p e r}$ with the same periodicity as the crystal. When this potential $V_{p e r}$ is substituted into the time-independent Schrodinger equation, we get

$$
\begin{equation*}
-\frac{\hbar^{2}}{2 m} \frac{\partial^{2} \psi}{\partial x^{2}}+V_{p e r} \psi=E_{n} \psi \tag{2.45}
\end{equation*}
$$

Note that we have added a subscript $n$ to the energy, since different energy bands in the crystal correspond to different energies. In solid state physics, the solutions to the above Equation (2.45) are known as Bloch functions $\psi_{n, k}$, which have the form:

$$
\begin{equation*}
\psi_{n, k}=e^{i k x} u_{n, k}(x) \tag{2.46}
\end{equation*}
$$

where $u_{n, k}(x)$ is a periodic function with the same periodicity as the crystal. The subscript $n$ indicates that this Bloch function belongs to the $n$th energy band (i.e. its energy is $E_{n}$ ) and $k$ denotes the crystal momentum of this wavefunction. A general wavefunction $\Psi$ can, therefore, be written as

$$
\begin{equation*}
\Psi=\Sigma a_{n}(k) \psi_{n, k} \tag{2.47}
\end{equation*}
$$

where $a_{n}(k)$ are complex coefficients, as described in Equation (1.10). With this brief idea of Bloch functions, let us proceed with the derivation of Kane's model.

First we will consider the familiar Schrodinger equation in spatial coordinates for our particular physical situation, which is a crystal lattice (the semiconductor) to which an external bias is applied. The potential term $V(x)$ in Schrodinger's
equation will, therefore, be a sum of the contribution due to the crystal lattice $\left(V_{p e r}\right)$ and the contribution due to the external bias $\left(V_{e x t}\right)$. It is assumed that the external bias $V_{\text {ext }}$ leads to a uniform electric field $E_{\text {ext }}$ in the crystal lattice. Since the potential due to a uniform electric field is linear $\left(V_{e x t}=-q E_{e x t} x\right)$, we can write the time-independent Schrodinger equation (1.17) as

$$
\begin{equation*}
-\frac{\hbar^{2}}{2 m} \frac{\partial^{2} \psi}{\partial x^{2}}+\left(V_{p e r}-q E_{e x t} x\right) \psi=E \psi \tag{2.48}
\end{equation*}
$$

We will now use the Bloch functions to convert Schrodinger's equation from this spatial form to what is known as the "crystal momentum representation" [4]. The crystal momentum representation is simply another form of Schrodinger's equation without any spatial dependence. In this representation, every operator and wavefunction in Schrodinger's equation is written as a linear combination of the Bloch functions. For decomposing the wavefunction $\psi$ into a linear combination of Bloch functions, we only need to substitute the equation for a general wavefunction (2.47) into Schrodinger's equation, which will give

$$
\begin{equation*}
\Sigma a_{n}(k)\left(\frac{\hbar^{2}}{2 m} \frac{\partial^{2} \psi_{n, k}}{\partial x^{2}}+V_{p e r} \psi_{n, k}\right)-\Sigma a_{n}(k) q E_{e x t} x \psi_{n, k}=\Sigma a_{n}(k) E \psi_{n, k} \tag{2.49}
\end{equation*}
$$

By comparing with Equation (2.45), we can observe that the term in the brackets is the energy of the $n$th band $E_{n}$. Making this substitution, and writing the equation for a single band, i.e. a single $a_{n}(k)$, we get

$$
\begin{equation*}
a_{n}(k) E_{n}-a_{n}(k) q E_{\text {ext }} x=a_{n}(k) E \tag{2.50}
\end{equation*}
$$

Note that there are two terms corresponding to energy in the above equation $E_{n}$ corresponding to the energy solely due to the crystal and $E$ corresponding to the total energy of the electron. We have now decomposed the wavefunction in terms of Bloch functions, leaving the position operator $x$ as the only spatially dependent operator in Equation (2.50). Therefore, to make Schrodinger's equation completely spatially independent, we now need to decompose this operator $x$ in terms of energy $E$ and momentum $k$. This procedure involves advanced quantum mechanics, and only the final result will be provided here.

The position operator $x$ in the crystal momentum representation is decomposed into an "intraband" operator $\partial \partial / \partial k$ and an "interband" operator $X_{n n}(k)$, where $n$ represents one energy band and $n^{\prime}$ represents another energy band. The Schrodinger equation in the crystal momentum representation can, therefore, be written as

$$
\begin{equation*}
\left[E_{n}(k)-\imath q E_{\text {ext }} \frac{\partial}{\partial k}-E\right] a_{n}(k)-\sum_{n^{\prime}} q E_{\text {ext }} X_{n n^{\prime}} a_{n^{\prime}}(k)=0 \tag{2.51}
\end{equation*}
$$

The position of any electron is found to depend on two factors - the contribution of the energy band that it belongs to and the contribution due to the interaction of that energy band with all the other energy bands in the crystal lattice. The intraband operator $l \partial / \partial k$ corresponds to the spatial behaviour of the electron resulting from its own energy band. The interband operator $X_{n n^{\prime}}(k)$, on the other hand, corresponds to the spatial behaviour of the electron due to the contribution of all the other energy bands inside the crystal lattice. It is this interband term that couples the various bands within the crystal, leading to tunnelling. In case no external field were present, there would be no position operator in Equation (2.49) and there would be no interband terms in the final equation to connect the different bands. Thus, electrons would be confined to their bands and there would be no.On the other hand, as the field is increased, the coupling term $\sum_{n^{\prime}} q E_{e x t} X_{n n^{\prime}} a_{n^{\prime}}(k)$ increases, leading to greater tunnelling.

The crystal momentum representation of Schrodinger's equation is difficult to solve. Therefore, as a first-order approximation, the eigenfunctions of Equation (2.51) are written after ignoring the interband terms $\sum_{n^{\prime}} q E_{\text {ext }}$ $X_{n n^{\prime}} a_{n^{\prime}}(k)$. This step is able to simplify the problem by including the effect of the electric field on the bands themselves, without including the effects of interaction between various bands. These eigenfunctions are then used, along with the interband operator $X_{n n^{\prime}}$, to calculate the rate of tunnelling from band $n$ to band $n^{\prime}$.

It should be noted that the above derivation was for a general periodic potential $V_{p e r}$. Kane's model can now be derived for a semiconductor by modelling the periodic potential $V_{p e r}$ as two bands interacting via the $k \cdot p$ perturbation [5]. The final result for the band-to-band generation rate $G_{b t b}$ is given as

$$
\begin{equation*}
G_{b t b}=\frac{E_{e x t}^{2} m_{r}^{1 / 2}}{18 \pi \hbar^{2} E_{G}^{1 / 2}} \exp \left\{\frac{-\pi m_{r}^{1 / 2} E_{G}^{3 / 2}}{2 \hbar\left|E_{e x t}\right|}\right\}=A \frac{E_{e x t}^{2}}{E_{G}^{1 / 2}} \exp \left\{-B \frac{E_{G}^{3 / 2}}{\left|E_{e x t}\right|}\right\} \tag{2.52}
\end{equation*}
$$

where $m_{r}$ is the reduced mass of the charge carrier.
The above model (2.52) is for a direct semiconductor, where the electron tunnels from the valence band to the conduction band without any change in its momentum. In the case of an indirect semiconductor, the electron must change its momentum when tunnelling from the valence band edge to the conduction band edge. To ensure conservation of momentum, this change in momentum is transferred to the semiconductor lattice in the form of quantised lattice vibrations (known as "phonons"), and the final result in this case is [6]:

$$
\begin{equation*}
G_{b t b}=\frac{E_{e x t}^{5 / 2} m_{r}^{1 / 2}}{18 \pi \hbar^{2} E_{G}^{1 / 2}} \exp \left\{\frac{-\pi m_{r}^{1 / 2} E_{G}^{3 / 2}}{2 \hbar\left|E_{e x t}\right|}\right\}=A \frac{E_{e x t}^{5 / 2}}{E_{G}^{1 / 2}} \exp \left\{-B \frac{E_{G}^{3 / 2}}{\left|E_{e x t}\right|}\right\} \tag{2.53}
\end{equation*}
$$

Note that the difference between the band-to-band generation rate in direct and indirect bandgap semiconductors is only in the power of the external electric field $E_{\text {ext }}$ in the pre-exponential term. In the case of direct semiconductors, this power is 2 (i.e. $E_{\text {ext }}^{2}$ ), while in the case of indirect semiconductors, this power is 2.5 (i.e. $E_{e x t}^{5 / 2}$ ).

While the Kane's model is used extensively to calculate the band-to-band tunnelling rate, there are many effects that this model ignores, such as the effects of traps and density of states. Many tunnelling models have subsequently been developed that use Kane's model as a base, and are more accurate in calculating the band-to-band tunnelling rate. One such model is the Hurkx model, which includes the effects of traps and density of states while calculating the band-toband tunnelling rate.

### 2.4.2.2 Hurkx model

This model was developed by Hurkx in 1992 [7] as a recombination model that included the effects of tunnelling. However, the Hurkx model is also used as a tunnelling model by many device simulators. The advantage of the Hurkx model over Kane's model is the inclusion of effects of trap-assisted tunnelling, and density of states. A qualitative outline of the model's derivation is now presented.

The net recombination rate $R$ in the Hurkx model is taken to be the sum of recombination rates due to traps (including both SRH recombination and trapassisted tunnelling) $R_{\text {trap }}$ and due to band-to -band tunnelling $R_{b b t}$ :

$$
\begin{equation*}
R=R_{t r a p}+R_{b b t} \tag{2.54}
\end{equation*}
$$

The expression for $R_{\text {trap }}$ is given as

$$
\begin{equation*}
R_{\text {trap }}=N_{T} \frac{c_{n} c_{p} n_{t} p_{t}-e_{n} e_{p}}{c_{n} n_{t}+c_{p} p_{t}+e_{n}+e_{p}} \tag{2.55}
\end{equation*}
$$

where $N_{T}$ is the trap density and $n_{t}$ and $p_{t}$ are the densities of electrons and holes having capture rates $c_{n}$ and $c_{p}$, respectively. The probability per unit time for emission of electrons and holes is $e_{n}$ and $e_{p}$, respectively. You may notice that this expression is very similar to the one given for SRH recombination [8]. The reason for this is that both the derivations follow the same procedure. In the Hurkx model the $R_{\text {trap }}$ term incorporates the effects of tunnelling while calculating the density of carriers ( $n_{t}$ and $p_{t}$ ) and their emission probabilities per unit time ( $e_{n}$ and $e_{p}$ ) since these quantities are increased due to tunnelling.

Let us first consider the effect of tunnelling on the carrier densities $n_{t}$ and $p_{t}$. In case high electric fields are present, electrons can tunnel into the bandgap and get captured by traps, as shown in Figure 2.5(a). Due to this, the density of carriers


Figure 2.5 (a) Electrons from the conduction band of the n-region can tunnel traps having energy lower than the conduction band minima. (b) Electrons occupying traps having energy above the conduction band minima can tunnel to the conduction band [7]. Source: Reproduced with permission of IEEE.
within the depletion region increases. The expression for the carrier densities is obtained by solving the effective-mass Schrodinger equation for a linear potential. The electron carrier density $n_{t}(x)$ is

$$
\begin{equation*}
n_{t}(x)=n(x)+\int_{\delta}^{x}\left(-\frac{d n(x)}{d x}\right)_{x=x_{1}} \frac{A i^{2}\left[\gamma\left(x-x_{1}\right)\right]}{A i^{2}[0]} d x_{1} \tag{2.56}
\end{equation*}
$$

where $A i$ is the Airy function, $\gamma=\left(2 q \bar{F} m^{*} \hbar^{-2}\right)^{1 / 3}, \bar{F}$ is the average electric field and $m *$ is the effective mass. The first term on the right-hand side $n(x)$ is the density of electrons without considering the effects of tunnelling. The second term is the contribution due to an electron at a position $x_{1}$ tunnelling to a trap at $x$. The
integration is performed over all the possible locations $x_{1}$ from where an electron can tunnel into a trap at $x$. A similar expression can be written for the hole density.

Let us now look at how tunnelling affects the emission rates. In case tunnelling is not considered, an electron in a trap needs thermal excitation to cover the entire trap depth $E_{C}-E_{T}$ to reach the conduction band. However, an electron can be partially excited through only a part of the trap depth, and can then tunnel through the remaining barrier, as shown in Figure 2.5(b). The emission probability can, therefore, be written as

$$
\begin{equation*}
e_{n}=e_{n 0}\left[1+\frac{1}{k T} \int_{0}^{\Delta E_{n}} e^{E / k T} \frac{A i^{2}\left(2 m^{*} \gamma^{-2} \hbar^{-2} E\right)}{A i^{2}(0)} d E\right] \tag{2.57}
\end{equation*}
$$

where $e_{n 0}$ is the emission probability if tunnelling is not considered.
In case the potential cannot be approximated as linear, the tunnelling probability obtained for a constant electric field $A i^{2}\left[\gamma\left(x-x_{1}\right)\right] / A i^{2}(0)$ in Equations (2.56) and (2.57) can be replaced by the tunnelling probability obtained from the WKB approximation (Equation (2.30)). This completes the calculation for the trap related recombination rate.

To calculate the recombination rate due to band-to-band tunnelling, Hurkx has used a slightly modified form of Kane's formula:

$$
\begin{equation*}
R_{b b t}=-B|F|^{\sigma} D e^{-F_{0} /|F|} \tag{2.58}
\end{equation*}
$$

where $F$ is the external field at the given point, $\sigma=2$ for direct transitions and $\sigma=5 / 2$ for indirect transitions. The pre-factor $B$ is a constant, while $F_{0}$ is proportional to $E_{g}^{3 / 2}$, where $E_{g}$ is the bandgap. The term $D$ was not present in Kane's model and is a function that accounts for the relative position of the Fermi levels $E_{f p}$ and $E_{f n}$ in the neutral p- and n-regions, respectively:

$$
\begin{equation*}
D\left(V, E_{f n}, E_{f p}\right)=\frac{1}{\exp \left[\left(-E_{f p}-q V\right) / k T\right]+1}-\frac{1}{\exp \left[\left(-E_{f n}-q V\right) / k T\right]+1} \tag{2.59}
\end{equation*}
$$

where $V$ is the electric potential in the region. To calculate the effect of density of states, it is assumed that an electron from the p-region (point A in Figure 2.6) tunnels to the n-region (point $B$ in Figure 2.6), both at a potential energy $E=q V$. The term $D$ is, therefore, the difference between the probability of occupation at point A and point B. Outside the depletion region, on the neutral $n$-side, the function $D$ is almost zero as the potential energy $q V$ equals the neutral n-side Fermi energy $E_{f n}$. Physically, this signifies that there are no final states in the p-region at this energy into which an electron can tunnel. Similarly, this function is negligible outside the depletion region on the neutral


Figure 2.6 Incorporation of density of states in the Hurkx model [7]. Source: Reproduced with permission of IEEE.
p-side, as there are no initial states from which an electron can tunnel. Therefore, this function $D$ is able to capture the effect of density of states on band-to-band tunnelling.

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## 3

## The tunnel FET

### 3.1 Device structure

We will now familiarise ourselves with the basic structure of the device we will be modelling in this book - the tunnel FET. Understanding the structure of any device goes beyond simply knowing the different regions, materials and dopings used. It is essential to understand the reason behind the particular structure chosen, and how each parameter in the structure optimises some desirable characteristics in the device behaviour. Therefore, in this chapter, apart from introducing the structure of the TFET, we will have a brief qualitative discussion about the behaviour of the TFET.

### 3.1. 1 The need for tunnel FETs

An essential area of focus in semiconductor technology, and devices in particular, is the continuous scaling down of the device dimensions. We have come a long way in this pursuit - from gigantic vacuum tubes that led to computers the size of large rooms, to the first MOSFET with a gate length of $300 \mu \mathrm{~m}$, to transistors with gate lengths of 14 nm or less. This has led to integrated circuits containing billions of transistors.

Scaling the length of a MOSFET has many benefits, besides the increased number of transistors in a chip. A reduced gate length leads to a reduced gate capacitance, thereby increasing the switching speed of the circuit. Moreover, the voltage scaling that is a necessary part of device miniaturisation also causes reduction in the power consumption of the device.

[^2]However, as the device dimensions were reduced to 50 nm and the power supply to 0.5 V , the OFF-state power consumption of MOSFETs became a major challenge. The drain current of a MOSFET is controlled by the thermionic emission from the source into the channel. As the gate voltage increases, the potential barrier between the source and the channel decreases, leading to an increase in the drain current. This leads to two problems - a larger OFF-state current due to subthreshold conduction and a higher subthreshold slope. The subthreshold slope $(S S)$ of a MOSFET is the change in gate voltage $V_{G}$ required to increase the drain current $I_{D S}$ by a factor of 10 . A lower subthreshold slope would allow for a higher ratio of ON-current to OFF-current $\left(I_{\text {ON }} / I_{\text {OFF }}\right)$, and would lead to a lower power dissipation in the OFF-state. The subthreshold slope of a MOSFET is given as

$$
\begin{equation*}
S S=\left(\frac{d\left(\log _{10} I_{D S}\right)}{d V_{G}}\right)^{-1}=2.3 \frac{k T}{q}\left(1+\frac{C_{d}}{C_{o x}}\right) \tag{3.1}
\end{equation*}
$$

where $C_{d}$ is the depletion region capacitance and $C_{o x}$ is the gate oxide capacitance. As we can see from Equation (3.1), the lowest possible value of the subthreshold slope for a MOSFET is $2.3 \mathrm{kT} / \mathrm{q}$, which is $60 \mathrm{mV} /$ decade at room temperature. For example, to get an $I_{\text {ON }} / I_{\text {OFF }}=10^{5}$, we need to apply a gate voltage of $5 \times 60 \mathrm{mV}=0.3 \mathrm{~V}$. Therefore, it is difficult to scale down the supply voltage if we want to realise a large $I_{\text {ON }} / I_{\text {OFF }}$ ratio. To overcome these fundamental limitations of a larger OFF-state current and poor subthreshold slope of the MOSFET, an alternate device was necessary.

One such device is the tunnelling field-effect transistor (TFET) [1-6]. TFETs can exhibit subthreshold slopes below $60 \mathrm{mV} /$ decade due to a fundamental difference in the mechanism of current control as compared to MOSFETs. In MOSFETs, the current depends on the thermionic emission of free carriers across the potential barrier between the source and the channel. On the other hand, the current in TFETs depends on the charge carriers tunnelling through a potential barrier between the source valence band and the channel conduction band. As this potential barrier is very wide in the OFF-state of the device, TFETs exhibit very low OFF-state current.

Apart from the limitation imposed by the subthreshold slope, MOSFETs in the sub-50 nm channel regime also suffer from various short channel effects, such as drain-induced barrier lowering, threshold voltage roll-off, charge sharing between gate and drain, etc. [7]. As we shall study later in the chapter, TFETs have a greater immunity to these short channel effects.

It may be pointed out that TFETs differ from the MOSFET only in the type of source doping. Therefore, the integration of the TFET fabrication process with the current MOSFET fabrication process would be easy.

Let us now examine the structure of this device.

### 3.1.2 Basic TFET structure

We will now examine the basic structure of a tunnelling FET. Later, we will study many variations of this structure, but the working principle of the TFET is based on this basic arrangement of regions, doping and terminals.

Figure 3.1(a) shows the basic structure of an n-channel TFET. The device has three regions - the source, the channel and the drain. Comparing the structure of an n-channel TFET with that of an n-channel MOSFET (Figure 3.1(b)), we find that the source doping in a TFET is p-type, whereas it is n-type in the MOSFET. This is the only major difference between a TFET and a MOSFET. The channel region
(a) n-channel TFET

(b) n-channel MOSFET

(c) p-channel TFET


Figure 3.1 Basic structure of a TFET.
in the TFET is usually intrinsic, or very lightly doped. We will now qualitatively examine the behaviour of TFETs.

### 3.2 Qualitative behaviour

The behaviour of any transistor is usually described by its current characteristics - a plot of the current flowing in the device under various biasing conditions. For a transistor, the most significant current characteristics are the transfer characteristics and the output characteristics. These are plots of the drain current in the transistor with respect to the gate and the drain bias, respectively.

In this section, we will develop a qualitative understanding of the behaviour of a TFET. The effect of biasing on the drain current of a TFET is best understood by observing the band diagrams of a TFET under various biasing conditions. While studying these, we will qualitatively predict the variation of the drain current with changing bias. Finally, we will compare our qualitative predictions with the actual characteristics of a TFET.

### 3.2.1 Band diagram

### 3.2.1.1 Thermal equilibrium

The thermal equilibrium band diagram of a TFET, that is with no external bias $\left(V_{G}=V_{S}=V_{D}=0\right)$, is shown in Figure 3.2. There are two depletion regions formed - one at the source-channel junction and the other at the channel-drain junction.

### 3.2.1.2 OFF-state

The TFET is in OFF-state when the drain voltage $V_{D S}>0$ and the gate voltage $V_{G S}=0$, which is similar to the OFF-state of a MOSFET. The band diagram for this case is shown in Figure 3.3.

In the OFF-state of the TFET, any charge carriers present in the conduction band of the channel would have a tendency to drift to the drain and thus generate a current. However, as the source is p-type, there are very few free electrons in its conduction band, and therefore very few electrons can be injected into the channel. This leads to a negligible OFF-state current. We should note that in the case of a MOSFET, the source is n-type and has free electrons in its conduction band. Through thermionic emission, a few of these electrons will be injected into the channel over the potential barrier at the source-channel junction. This leads to a higher OFF-state current in a MOSFET as compared to a TFET.


Figure 3.2 Band diagram at the surface of a n-channel TFET in thermal equilibrium (i.e. at zero bias).


Figure 3.3 Band diagram along the surface of a TFET in the OFF-state (i.e. at $V_{G S}=0 \mathrm{~V}$ ).

### 3.2.1.3 ON-state

For current to flow in the device, charge carriers need to be injected into the conduction band of the channel. In the case of TFETs, since there are negligible free electrons in the conduction band of the p-type source, these charge carriers originate from the valence band of the source.

As we increase the gate voltage $V_{G S}$, the energy bands in the channel change with respect to the source as shown in Figure 3.4. At a certain value of the gate voltage $V_{G S}$, the valence band in the source gets aligned with the conduction band in the channel, as shown in Figure 3.4(a). In the OFF-state of the device, the electrons in the valence band of the source did not have any available energy state in the channel into which they could tunnel. Now that the valence band of the source is aligned with the conduction band of the channel, electrons can tunnel from the former into the latter through the potential barrier formed by the bandgap $E_{G}$ (dashed shape in Figure 3.4(a)). The gate voltage at which this alignment of the source valence band and the channel conduction band occurs is the beginning of the ON-state of the TFET.

Notice that in Figure 3.4(a) the valence band of the source and the conduction band of the channel are just aligned. As the gate bias is further increased (Figure 3.4(b)), the bands in the channel region are further lowered in energy, and electrons occupying energy levels from the valence band edge of the source $E_{V, \text { Source }}$ to the conduction band edge of the channel $E_{C, \text { Channel }}$ can tunnel to the conduction band in the channel. This leads to a steep increase in the current.

In addition, an increase in the gate bias leads to a reduction in the length of the tunnelling barrier (also called the tunnelling length), which further increases the current. Let us compare the potential barrier through which an electron on the valence band edge of the source tunnels to reach the conduction band in the channel for a low (Figure 3.4(a)) and a high (Figure 3.4(b)) gate bias. As we can observe, the barrier height is the same in both cases - it is the bandgap $E_{G}$ of the material. However, due to a greater electric field in the case of a higher gate bias, the tunnelling length is decreased. Therefore, the tunnelling probability increases in the case of a higher gate bias, leading to a higher current. We should remember that the tunnelling probability depends exponentially on the tunnelling width (Equation (2.14)). Hence, an increase in the gate voltage not only increases the number of electrons that are able to tunnel but also increases their tunnelling probability. Therefore, the current would vary significantly with a changing gate voltage.

In the case of a high gate voltage (Figure 3.4(b)), let us compare the tunnelling probability of an electron at the valence band edge of the source ( $E_{V, \text { Source }}$ ) with that of an electron occupying the lowest energy from which tunnelling can occur


Figure 3.4 Band diagram along the surface of a TFET (a) at the beginning of the ON-state and (b) deep into the ON-state.
$\left(E_{C, \text { Channel }}\right)$. The tunnelling probability depends on the height and the length of the potential barrier. The height of the potential barrier is the same for all energy levels - it is the bandgap of the material. The length of the potential barrier depends on the slope of the energy bands (i.e. the electric field) - a higher slope leading to a shorter tunnelling length. Using our knowledge of the $\mathrm{p}-\mathrm{n}$ junction diode, the slope of the energy bands is highest at the junction of p-type and n-type regions. In the case of TFETs, this junction lies almost at the edge of the source, since the source is heavily doped as compared to the channel. Hence, the electron at the source edge occupying the energy level $E_{V, \text { Source }}$ tunnels through a potential barrier having a shorter tunnelling length $\left(L_{T_{1}}\right)$ as compared to the electron at energy $E_{C \text {,Channel }}\left(L_{T_{2}}\right)$. Therefore, the tunnelling probability of the electron at energy $E_{C, \text { Channel }}$ is lower than that of the electron at the valence band edge $E_{V, \text { Source }}$. Thus, we can say that the contribution to the total current decreases for lower energy states in the source valence band and is negligible for energy states that are very low in the source valence band. Due to this, at a high enough gate voltage, the drain current increment with increasing gate voltage is almost solely due to a decrease in the tunnelling length.

As the energy bands in the channel are further lowered due to increasing gate bias, another effect occurs. As in the case of a MOSFET, an inversion charge layer starts forming in the channel. As the gate bias is positive, this layer consists of electrons. The magnitude of this inversion charge layer increases with gate bias, and at sufficiently high gate bias the inversion charge layer leads to "pinning" of the channel potential, as we shall now study.

### 3.2.1.4 Pinning of the channel potential

Till this point, we were modulating the channel potential by changing the gate voltage. However, as the gate voltage is increased above the drain voltage (i.e. $V_{G S}>V_{D S}$ ), the magnitude of the inversion charge layer discussed in the preceding section becomes comparable to the electron density in the $n^{+}$-drain region. This leads to the channel being effectively "shorted" to the drain, and the channel potential is approximately equal to the drain potential. Due to this, the energy bands in the channel do not significantly change with a further increase in gate voltage. Therefore, the potential in the channel is said to be "pinned" to the drain potential. This phenomenon is called pinning of the channel potential. It should be noted that this pinning does not occur precisely when $V_{G S}=V_{D S}$, since the magnitude of the inversion charge changes continuously, due to which the gate control of the channel potential is reduced when $V_{G S} \sim V_{D S}$ and is completely lost when $V_{G S}>V_{D S}$.

In the preceding section, we discussed that an increase in the gate bias increases the current in the device due to two factors. The first was an increase


Figure 3.5 Band diagram along the surface of the TFET with increasing value of the gate voltage $\left(V_{G S}\right)$ in the $O N$-state.
in the number of energy levels in the source valence band from where tunnelling is possible, which occurred because the conduction band in the channel was further lowered in energy. However, when $V_{G S}>V_{D S}$, the channel potential is pinned to the drain potential, and hence an increase in gate bias will no longer lead to a lowering of the conduction band energy in the channel.

The other factor contributing to a higher current with an increase in gate bias was the increased electric field in the source-channel junction, which led to a reduction in the tunnelling length. As we can observe from a comparison of the energy band diagrams in the source-channel junction for three gate biases $V_{G 3}>V_{G 2}>V_{D}>V_{G 1}$ (Figure 3.5), the electric field increases with increasing gate bias even when the channel potential is pinned to the drain potential.

Therefore, when $V_{G S}>V_{D S}$, the current in the device increases with gate bias, but at a lower rate than in the case of $V_{G S}<V_{D S}$.

### 3.2.1.5 Ambipolar behaviour

In the previous sections, a positive gate voltage was applied with the drain voltage $V_{D S}>0$. Let us now observe the behaviour of a TFET when a negative gate voltage is applied.
(a)

(b)


Figure 3.6 Band diagram along the surface of a TFET (a) for a zero gate voltage $\left(V_{G S}\right)$ and a positive drain voltage $\left(V_{D S}\right)$, (b) for a negative gate voltage $\left(V_{G S}\right)$ and a positive drain voltage $\left(V_{D S}\right)$, (c) for a negative gate voltage $\left(V_{G S}\right)$ such that the valence band in the source gets aligned with the conduction band in the drain and a positive drain voltage $\left(V_{D S}\right)$, (d) for a more negative gate voltage $\left(V_{G S}\right)$ as compared to Figure 3.6 (c) and a positive drain voltage $\left(V_{D S}\right)$ and (e) with an increasing negative value of the gate voltage $\left(V_{G S}\right)$ and a positive drain voltage ( $V_{D S}$ ).


Figure 3.6 (Continued)
(e)


Figure 3.6 (Continued)

Initially, as $V_{G S}=0$ (Figure 3.6(a)), no electrons can tunnel from the source valence band to the channel conduction band or from the channel valence band to the drain conduction band. As the gate voltage is decreased below 0 V , the energy band of the channel moves upward with respect to the source (Figure 3.6(b)). When the valence band of the channel is aligned with the conduction band of the drain (Figure 3.6(c)), electrons from the valence band of the channel can tunnel into the conduction band of the drain, resulting in a current flow. Note that the electrons are tunnelling in the same direction as in the case of a positive gate bias, that is from the left to the right (compare Figure 3.4(b) and (c)). This results in the device current having the same polarity even at a negative gate bias. A further increase in the negative gate bias (Figure 3.6(d)) causes a significant increase in the drain current due to (i) reduction of the tunnelling length at the channel-drain junction and (ii) an increase in the number of states in the channel valence band from where electrons can tunnel to the conduction band of the drain. This is called ambipolar conduction.

As the negative gate bias is further increased, the channel potential gets pinned to the source potential (Figure 3.6(e)). Therefore, as discussed in the section on pinning (Section 3.2.1.4), the channel potential no longer depends on the gate potential. After this point, increasing negative gate voltage causes an increase in the drain current solely due to a decrease in the tunnelling length. Therefore, the drain current increases at a lower rate.

### 3.2.1.6 Effect of varying the drain voltage

Let us now observe the behaviour of the TFET when the drain voltage is varied, for a fixed gate voltage $V_{G S}>0$. As long as the drain voltage is lower than the gate voltage (i.e. $V_{D S}<V_{G S}$ ), the channel potential is pinned to the drain potential, as shown in Figure 3.7. As we can observe from these band diagrams, when $0 \leq V_{D S}<V_{G S}$, there is no depletion region at the channel-drain junction. Therefore, there is negligible resistance between the channel and drain, leading to the voltage being nearly constant in this region. The channel potential is thus solely controlled by the drain potential. As the drain bias is increased, the energy bands in the drain as well as in the channel get lowered with respect to the source. This causes an increase in the tunnelling of electrons from the source to the channel, and therefore a significant increase in the drain current.

However, when the drain voltage is increased such that $V_{D S} \sim V_{G S}$, a depletion region starts to form at the channel-drain junction. When this depletion region is of the same length as in the case of thermal equilibrium, the drain control over the channel potential is significantly reduced, as shown in Figure 3.7. Therefore, the device current remains nearly constant with a further increase in the drain bias.

We will now compare the qualitative predictions made in this section with the observed characteristics of a TFET.


Figure 3.7 Band diagram along the surface of a TFET for a fixed value of the gate voltage $\left(V_{G S}\right)$ (in the $O N$-state) and different values of drain voltage ( $V_{D S}$ ).

### 3.2.2 Device characteristics

### 3.2.2.1 Transfer characteristics

Figure 3.8(a) shows the transfer characteristics of a TFET in which the source and drain dopings are equal and opposite. The regions of operation discussed in the previous section have been marked on the figure. As we can observe, there is a very low current in the OFF-state of the device, when the conduction band of the channel is not aligned with the valence band of the source. As the gate bias is increased, the current rapidly increases due to reduction of the tunnelling width and an increase in the number of initial states in the source from where tunnelling can occur. At a higher gate bias, the rate of increase of current reduces since the contribution of the additional initial states in the source valence band is negligible. As the gate bias approaches the drain potential, the rate of increase of current further decreases due to pinning.

A similar behaviour is observed in the negative bias region. As mentioned in Section 3.2.1.5, this symmetrical behaviour of the TFET is referred to as ambipolar operation and the current curve in the negative bias region is referred to as the ambipolar current of the TFET. Let us briefly explore this ambipolar behaviour of the TFET. The current characteristics under positive and negative gate bias are due to source-channel and drain-channel tunnelling, respectively. The transfer characteristics due to source-channel tunnelling (ON-state current) and drain-channel tunnelling (ambipolar current) have been separately indicated in Figure 3.8(b). Since the source and drain doping is equal and opposite, these two curves are perfectly symmetrical as well.

Figure 3.8(c) compares the transfer characteristics for different drain voltages. We can observe that an increase in the drain voltage causes a larger change in the ambipolar current than in the ON -state current. Let us first consider the effect of the varying the drain voltage on the ambipolar current.

When the gate voltage is negative, an increase in the drain voltage causes the energy bands in the drain to become lower with respect to the channel. This leads to an increase in channel-drain tunnelling, thus causing an increase in the ambipolar current. Therefore, for a given negative gate voltage, the ambipolar current differs significantly for different drain voltages.

Let us now consider the effect of varying the drain voltage on the ON-state current. Before the onset of pinning $\left(V_{G S} \leq V_{D S}\right)$, an increase in the drain voltage has a negligible effect on the channel potential, and hence on the source-channel tunnelling. Therefore, before the onset of pinning, a change in the drain voltage has a negligible effect on the ON-state current for a given gate voltage. Pinning occurs when $V_{G S} \geq V_{D S}$, in which case the channel potential is fixed at the drain potential. After pinning has occurred, an increase in the drain voltage lowers the channel energy bands with respect to the source. This leads to an increase


Figure 3.8 Transfer characteristics $\left(I_{D}-V_{G S}\right)$ of an n-channel TFET (a) for a positive value of $V_{D S}$, showing different regions of operations, ( $b$ ) showing the source-channel tunnelling (ON-state current) and drain-channel tunnelling (ambipolar current) and (c) for different values of $V_{D S}$.


Figure 3.8 (Continued)
in the number of energy states in the source valence band from where electrons can tunnel to the channel conduction band. However, the tunnelling length associated with these energy states is very large and their contribution to the source-channel tunnelling is negligible. Therefore, after pinning has occurred, lowering of the channel energy bands with respect to the source increases the source-channel tunnelling solely due to a shorter tunnelling length. Thus, a change in the drain voltage leads to a smaller difference in the ON-state current after the onset of pinning, as compared to the ambipolar current.

In other words, an increase in the drain voltage increases the number of states available for tunnelling in the ambipolar region, and the tunnelling length for the electrons in these states is smaller. After pinning has occurred in the ON-state, a higher drain voltage increases the number of available states for tunnelling, and the tunnelling length for the electrons in these states is larger. Therefore, varying the drain voltage causes a smaller change in the ONstate current as compared to the ambipolar current. This results in the minima of transfer characteristics to shift towards the right with increasing drain voltage.

### 3.2.2.2 Subthreshold characteristics of MOSFET versus TFET

The main premise of studying the TFETs as an alternative to the MOSFETs is due to the improved subthreshold swing (SS) exhibited by the TFETs. While

MOSFETs have a thermal limit of $60 \mathrm{mV} / \mathrm{dec}$ on SS, the TFETs can achieve an SS below $60 \mathrm{mV} / \mathrm{dec}$ as they operate on a different mechanism (i.e. tunnelling) as compared to the MOSFET (i.e. thermionic injection). However, with this difference in the mechanism of current conduction in the subthreshold region comes a difference in the definition of the subthreshold swing.

In the subthreshold region, a MOSFET conducts current by the mechanism of diffusion of carriers across the channel, and can be written as [8]

$$
\begin{equation*}
I_{D S}=\mu_{e f f} \frac{W}{L} \sqrt{\frac{\varepsilon_{S i} q N_{A}}{4 \psi_{B}}}\left(\frac{k T}{q}\right)^{2} e^{q\left(V_{G S}-\mathrm{V}_{\mathrm{th}}\right) / m k T}\left(1-e^{-q V_{D S} / k T}\right) \tag{3.2}
\end{equation*}
$$

where $\psi_{B}$ is the Fermi potential of the silicon body and the other terms have their usual meaning.

The subthreshold swing (SS) for a MOSFET is defined as

$$
\begin{equation*}
S S=d \log \left(I_{D S}\right) / d V_{G S} \tag{3.3}
\end{equation*}
$$

Hence, the SS of a MOSFET is constant with $V_{G S}$.
On the other hand, a TFET, even in the subthreshold region, conducts by the mechanism of band-to-band tunnelling. As seen in Equation (2.53), the tunnelling generation rate is an inverse exponential function of the electric field. Therefore, in a TFET:

$$
\begin{equation*}
I_{D S} \propto e^{-1 / E} \tag{3.4}
\end{equation*}
$$

where $E$ is the lateral electric field in the channel, which in turn is a function of $V_{G S}$. Hence, in a TFET, the SS varies with the gate voltage and does not have a constant value as in the case of a MOSFET. Hence, for a TFET, two kinds of SS are defined in general: (i) point subthreshold swing and (ii) average subthreshold swing.

The point subthreshold swing is defined at a particular value of $V_{G S}$, as given below:

$$
\begin{equation*}
S S_{p o \text { int }}\left(V_{G S}\right)=d \log \left(I_{D S}\left(V_{G S}\right)\right) / d V_{G S} \tag{3.5}
\end{equation*}
$$

The average subthreshold swing is defined over a range of $V_{G S}$ values, as given below:

$$
\begin{equation*}
S S_{A V G}=\frac{V_{t h}-V_{\text {off }}}{\log \left(I_{D S}\left(V_{t h}\right)-I_{D S}\left(V_{\text {off }}\right)\right)} \tag{3.6}
\end{equation*}
$$

where $V_{t h}$ is the threshold voltage of the TFET and $V_{\text {off }}$ is the gate voltage in the OFF-state.

At any point in this text, unless mentioned otherwise, the subthreshold swing in the context of a TFET refers to the $S S_{A V G}$.

### 3.2.2.3 Output characteristics

Figure 3.9 shows the output characteristics of a TFET. As discussed in Section 3.2.1.6, initially the gate potential is greater than the drain potential, due to which the channel potential is pinned at the drain potential. Therefore, as the drain potential is increased, the channel potential increases, leading to a significant increase in the current. As the drain potential approaches the gate potential, the channel potential is no longer dependent on the drain potential. Therefore, the current remains nearly constant with increasing drain bias. In this case, the output characteristics are saturated. In the following section, we will compare the output characteristics of a MOSFET and a TFET.

A comparison of the output characteristics of the MOSFET (Figure 3.10) and the TFET (Figure 3.9) indicates two prominent differences. First, the output resistance in the saturation region is much higher in the case of the TFET, since channel length modulation has a negligible effect on the drain current of a TFET. Moreover, in the saturation region ( $V_{D S}>V_{G S}$ ), changing the drain voltage has a negligible effect on the channel potential, due to which the drain current due to source-channel tunnelling remains constant.


Figure 3.9 Output characteristics $\left(I_{D}-V_{D S}\right)$ of an n-channel TFET for different values of $V_{G S}$.


Figure 3.10 Output characteristics ( $I_{D}-V_{D S}$ ) of an n-channel MOSFET for different values of $V_{G S}$.

The other difference in the output characteristics is in the saturation voltage, which is higher in the case of the TFET. This behaviour is referred to as delayed saturation, and makes TFETs less suitable for low-voltage analog applications. Let us examine the physics behind this phenomenon.

### 3.2.2.4 Delayed saturation in output characteristics

In the output characteristics of a MOSFET, as $V_{D S}$ increases, the drain current saturates. This saturation occurs because, as the drain bias is increased, the electric field at the drain-channel junction becomes large enough to make it reverse-biased, thus causing a pinch-off in the channel at the drain end. Saturation of the drain current with an increase in $V_{D S}$ is also observed in the output characteristics of a TFET. However, in a TFET, the drain voltage at which the drain current saturates (i.e. $V_{D S(\mathrm{sat})}$ ) is higher as compared to that of a MOSFET with a similar structure, as can be seen in Figure 3.10. Hence, we get a delayed saturation in output characteristics of a TFET. Let us now understand why a TFET exhibits such behaviour in its output characteristics.

Let us have a look at Figure 3.11, which compares the surface potential of the TFET and the MOSFET in the linear region for $V_{G S}>0$. Surface potential is


Figure 3.11 Surface potential of a p-channel TFET and a p-channel MOSFET in the linear and the saturation regions of operation.
defined as the potential along the channel of the device at the $\mathrm{Si}-\mathrm{SiO}_{2}$ interface. The potential is measured with respect to the source Fermi level, that is the source Fermi level is taken to be $\psi=0$. Here we can see that in a TFET, as the drain bias is increased from $V_{D S}=0$ to $V_{D S}<V_{G S}$, a large part of the drain bias drops across the source-channel junction and only a small part drops across the rest of the channel, whereas in a MOSFET, the drain bias drops across the entire channel almost linearly. Thus the lateral electric field at the drain-channel junction is less in a TFET as compared to that in a MOSFET for the same biasing conditions. However, the magnitude of lateral electric field required at the drain-channel junction to cause the pinch-off of the channel is the same for both TFETs and MOSFETs, since the inversion charge density is identical in the channel. Hence, a greater drain bias $\left(V_{D S}\right)$ is needed to cause the channel to be pinched-off in a TFET, leading to an extended linear region in the output characteristics. Thus, the saturation in the output characteristics of a TFET is achieved at a higher $V_{D S}$ as compared to that in a MOSFET with a similar structure.

This behaviour of a TFET can be detrimental in analog applications, as the transistor in an analog circuit works primarily in the saturation region.

A delayed saturation can severely hamper the transconductance of the device and will affect the gain of an amplifier designed using the TFETs. It will also affect the maximum possible output swing of an amplifier (without distorting the output signal). In digital circuits, delayed saturation will affect the switching speed of the logic gate designed using the TFET, as the transistor will not be operating at the maximum possible drain current for a given supply voltage if the saturation voltage $V_{D S(S a t)}$ is greater than the supply voltage $V_{D D}$. Hence, while designing a TFET for CMOS application, one should make sure that the saturation in the output characteristics of the TFET takes place at $V_{D S}<V_{G S}$, so that at $V_{D S}=V_{D D}$, the device is operating at the maximum possible current.

The characteristics that we have studied in this section are dependent on the particular parameters chosen while fabricating the TFET, such as the dopings, the gate work function, etc. Variation of these parameters allows us to modify the TFET according to the final specifications that are desired. In the next section, we will briefly examine these dependencies.

### 3.2.3 Performance dependence on device parameters

### 3.2.3.1 Doping

As already mentioned while comparing the structure of a TFET with that of a MOSFET, the primary difference between the two is the source doping. The source doping in an n -channel TFET is $\mathrm{p}^{+}$-type. The reason for this can be understood by comparing the band diagrams of the two TFET structures - one with a high source doping $N_{A 1}$ and the other with a lower source doping $N_{A 2}$, as shown in Figure 3.12. As we can observe, a higher doping in the source leads to a shorter depletion region at the source-channel junction. Due to this, as indicated in Figure 3.12, the electron occupying the valence band edge in the source would have to tunnel through a shorter distance $L_{t_{1}}$ in the case of higher source doping, as compared to the larger distance $L_{t_{2}}$ in the case of lower source doping. Therefore, the tunnelling probability would be greater in the case of a higher source doping, thereby resulting in a larger ON-state current.

Following a similar reasoning, an increase in the drain side doping would lead to a greater drain-channel tunnelling in the case of negative gate bias, thereby increasing the ambipolar current. In many applications, the ambipolar current is sought to be minimised, which may be accomplished by reducing the drain doping. The transfer characteristics of two TFETs is compared in Figure 3.13 - one with a high drain doping and one with a lower drain doping. We find that the ambipolar current is successfully reduced by reducing the drain doping.


Figure 3.12 Band diagrams along the surface of an n-channel TFET with different values of source doping concentration.


Figure 3.13 Transfer characteristics of an n-channel TFET with different drain doping concentrations for a fixed drain voltage ( $V_{D S}$ ).

### 3.2.3.2 Gate work function

In Figure 3.14, we have compared the band diagram of a TFET with a higher gate work function $\Phi_{G 1}$ to that of a TFET with a lower gate work function $\Phi_{G 2}$, at thermal equilibrium. Due to the formation of a greater inversion charge layer in the latter case, the energy bands of the channel are lower. This leads to a steeper source-channel junction, thereby (as discussed in the preceding section) increasing the source-channel tunnelling and the ON -state current. It should be noted that this increase in the ON-state current is accompanied by an increase in the OFFstate current, and thus an increased power dissipation in the OFF-state. Moreover, the drain-channel tunnelling is reduced in this case, since the channel energy bands are now closer to the drain energy bands (Figure 3.14). This behaviour can be observed by comparing the transfer characteristics of these two TFET structures in Figure 3.15.

Another effect of changing the gate work function on the transfer characteristics can be observed from the transfer characteristics (Figure 3.15) - shifting of the central point of the current curve to the left upon a decrease in the gate work function. As discussed in Section 3.2.2.1 of this chapter, the transfer characteristics of the TFET can be viewed as a superposition of two curves - the left-hand side curve due to the drain-channel tunnelling and the right-hand curve due to the source-channel tunnelling. Decreasing the gate work function increases the


Figure 3.14 Band diagram along the surface of an n-channel TFET for different values of the gate metal work function.


Figure 3.15 Transfer characteristics of an n-channel TFET with different values of the gate work function.
source-channel tunnelling and decreases the drain-channel tunnelling, raising the right-hand curve and lowering the left-hand curve along the current axis, thereby shifting the central point of the curve to the left.

### 3.2.3.3 Gate oxide

The function of the gate oxide (in both MOSFETs and TFETs) is twofold - to insulate the gate and to enable the gate to control the electrostatics in the channel. Since the oxide is an insulator, the gate oxide prevents current leakage from the channel to the gate.

The latter function of the gate oxide is enhanced by choosing thin oxides with a high dielectric constant. Let us examine this point further. The oxide capacitance is given as $C_{o x}=\epsilon_{o x} / t_{o x}$, where $\epsilon_{o x}$ is the permittivity of the oxide and $t_{o x}$ is the thickness. As this formula shows, the gate oxide capacitance can be increased by reducing the thickness. A greater oxide capacitance leads to a higher charge being formed in the channel at the same gate voltage, leading to a greater control of the gate on the electrostatics of the channel. However, a thinner gate oxide may lead to some carriers from the channel tunnelling to the gate, generating a gate leakage current [9], which is not desirable. This can be prevented by the use of a material with a high permittivity, such as an oxide, in addition to a thin layer


Figure 3.16 Gate leakage in a TFET through (a) $\mathrm{SiO}_{2}$ and (b) $\mathrm{HfO}_{2} / \mathrm{SiO}_{2}$ gate stack [9]. Source: Reproduced with permission of APEX/JJAP, Chaturvedi and M. J. Kumar, "Impact of gate leakage considerations in tunnel field effect transistor design", Japanese Journal of Applied Physics, vol. 53: 7, pp.074201-1-8, June 2014.
of silicon dioxide. As shown in Figure 3.16, this would cause a thicker potential barrier between the channel and the gate, thereby reducing the tunnelling rate. The use of an oxide material with high permittivity has the added benefit of further reducing the oxide capacitance and thus increasing the gate control of the channel.

While varying the structural parameters of a TFET results in a significant modification of TFET characteristics, the use of different structures can further enhance the device behaviour. We will now briefly look at commonly studied TFET structures and the advantages that these structures provide when compared to a basic TFET.

### 3.3 Types of TFETs

Based on the structure, TFETs can be broadly classified into two categories: planar and three-dimensional structures. A planar TFET is a device in which the cur-rent-carrying surface is planar. The device can be made on a bulk silicon wafer or on an SOI wafer. For better gate control over the channel, SOI TFETs are preferred over bulk TFETs, and only the former are extensively studied. Let us first study certain important planar TFET structures.

### 3.3.1 Planar TFETs

Figure 3.17 shows the structure of an SOI TFET. It consists of a thin layer of silicon (typically around 10 nm or less) grown on a layer of buried oxide (around a hundred nm thick) on a silicon substrate. The gate oxide ( $1-2 \mathrm{~nm}$ thick) is grown


Figure 3.17 Schematic of a p-channel SOI TFET.


Figure 3.18 Schematic of a p-channel double gate (DG) TFET.
on the thin silicon layer followed by deposition of gate, source and drain metal contacts. The source and drain are then formed by the appropriate doping in the thin silicon layer. In an SOI TFET, the entire thin silicon layer is depleted and the buried oxide layer blocks all the source-to-drain leakage paths through the bulk region. Moreover, the drain-body and source-body depletion regions are small, thus providing a better gate control over the channel. This also leads to a smaller source/drain to substrate capacitance. The advantages of an SOI structure can be further enhanced using the following device structures.

### 3.3.1.1 Double gate TFET

Figure 3.18 shows the structure of a double gate TFET. It consists of two gates, one at the top (called the front gate) and the other at the bottom (called the back gate). This configuration improves the electrostatic control of the gate on the channel since now the field lines from the gate terminate at the back gate rather than terminating in the channel. The ON-state current is also increased as compared to a single gate TFET, since there are two channels in which current can flow in the device.


Figure 3.19 Schematic of a p-channel dual material gate (DMG) TFET [10]. Source: Reproduced with permission of IEEE.

### 3.3.1.2 Dual material gate TFET

An important variation of the TFET structure is the dual material gate (DMG) TFET [10]. A schematic of this device is shown in Figure 3.19. It consists of two gates of different work functions along the length of the channel - one gate covers the part of the channel near the source and the second gate the part near the drain. The gate near the source is called the tunnelling gate, as tunnelling in the ON-state of the device occurs at the source-channel junction. The gate near the drain is called the auxiliary gate. The DMG structure can be fabricated using sidewall spacer techniques [11-13].

In a p-type DMG TFET, the tunnelling gate has a higher work function as compared to the auxiliary gate. As discussed in Section 3.2.3.1, this leads to a higher potential difference between the source and the channel, thus reducing the tunnelling length and increasing the ON -state current. If the length of the tunnelling gate is small in comparison with the auxiliary gate, the surface potential in the OFFstate depends solely on the auxiliary gate work function. As the auxiliary gate has a lower work function, the potential difference between the source and the channel is lower in the OFF-state, thus reducing the OFF-state current. This behaviour is displayed in Figure 3.20, which compares the transfer characteristics of two single material gate (SMG) TFETs having different gate work functions with that of a DMG TFET. The SMG with a higher work function (4.8 eV) has a higher ONstate current than the SMG with a lower work function ( 4.4 eV ). However, this higher ON-state current is accompanied by a correspondingly higher OFF-state current, leading to a higher subthreshold swing and a greater power dissipation in the OFF-state. While the SMG with the lower gate work function has a lower subthreshold swing, its ON-state current is low. The DMG is able to combine the benefits of both these structures - it has an ON-state current equal to an SMG with a gate work function of 4.8 eV and an OFF-state current equal to that of the SMG


Figure 3.20 Comparison of the transfer characteristics of two single material gate (SMG) TFETs having different gate work functions to that of a DMG TFET.
with a gate work function of 4.4 eV . Therefore, the DMG TFET provides a higher subthreshold slope as compared to a conventional SMG TFET as well as a higher ON-state current. It also has lower DIBL effects as the channel under the auxiliary gate has a higher resistance as compared to the channel under the tunnelling gate, causing a lower potential drop in the latter and shielding it from the variations in the drain bias [10, 14]. It also has less threshold voltage roll-off.

### 3.3.1.3 p-n-p-n TFET or n-p-n-p TFET

Another popular TFET structure is the p-n-p-n TFET [15-18], the structure of which is shown in Figure 3.21. It consists of a thin $\mathrm{P}^{+}$pocket at the source-channel junction. This pocket leads to the formation of a local minima at the conduction band edge $E C$ at $V G S=0 \mathrm{~V}$, as shown in Figure 3.22, which results in a more abrupt change and a lower value of tunnelling barrier width. It also increases the lateral electric field at the tunnelling junction, giving a higher ON -state current. Apart from this, a reduction in the vertical electric field minimises the generation rate of the interface traps, improving device reliability. For obtaining a steep subthreshold slope, an optimum length ( $\sim 4 \mathrm{~nm}$ ) of the pocket-doped region is needed. If the pocket is too long, it is not fully depleted, and the tunnelling junction is not gate controlled. This degrades the subthreshold swing of the device. Figure 3.23 shows the band diagram of p-n-p-n TFET with varying widths of the pocket. Fabricating a narrow doped pocket and ensuring the steepness of the junction is difficult. It is possible either with an epitaxial growth in a vertical transistor or a very precise implant. One possible solution to this difficulty is to use the charge plasma


Figure 3.21 Schematic of a p-channel n-p-n-p TFET (i.e. p-n-p-n TFET for an n-channel TFET).


Figure 3.22 Band diagram of a p-channel n-p-n-p TFET (i.e. p-n-p-n TFET for an n-channel TFET) along the surface in the ON-state.


Figure 3.23 Band diagram of a p-channel n-p-n-p TFET (i.e. p-n-p-n TFET for an n-channel TFET) along the surface with varying widths of the $P^{+}$pocket region.


Figure 3.24 Schematic of a charge plasma p-n-p-n TFET.
technique to generate the p-n-p-n doping sequence. Figure 3.24 shows the structure of the device, where a metal of the gate work function $(\Phi)$ of 5.93 eV is used to create a $\mathrm{P}^{+}$source region. This results in a p-n-p-n doping sequence in the device with an abrupt junction and without the use of any implants. Hence, the charge plasma technique can be very useful in the fabrication of a p-n-p-n TFET.

### 3.3.1.4 Raised Ge-source TFET

As silicon-based TFETs have a low ON-state current, the use of germanium (Ge) in the source is popular due to a low bandgap of Ge. Also, since tunnelling is a function of the electric field, modifying the structure of the TFET to align the direction of the tunnelling of the carriers with the gate electric field is also a good option [19]. Hence, a raised germanium source TFET as shown in Figure 3.25 is an important TFET structure and provides many advantages over a conventional Si-based TFET structure. As can be seen in Figure 3.25, the raised source structure provides a higher tunnelling area as compared to a conventional Si TFET in which tunnelling occurs only in a small area near at source-channel junction near the surface of the device. In a raised source structure, tunnelling occurs in the entire thickness of the source ( $T_{G e}$ ) in the direction perpendicular to the gate. Also, the use of Ge, which has a low bandgap, increases the ON-current. Since the tunnelling of the carriers is aligned with the direction of the gate electric field, the device also has an improved subthreshold slope (SS) over a conventional TFET because of better gate control over the tunnelling of carriers. Thus, a raised Ge-source TFET provides a higher ON-current and a steeper SS over a conventional TFET.

### 3.3.1.5 Heterojunction TFET

Another TFET structure proposed for achieving a higher ON-current is the heterojunction TFET [20], made up of III-V materials. Figure 3.26(a) shows the structure of a heterojunction TFET. It consists of $\mathrm{GaAs}_{0.35} \mathrm{Sb}_{0.65}$ as the source material


Figure 3.25 Schematic of (a) planar, (b) partially raised and (c) fully raised Ge-source TFETs. Dominant directions of tunnelling are shown by arrows [19].
and $\mathrm{In}_{0.7} \mathrm{Ga}_{0.3} \mathrm{As}$ as the channel and drain material, giving us a heteromaterial junction at the source- channel interface. Figure 3.26(b) shows the band diagram of such a device at the surface. As can be seen in Figure 3.26(b), the band diagram of a heterojunction is staggered at the source-channel junction. This leads to a shorter tunnelling width, thus leading to an increased ON-current. Also, III-V materials are direct bandgap materials and have a bandgap smaller than Si. This also contributes to the increased ON -state current in heterojunction TFETs.

### 3.3.1.6 Ferroelectric TFET

A structure proposed for improving the SS and the ON -state current of a TFET is the ferroelectric TFET. In this structure a ferroelectric material in used in the gate stack as shown in Figure 3.27. When a gate voltage is applied, the polarisation of the ferroelectric material in the gate stack increases the effective gate voltage seen by the channel [21]. In other words, the vertical electric field across the gate oxide increases, thus causing more inversion on the channel for a particular value of


Figure 3.26 (a) Schematic view of an n-channel heterojunction TFET. (b) Band diagram of the heterojunction TFET shown in (a) [20]. Source: Reproduced with permission of IEEE.


Figure 3.27 Schematic view of a p-channel ferroelectric TFET.
applied gate voltage. Thus, the steepness of the $\mathrm{I}_{\mathrm{OFF}}$ to $\mathrm{I}_{\mathrm{ON}}$ transition increases, improving the SS and the ON -current. Figure 3.28 gives a qualitative description of the transfer characteristics of a ferroelectric TFET and compares them with that of a MOSFET and a conventional TFET.

### 3.3.2 Three-dimensional TFETs

A three-dimensional TFET is a device in which the current-carrying surface extends in all three dimensions. Two of the most important three-dimensional structures are the gate all around nanowire TFET and the tri-gate TFET.


Figure 3.28 Comparison of the $I_{D}-V_{G S}$ curves of a ferroelectric TFET with that of a MOSFET and a conventional TFET [21].


Figure 3.29 Schematic view of a gate all around (GAA) nanowire TFET.

### 3.3.2.1 Gate all around nanowire TFET

The increased gate control observed in a double gate TFET is further improved in a gate all around (GAA) TFET [22-28]. Figure 3.29 shows the structure of a gate all around nanowire TFET. It consists of a silicon nanowire of radius 10 nm , surrounded by a gate oxide of thickness 2 nm , covered all around by the gate metal. Such a structure can achieve a high level of electrostatic control of the gate, as all the field lines originating from the drain can terminate at the gate, without significant penetration into the channel. This leads to a steeper subthreshold slope and diminished short channel effects such as DIBL and threshold voltage roll-off. It also enhances the ON-state current, as this geometry provides a large area for the current to flow as compared to a planar device.


Figure 3.30 Schematic view of a tri-gate (fin) TFET.

### 3.3.2.2 Tri-gate/fin TFET

The fin FET or tri-gate FET is a very popular architecture in MOSFETs, which has been used in commercial production by Intel in its 22 nm technology. Hence, a trigate or fin TFET (Figure 3.30) becomes an important structure to study [29]. Once again, this structure leads to an increased gate control over the channel and an increased area of current flow. This leads to a higher ON-state current, a steeper subthreshold slope and diminished short channel effects. These improvements provided by the tri-gate TFET are lesser than those provided by the GAA nanowire TFET, but fabrication of a tri-gate TFET is comparatively simpler. Moreover, as tri-gate MOSFETs are already used in commercial production, there is a greater chance for commercial production of tri-gate TFETs.

### 3.3.3 Carbon nanotube and graphene TFETs

The primary motivation behind the design of TFETs is to fabricate a device that exhibits a sub $60 \mathrm{mV} /$ decade subthreshold swing over a wide range of $V_{G S}$, and having an ON-state current that is of the order of $1-10 \mu \mathrm{~A} / \mu \mathrm{m}$. It is difficult to realise silicon TFETs that consistently exhibit an ON-current of the order of silicon MOSFETs used in CMOS technology, while also having a subthreshold swing of sub- $60 \mathrm{mV} /$ decade. While ideally simulated devices exhibit a low subthreshold swing (SS) for high ON-state currents, the experimental demonstration of the same has been limited. Si and Ge TFETs are not able to achieve a high


Figure 3.31 (a) Point tunnelling and (b) line tunnelling in a TFET.

ON-state current due to their indirect bandgap. III-V semiconductors are predicted to have high ON-state currents since they have direct bandgaps, yet the experimental realisations of such TFETs do not yet show low SS for a large range of $V_{G S}$. Two-dimensional materials are the most promising candidates for the fabrication of such devices. A lack of dangling surface bonds in such materials contributes to a higher degree of gate control, and their high mobilities and low bandgaps, especially in the case of graphene, are promising for both high ON-state and low SS applications. Due to a combination of these factors, carbon nanotube (CNT) TFETs and graphene nanoribbon TFETs are theoretically predicted to have the best performance at low gate lengths. In general, the fabrication of TFETs exhibits a trade-off between high ON-state currents and low SS [30].

### 3.3.4 Point versus line tunnelling in TFETs

The most general problem exhibited by TFETs is their low ON-state current. The ON-state current of a conventional Si-based TFET is of the order of $10^{-6} \mathrm{~A} / \mu \mathrm{m}$. To boost the ON -state current of a TFET, one of the most common approaches is to increase the effective volume of the tunnelling region. In a conventional TFET structure (shown in Figure 3.31(a), the tunnelling of carriers takes place in a small region at the surface near the source-channel junction, in the direction along the channel. This kind of tunnelling at the source-channel junction is known as point
tunnelling. One of the ways to increase this volume of tunnelling region is to overlap the gate over the source region and make the carriers tunnel in the vertical direction towards the gate [31-33] (shown in Figure 3.31(b)). This technique increases the effective volume of the tunnelling region, thus increasing the drain current. The carriers due to the high vertical electrical field of the gate tunnel within the source region are then swept away towards the drain due to the lateral electric field of the drain bias. This structural configuration of tunnelling is known as line tunnelling. One example of a line tunnelling FET that you are already familiar with is the raised-Ge source TFET, discussed in Section 3.3.1.4. All the other TFETs discussed in Section 3.3 are point tunnelling TFETs.

### 3.4 Other steep subthreshold transistors

In the quest of finding alternatives for highly scaled MOSFETs, a large number of steep subthreshold devices have been explored. Some of these devices include impact-ionisation MOS (I-MOS) [34-38], MOSFETs with nanoelectromechanical gate electrodes (NEMFETs) [39-41], nanoelectromechanical (NEM) relays [42, 43], ferroelectric FETs [44, 45] and feedback FETs [46]. All these devices are capable of achieving a subthreshold swing below $60 \mathrm{mV} /$ decade at room temperature. However, most of these devices exhibit a low SS only for a small range of gate voltages and not for the entire subthreshold region. I-MOS, NEMFETs and NEM relays have a steep SS only in a local region and their average SS is much degraded. Moreover, NEMFETs and NEM relays have an ON-state current lower than the conventional TFET. Devices such as I-MOS are not suitable for low-voltage applications since they need a supply voltage more than 1.0 V . Further, feedback FETs and ferroelectric FETs exhibit a degraded output resistance [47]. Also, all of these devices require a complex process of fabrication and, hence, suffer from both yield and reliability issues.

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## 4

## Drain current modelling of tunnel FET: the task and its challenges

### 4.1 Introduction

In a MOSFET, free carriers from the source cross the source-channel barrier on application of drain voltage, whereas in a TFET, free carriers are generated due to band-to-band tunnelling across the source-channel junction, which are then swept towards the drain due to the applied drain electric field. As discussed in the first three chapters, since quantum mechanical effects are at the basis of the operation of a TFET, its modelling approach inherently differs from that of a MOSFET. The approaches to modelling TFETs, and quantum devices in general, can be divided into two broad categories - atomistic modelling and analytical modelling. In the case of TFETs, most atomistic modelling derives its basis from the non-quilibrium Green function (NEGF) approach. This methodology is far more accurate and powerful, especially at small device dimensions, when quantum effects become far more prominent. The first step in an NEGF approach usually involves the extraction of the band structure of the system using the density functional Theory (DFT)-based calculations. Subsequently, this band structure is used in conjunction with a ballistic quantum transport model to self-consistently solve for the

[^3]

Figure 4.1 Schematic depicting the depletion region and the inversion layer charge in the channel of a TFET.
wavefunctions, allowed energy states, energy values and so on. However, this approach also has its drawbacks. First, it is computationally intensive, and running an NEGF simulation can take months on a fast cluster of computers. On the other hand, many models (such as Schenk's model) use the NEGF approach as a first step, and subsequently apply various approximations to obtain a more computationally efficient model.

The other category of modelling uses an analytical approach, where known physics equations and theories are combined, approximated, and modified to give the final results of the device. The actual theories used may vary across different approaches, ranging in complexity, from the $\vec{k} \cdot \vec{p}$ method to solving approximated versions of Poisson's equation. The advantage of the analytical approach is twofold - it is less computationally intensive and gives a better understanding of the essential physical phenomenon behind the device functioning. This is the approach to modelling that will be explained in this book.

Figure 4.1 depicts the cross-sectional view of a TFET in the ON-state $\left(\left|V_{G S}\right|>V_{t h}\right.$ and $\left.\left|V_{D S}\right|>0\right)$ showing the depletion region and the inversion layer charge in the channel. It shows the existence of an inversion layer (i.e. the channel) at the surface (i.e. $\mathrm{Si}-\mathrm{SiO}_{2}$ interface) in the body. It also shows a depletion region between the source and the channel, which is due to a difference in the type of carriers in the source and in the channel. This depletion region has a high electric field, which leads to a potential drop greater than $E_{g} / q$ (where $E_{g}$ is the bandgap of the material) across the source and the channel leading to the tunnelling of carriers through the bandgap. Since the inversion layer is formed at the surface, tunnelling of carriers occurs only at the surface, as shown in Figure 4.1. As can be seen in different tunnelling models discussed in Chapter 2, the generation rate of carriers for band-to-band tunnelling is a function of the electric field. Hence, to calculate the drain current in a TFET, we first need to find the electric field at the surface in the tunnelling region (i.e. the source-channel depletion region), as shown in Figure 4.1. Once the potential profile is modelled accurately, the electric field can be calculated by taking its derivative. The challenge in modelling the surface potential in the tunnelling region comes from the two-dimensional nature of the


Figure 4.2 Electric field and along the surface of a TFET in the $x$ - and the $y$-directions.
electric field in this region. In the channel of a MOSFET and also in that of a TFET, we take the gradual channel approximation, which assumes the electric field in the $x$-direction to be negligible as compared to the electric field in the $y$-direction, and hence we have to solve the one-dimensional Poisson equation in the channel as given below:

$$
\begin{equation*}
\frac{\partial^{2} \psi}{\partial y^{2}}=\frac{q}{\varepsilon} \tag{4.1}
\end{equation*}
$$

where $\psi$ is the potential.
However, in the tunnelling region (depletion region shown in Figure 4.1), the gradual channel approximation is not valid, as can be seen in Figure 4.2 as the $x$ - and $y$-direction electric fields are comparable in magnitude. Hence, we have to solve the two-dimensional Poisson equation as given below:

$$
\begin{equation*}
\frac{\partial^{2} \psi}{\partial x^{2}}+\frac{\partial^{2} \psi}{\partial y^{2}}=\frac{q}{\varepsilon} \tag{4.2}
\end{equation*}
$$

Equation (4.2) cannot be integrated and solved as in the case of Equation (4.1), and hence different mathematical methods and approximations are needed to solve it.

The next step in the modelling of a TFET is finding the tunnelling generation rate using the surface potential obtained by solving Equation (4.2). The tunnelling
generation rate is calculated by using local or non-local models of tunnelling, as described in Chapter 1. A local model is a function of the local electric field and gives the tunnelling generation rate at a particular point. The surface potential can be differentiated and used in a local tunnelling model. A non-local model, on the other hand, uses the tunnelling length, which is the length over which the potential has a drop of $E_{g} / q$. Hence, a non-local model becomes a function of the surface potential. The next and the most challenging step is integrating this tunnelling rate over the volume of the tunnelling region. The expression for the tunnelling generation rate $G_{b t b}$ using Kane's model (local model) is given below:

$$
\begin{equation*}
G_{b t b}=A \frac{\left|E^{2}\right|}{\sqrt{E_{g}}} \exp \left[-B \frac{E_{g}^{3 / 2}}{|E|}\right] \tag{4.3}
\end{equation*}
$$

where $E$ is the electric field, $E_{g}$ is the bandgap and $A$ and $B$ are tunnelling parameters.

To find the drain current $I_{d}$, we have to integrate the tunnelling generation rate over the volume of the tunnelling region:

$$
\begin{equation*}
I_{d}=q \int G_{b t b} d V \tag{4.4}
\end{equation*}
$$

The expression for $G_{b t b}$ in Equation (4.3) does not have a closed form integration as it involves both linear and exponential terms in the electric field. Hence, this integration is the biggest challenge in modelling the drain current in tunnel FETs and various approximations and assumptions are needed to be used to obtain an expression for the drain current. Modelling the drain current in a TFET mainly involves two steps: (i) solving for the surface potential and (ii) finding the tunnelling generation rate and integrating it over the tunnelling volume.

### 4.2 TFET modelling approach

In this section, using an elementary TFET model, we will describe the basic approach towards developing a drain current model for a TFET [1]. We take a p-channel SOI TFET with a high source and drain doping of $10^{21} / \mathrm{cm}^{3}$, as shown in Figure 4.3. The device has a channel length of $L$, silicon film thickness of $T_{S i}$, oxide thickness $T_{o x}$ and a body doping of $N_{A}$. The device has a long channel (i.e. channel length $>50 \mathrm{~nm}$ ) and has a thin silicon film ( $T_{S i} \sim 10 \mathrm{~nm}$ ) such that the body is fully depleted under the influence of a gate voltage. To model the TFET, first we have to solve for the surface potential of the device. Therefore, let us begin by observing the simulation results for the surface potential of the TFET and an equivalent MOSFET, as shown in Figure 4.4. It can be seen from this figure that


Figure 4.3 Schematic view of the TFET.


Figure 4.4 Comparison between the surface potential of the TFET shown in Figure 4.3 and that of an equivalent MOSFET.
the surface potential in the channel of the TFET is almost constant and varies rapidly in the source-channel junction. Let us assume the value of this constant potential in the channel to be $\psi_{C}$. Hence, we need to find the value of $\psi_{C}$ and model the surface potential in the source channel junction.

### 4.2.1 Finding the value of $\boldsymbol{\psi}_{\boldsymbol{C}}$

As can be seen in Figure 4.4, the value of $\psi_{C}$ is equal to the surface potential of the equivalent MOSFET at the drain end. Hence, we get the following expression for $\psi_{C}$ :

$$
\begin{gather*}
\psi_{C}=\psi_{B}+V_{D S} \quad \text { for } V_{D S}<\left|V_{G S}-V_{T h}\right| \text { in the linear region }  \tag{4.5}\\
\psi_{C}=\psi_{B}+\left|V_{G S}-V_{t h}\right| \quad \text { for } V_{D S}<\left|V_{G S}-V_{T h}\right| \text { in the saturation region } \tag{4.6}
\end{gather*}
$$



Figure 4.5 Potential profile along the y-direction of the TFET.
where $\psi_{B}$ is the built-in potential of the channel, which is the sum of the band bending in the body and drop across the buried oxide (Figure 4.5).

### 4.2.2 Modelling the surface potential in the source-channel junction

Now we need to model the surface potential in the depletion region at the source-channel junction (i.e. the tunnelling region), by solving the two- dimensional Poisson equation (4.3). Using a parabolic approximation for the potential in the $y$-direction (also known as the pseudo-2D method, which will be described in detail in the next chapter) and the $y$-direction boundary conditions (shown in Figure 4.6).

The potential at the $\mathrm{Si}-\mathrm{SiO}_{2}$ interface is equal to the surface potential:

$$
\begin{equation*}
\psi(x, 0)=\psi_{S}(x) \tag{4.7}
\end{equation*}
$$

The electric field at the Si-buried oxide interface is equal to zero:

$$
\begin{equation*}
E_{y}\left(x, T_{S i}\right)=0 \tag{4.8}
\end{equation*}
$$

The electric field displacement $(D)$ is continuous across the $\mathrm{Si-} \mathrm{SiO}_{2}$ interface:

$$
\begin{equation*}
E_{y}(x, 0)=-C_{o x}\left(\psi_{G}-\psi_{S}(x)\right) / \varepsilon_{S i} \tag{4.9}
\end{equation*}
$$

Using the boundary conditions given by Equations (4.7) to (4.9), we get the following general form solution for the surface potential using the twodimensional Poisson equation:


Figure 4.6 Potential profile along the y-direction in a TFET in the body region showing the boundary conditions.

$$
\begin{gather*}
\psi_{S}(x)=C \exp \left(\frac{x-x_{i}}{L_{d}}\right)+D \exp \left(\frac{-\left(x-x_{i}\right)}{L_{d}}\right)+\psi_{G}  \tag{4.10}\\
L_{d}=\sqrt{T_{S i} T_{a x} \varepsilon_{S i} / \varepsilon_{o x}} \tag{4.11}
\end{gather*}
$$

Equation (4.10) has three unknowns ( $C, D$ and $x_{i}$ ), which can be calculated using the boundary conditions in the $x$-direction (shown in Figure 4.7). Note that for modelling the surface potential in the tunnelling region, $x=0$ is taken at the source-channel junction.

The surface potential at $x=x_{i}$ is equal to $\psi_{C}$ :

$$
\begin{equation*}
\psi_{S}\left(x_{i}\right)=\psi_{C} \tag{4.12}
\end{equation*}
$$

The electric field at $\left(E_{x}\right) x=x_{i}$ is equal to zero.

$$
\begin{equation*}
\frac{\partial \psi_{S}\left(x_{i}\right)}{\partial x}=0 \tag{4.13}
\end{equation*}
$$

The surface potential at the source is equal to the built-in potential between the source and the body, $V_{b i}=k t / q \ln \left(N_{\text {Source }} N_{\text {body }} / n_{i}^{2}\right)$, so

$$
\begin{equation*}
\psi_{S}(0)=V_{b i} \tag{4.14}
\end{equation*}
$$

Using Equations (4.12) and (4.13) we get $C$ and $D$ :

$$
\begin{equation*}
C=D=\left(\psi_{C}-\psi_{G}\right) / 2 \tag{4.15}
\end{equation*}
$$



Figure 4.7 Potential profile at the surface along the $x$-direction of a TFET, showing the boundary conditions.

Using Equation (4.14) we get the following value of $x_{i}$ :

$$
\begin{equation*}
x_{i}=L_{d} \cosh ^{-1}\left[V_{b i} /\left(\psi_{C}-\psi_{G}\right) / 2\right] \tag{4.16}
\end{equation*}
$$

Finally, we get the following expression for the surface potential:

$$
\begin{equation*}
\psi_{S}(x)=\left(\psi_{C}-\psi_{G}\right) \cosh \left(\frac{\left.x-L_{d} \cosh ^{-1}\left(V_{b i} /\left(\psi_{C}-\psi_{G}\right)\right)\right)}{L_{d}}\right)+\psi_{G} \tag{4.17}
\end{equation*}
$$

This entire method of finding the surface potential giving us Equation (4.17) is known as the pseudo-2D model and is described in detail in the next chapter.

### 4.2.3 Finding the tunnelling current

Now, using the expression for surface potential given in Equation (4.17), we will find the drain current of the device. For this, we first need to find the tunnelling generation rate using Equation (4.3) and then integrate it. Figure 4.8 shows the variation in the tunnelling generation rate along the length of the device. As $G_{b t b}$ is an exponential function of the electric field, it falls sharply as we move away from the source-channel junction, where the electric field is the highest. Hence, in this model, we find this highest generation rate and multiply it by a constant factor to find the drain current. We begin with finding the minimum tunnelling length, which is the minimum length across which we get a potential drop of $E_{g} / q$, as shown in Figure 4.9 Since the curve of surface potential is steepest near the source


Figure 4.8 Tunnelling generation rate $\left(G_{b t b}\right)$ at the surface, along the $x$ direction of a TFET.


Figure 4.9 Surface potential profile at the surface, along the $x$-direction of a TFET, showing the shortest tunnelling length.
(as can be seen in Figure 4.6), the minimum tunnelling length $\left(L_{T W}\right)$ will be given by the distance from the source at which the surface potential falls by $E_{g} / q$, as described by the equation below:

$$
\begin{equation*}
L_{T W}=x\left(\psi_{S}(0)-E_{g} / q\right)-x\left(\psi_{S}(0)\right) \tag{4.18}
\end{equation*}
$$

Using $L_{T W}$ we find the average electric field over the minimum tunnelling length $\left(E_{T W}\right)$ :

$$
\begin{equation*}
E_{T W}=E_{g} / q L_{T W} \tag{4.19}
\end{equation*}
$$

Now we use the average electric field over the minimum tunnelling length ( $E_{T W}$ ) to find the tunnelling generation rate at the source-channel interface, using Kane's model for band-to-band tunnelling (Equation (4.3)), as given below:

$$
\begin{equation*}
G_{b t b}(0)=A \frac{\left|E_{T W}^{2}\right|}{\sqrt{E_{g}}} \exp \left[-B \frac{E_{g}^{3 / 2}}{\left|E_{T W}\right|}\right] \tag{4.20}
\end{equation*}
$$

Kane's model is a local tunnelling model and the above generation rate is what we obtain by using the average electric field along the minimum tunnelling length in Kane's model. Now, since $G_{b t b}$ is an exponential function of the electric field, we assume the generation rate $G_{b t b}(0)$, given by Equation (4.20) as the dominant generation rate, as it is obtained by using the highest electric field present in the device. Hence, to obtain the drain current, we multiply this generation rate ( $G_{\text {btb }}(0)$ ) by a constant $A_{V}$, which accounts for the volume over which the generation of carriers by tunnelling is taking place (i.e. the volume of tunnelling region). This gives the following equation for the drain current:

$$
\begin{equation*}
I_{D}=A_{V} A \frac{\left|E_{T W}^{2}\right|}{\sqrt{E_{g}}} \exp \left[-B \frac{E_{g}^{3 / 2}}{\left|E_{T W}\right|}\right] \tag{4.21}
\end{equation*}
$$

The parameter $A_{V}$ has no analytical form and has to be extracted from experimental or simulated characteristics.

### 4.3 MOSFET modelling approach

The TFET modelling approach described in the previous sections is based on the surface potential of the device and is known as the surface potential-based modelling approach. MOSFET models, however, are based on two types of approaches. The charge-based approach and the surface potential-based approach. The basic textbook model that everybody is familiar with is a charge-based model, which gives us the following equation for the drain current $I_{d}$ in the linear region [2]:

$$
\begin{equation*}
I_{D}=\frac{\mu_{n} C_{o x} W}{L}\left(\left(V_{G S}-V_{t h}\right) V_{D S}-\frac{V_{D S}{ }^{2}}{2}\right) \tag{4.22}
\end{equation*}
$$

where $\mu_{n}$ is the electron mobility, $C_{o x}$ is oxide capacitance, $W$ is the channel width and $L$ is the channel length. In a charge-based modelling approach, the channel charge density is calculated first, using the gate capacitance, and then the drain current is calculated by using the drift and diffusion equations on the channel charge.

Another approach for modelling a MOSFET is the surface potential-based approach. In this approach, we solve Poisson's equation in the channel given by the following equation and then find the charge density in the channel region using the surface potential:

$$
\begin{equation*}
\frac{\partial^{2} \psi(x, y)}{\partial x^{2}}+\frac{\partial^{2} \psi(x, y)}{\partial y^{2}}=\frac{q}{\varepsilon}\left(N_{S U B}+n(x, y)-p(x, y)\right) \tag{4.23}
\end{equation*}
$$

where $\psi$ is the potential, $N_{S U B}$ is the body doping and $n$ and $p$ are electron and hole concentrations, respectively.

However, since the gradual channel approximation holds in the channel of a MOSFET, Equation (4.23) is simplified into a one-dimensional Poisson equation as given by

$$
\begin{equation*}
\frac{\partial^{2} \psi(x, y)}{\partial y^{2}}=\frac{q}{\varepsilon}\left(N_{S U B}+n(x, y)-p(x, y)\right) \tag{4.24}
\end{equation*}
$$

For an n-channel MOSFET we can neglect the $p(x, y)$ term in Equation (4.24) and vice versa for a p-channel MOSFET.

After solving for the surface potential using Poisson's equation (4.24), we find the channel charge. Once the channel charge $q_{i}$ is known, we apply the drift diffusion equations on the channel charge to find the drain current $I_{d}$ :

$$
\begin{equation*}
I_{d}=W C_{o x} \int_{0}^{V_{D S}} \mu q_{i} d V \tag{4.25}
\end{equation*}
$$

which can be rewritten in terms of surface potential $\psi_{S}$ as

$$
\begin{equation*}
I_{d}=W C_{o x} \int_{x=0}^{x=L} \mu\left[\phi_{i} \frac{d q_{i}}{d x}-q_{i} \frac{d \psi_{s}}{d x}\right] d x \tag{4.26}
\end{equation*}
$$

The current is dominated by diffusion of the charges in the subthreshold region and by the drift of charges in the strong inversion region.

In summary, modelling of a MOSFET using the surface potential approach involves two steps: (i) solving the simplified one-dimensional Poisson equation for finding the surface potential and (ii) using the surface potential for calculating
the inversion charge and then using the drift diffusion equation for finding the drain current.

## References

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## 5

## Modelling the surface potential in TFETs

By now you are familiar with the basic approach of modelling a TFET, as shown in Figure 5.1. In the previous chapter, we identified the two main steps in the modelling of TFETs. In this chapter, we will discuss the first step, that is solving for the surface potential, in detail. In the next chapter, we will be covering the second step, that is finding the tunnelling generation rate and integrating it over the tunnelling volume.

To model the surface potential, let us write the two-dimensional Poisson equation in the channel of the TFET:

$$
\begin{equation*}
\frac{\partial^{2} \psi(x, y)}{\partial x^{2}}+\frac{\partial^{2} \psi(x, y)}{\partial y^{2}}=\frac{q N_{A}}{\varepsilon_{S i}} \tag{5.1}
\end{equation*}
$$

where $N_{A}$ is the body doping of the TFET. Equation (5.1) does not include the mobile inversion charge in the channel. The solution to Equation (5.1) will give us the surface potential. Equation (5.1) is a second-order differential equation in two variables and, hence, it cannot be integrated in a straightforward way. Solving this equation is a major challenge in modelling a TFET. Hence, in this chapter, we describe various approaches and methods that deal with solving Equation (5.1) and finding the surface potential in a TFET. These approaches involve various approximations, assumptions, simplifications and mathematical techniques to solve the two-dimensional (2D) Poisson equation.

[^4]

Figure 5.1 Schematic view of a TFET.

### 5.1 The pseudo-2D method

The Pseoudo-2D method, also known as the parabolic approximation method, is one of the most widely used methods for modelling the surface potential in a TFET. This method essentially simplifies the 2D Poisson equation (5.1) into a sec-ond-order 1D linear differential equation, which is then solved using basic mathematical techniques. The method was first suggested by K. K. Young [1] and has since then become popular for modelling TFETs and for studying short channel effects (SCEs) in MOSFETs [2, 3]. In this section, we will describe the pseudo-2D method for solving the surface potential of a TFET [4-19].

### 5.1.1 Parabolic approximation of potential distribution

Let us first take a look at the potential distribution in a TFET in the $x$ - and $y$-directions as given in Figures 5.2 and 5.3, respectively. As can be seen in Figure 5.3, the potential distribution at any $x=x_{i}$ along the $y$-direction (i.e. $\psi(x, y)$ ) is monotonous in nature and can be approximated by a polynomial function, as given below:

$$
\begin{equation*}
\psi\left(x_{i}, y\right)=a_{0}+a_{1} y+a_{2} y^{2}+a_{3} y^{3} \tag{5.2}
\end{equation*}
$$

where $a_{0}, a_{1}, a_{2}$, etc., are coefficients of the polynomial in $y$. At each value of $x$ (i.e. $x=x_{i}$ ), we have a different set of polynomial coefficients $a_{0}(x), a_{1}(x), a_{2}(x)$, etc. Hence, Equation (5.2) becomes

$$
\begin{equation*}
\psi(x, y)=a_{0}(x)+a_{1}(x) y+a_{2}(x) y^{2}+a_{3}(x) y^{3} \tag{5.3}
\end{equation*}
$$

To evaluate $\psi(x, y)$, we have to first find the polynomial coefficients $\left(a_{0}(x)\right.$, $\left.a_{1}(x), a_{2}(x), \ldots\right)$. The coefficients can be found by making use of the following boundary conditions in the $y$-direction.


Figure 5.2 Potential distribution along the $x$-direction of a TFET at the surface.


Figure 5.3 Potential distribution along the y-direction of a TFET.
(i) The electric field at the Si-buried oxide interface (i.e. $x=t_{S i}$ ) is equal to zero, which gives us

$$
\begin{equation*}
\left.\frac{\partial \psi(x, y)}{\partial y}\right|_{y=t_{s i}}=0 \tag{5.4}
\end{equation*}
$$

(ii) The potential at the $\mathrm{Si}-\mathrm{SiO}_{2}$ interface (i.e. $x=0$ ) is equal to the surface potential $\left(\psi_{S}(x)\right)$, which gives us:

$$
\begin{equation*}
\psi(x, 0)=\psi_{S}(x) \tag{5.5}
\end{equation*}
$$

(iii) The electric field displacement is continuous across the $\mathrm{Si}-\mathrm{SiO}_{2}$ interface (i.e. $x=0$ ), which gives us

$$
\begin{equation*}
\left.\frac{\partial \psi(x, y)}{\partial y}\right|_{y=0}=\frac{-C_{o x}\left(\psi_{G}-\psi_{S}(x)\right)}{\varepsilon_{S i}} \tag{5.6}
\end{equation*}
$$

where $C_{o x}$ is the gate oxide capacitance per unit area and $\psi_{G}$ is the gate potential. The gate potential $\psi_{G}$ is given by

$$
\begin{equation*}
\psi_{G}=V_{G S}-V_{F B} \tag{5.7}
\end{equation*}
$$

where $V_{F B}$ is the flat-band voltage and is dependent on the work function of the gate.

Finally, there are three boundary conditions known to us, in the $y$-direction, as given by Equations (5.4) to (5.6). Using these boundary conditions, we can only evaluate the first three polynomial coefficients in Equation (5.3). We have to ignore the higher-order terms (i.e. terms $y^{3}, y^{4}, y^{5}$ and so on), which gives us the following form of Equation (5.3):

$$
\begin{equation*}
\psi(x, y)=a_{0}(x)+a_{1}(x) y+a_{2}(x) y^{2} \tag{5.8}
\end{equation*}
$$

Hence, this method for solving the surface potential is known as the parabolic approximation approach. Here we are approximating the potential distribution in the $y$-direction by using a parabolic (i.e. a second-order polynomial) expression (5.8). We will now find the coefficients $a_{0}(x), a_{1}(x)$ and $a_{2}(x)$ using the boundary conditions (5.4) to (5.6).

Substituting Equation (5.8) into Equation (5.4) we get

$$
\begin{equation*}
a_{1}(x) y+2 a_{2}(x) t_{S i}=0 \tag{5.9}
\end{equation*}
$$

Substituting Equation (5.8) into Equation (5.5) we get

$$
\begin{equation*}
a_{0}(x)=\psi_{S}(x) \tag{5.10}
\end{equation*}
$$

Finally, substituting Equation (5.8) into Equation (5.6) we get

$$
\begin{equation*}
a_{1}(x)=\frac{-C_{o x}\left(\psi_{G}-\psi_{S}(x)\right)}{\varepsilon_{S i}} \tag{5.11}
\end{equation*}
$$

Equations (5.10) and (5.11) give us $a_{0}(x)$ and $a_{1}(x)$, respectively. Using Equation (5.11) in (5.9) gives us $a_{2}(x)$.

$$
\begin{equation*}
a_{2}(x)=\frac{C_{o x}\left(\psi_{G}-\psi_{S}(x)\right)}{2 t_{S i} \varepsilon_{S i}} \tag{5.12}
\end{equation*}
$$

Now, by using Equations (5.10) to (5.12), Equation (5.8) becomes

$$
\begin{equation*}
\psi(x, y)=\psi_{S}(x)+\frac{-C_{o x}\left(\psi_{G}-\psi_{S}(x)\right)}{\varepsilon_{S i}} y+\frac{C_{o x}\left(\psi_{G}-\psi_{S}(x)\right)}{2 t_{S i} \varepsilon_{S i}} y^{2} \tag{5.13}
\end{equation*}
$$

Equation (5.13) gives the complete form of the parabolic approximation of potential in the $y$-direction.

### 5.1.2 Solving the 2D Poisson equation using parabolic approximation

Using Equation (5.13) in Equation (5.1) and solving at $y=0$ simplifies the 2D Poisson equation (5.1) into the following second-order 1D linear differential equation in the surface potential, as given below:

$$
\begin{equation*}
\frac{\partial^{2} \psi_{S}(x)}{\partial x^{2}}-\frac{C_{o x}}{t_{S i} \varepsilon_{S i}} \psi_{S}(x)=\frac{q N_{A}}{\varepsilon_{S i}}-\frac{C_{o x}}{t_{S i} \varepsilon_{S i}} \psi_{G} \tag{5.14}
\end{equation*}
$$

The homogenous part of Equation (5.14) is

$$
\begin{equation*}
\frac{\partial^{2} \psi_{S}(x)}{\partial x^{2}}-\frac{C_{o x}}{t_{S i} \varepsilon_{S i}} \psi_{S}(x)=0 \tag{5.15}
\end{equation*}
$$

The general form solution of Equation (5.15) is

$$
\begin{equation*}
\psi_{S}(x)=C \exp \left(\frac{x}{L_{d}}\right)+D \exp \left(\frac{-x}{L_{d}}\right) \tag{5.16}
\end{equation*}
$$

where the characteristic length is

$$
\begin{equation*}
L_{d}=\sqrt{t_{S i} \varepsilon_{S i} / C_{o x}}=\sqrt{t_{S i} t_{o x} \varepsilon_{S i} / \varepsilon_{o x}} \tag{5.17}
\end{equation*}
$$

A particular solution of Equation (5.14) is

$$
\begin{equation*}
\psi_{S}(x)=\psi_{G}-\frac{q N_{A} L_{d}^{2}}{\varepsilon_{S i}} \tag{5.18}
\end{equation*}
$$

We get the final solution to Equation (5.14) by adding the homogenous solution and the particular solution, which is given by

$$
\begin{equation*}
\psi_{S}(x)=C \exp \left(\frac{x}{L_{d}}\right)+D \exp \left(\frac{-x}{L_{d}}\right)+\psi_{G}-\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}} \tag{5.19}
\end{equation*}
$$

Equation (5.19) gives us the general form solution for the surface potential of a TFET, but our task is not yet over. Equation (5.19) has two unknown coefficients ( $C$ and $D$ ). To find these coefficients, we make use of the $x$-direction boundary conditions.

### 5.1.3 Solution for the surface potential

Let us look at the surface potential distribution of a TFET as given in Figure 5.4. We can divide the body of the TFET into two regions, region R1 (the tunnelling region) and region R2 (the channel region), as shown in Figure 5.4. As can be seen, in the channel region the surface potential is almost constant. This is because the $x$-direction electric field in the tunnelling region is high and the $x$-direction electric field in the channel region is small. Let us define this constant potential as $\psi_{C}$. The value of $\psi_{C}$ can be calculated by using the similarity in the drain sides of a MOSFET and a TFET. As can be seen in Figure 5.4, the value of $\psi_{C}$ is equal to the surface potential of the equivalent MOSFET at the drain end, which gives the following expressions for $\psi_{C}$ :

$$
\begin{gather*}
\psi_{C}=\psi_{B}+V_{D S} \text { for } V_{D S}<\left|V_{G S}-V_{T h}\right| \text { in the linear region }  \tag{5.20}\\
\psi_{C}=\psi_{B}+\left|V_{G S}-V_{t h}\right| \text { for } V_{D S}>\left|V_{G S}-V_{T h}\right| \text { in the saturation region } \tag{5.21}
\end{gather*}
$$



Figure 5.4 Surface potential along the channel of a TFET.


Figure 5.5 Potential along the depth (y-direction) of a TFET.
where $\psi_{B}$ is the built-in potential of the channel, which is the sum of the band bending in the body and drop across the buried oxide (Figure 5.5) and $V_{t h}$ is the threshold voltage of a MOSFET of an equivalent structure. This gives us the value of the surface potential in region R 2 . Also, the value of the surface potential at the boundary of region R 1 and region R 2 is $\psi_{C}$, which is our first $x$-direction boundary condition:

$$
\begin{equation*}
\psi_{S}\left(x_{i}\right)=\psi_{C} \tag{5.22}
\end{equation*}
$$

where $x_{i}$ is the position of the boundary between the region R 1 and region R 2 (also the length of region R1), the value of which is unknown to us at this point.

Our second $x$-direction boundary condition comes from the fact that the electric field at $x=x_{i}$ is zero, as can be seen in Figure 5.4:

$$
\begin{equation*}
\frac{\partial \psi_{S}\left(x_{i}\right)}{\partial x}=0 \tag{5.23}
\end{equation*}
$$

The third $x$-direction boundary condition is given by the value of the surface potential at the source-body interface, which is equal to the built-in potential between the source and the body $\left(V_{b i}\right)$ :

$$
\begin{equation*}
\psi_{S}(0)=\psi_{s r c}=V_{S}+V_{b i}=V_{b i} \tag{5.24}
\end{equation*}
$$

where $\psi_{s r c}$ is the source potential and $V_{S}$ is the source voltage, which is taken to be zero. Here

$$
\begin{equation*}
V_{b i}=\frac{k T}{q} \ln \left(\frac{N_{\text {Source }} N_{A}}{n_{i}^{2}}\right) \tag{5.25}
\end{equation*}
$$

Equations (5.22) to (5.24) give us three $x$-direction boundary conditions. Now, using these three boundary conditions, we will find the three unknowns (i.e. $C, D$ and $x_{i}$ ), but before that we will make a small change in the form of Equation (5.19), as given below:

$$
\begin{equation*}
\psi_{S}(x)=C \exp \left(\frac{\left(x-x_{i}\right)}{L_{d}}\right)+D \exp \left(\frac{\left(x-x_{i}\right)}{L_{d}}\right)+\psi_{G}-\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}} \tag{5.26}
\end{equation*}
$$

In Equation (5.26), we have shifted the centre of the exponential to $x=x_{i}$, which will help us to obtain a simpler solution for the surface potential. Please note that the general form of Equation (5.26) is the same as that of Equation (5.19). Only the expressions of the coefficients $C$ and $D$ would be simpler for Equation (5.26) as compared to Equation (5.19). This will become clear later, as we proceed with the solution.

Now, using Equation (5.26) in Equation (5.23) will give us

$$
\begin{align*}
& C-D=0  \tag{5.27}\\
& \Rightarrow C=D \tag{5.28}
\end{align*}
$$

Hence, Equation (4.26) can be rewritten as

$$
\begin{equation*}
\psi_{S}(x)=C \cosh \left(\frac{\left(x-x_{i}\right)}{L_{d}}\right)+\psi_{G}-\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}} \tag{5.29}
\end{equation*}
$$

The above simplification would not have been possible if we had not shifted the centre of the exponential of Equation (5.19) to $x=x_{i}$.

Using Equation (5.29) in Equation (5.22) gives us

$$
\begin{equation*}
C=\left(\psi_{C}-\left(\psi_{G}-\frac{q N_{A} L_{d}^{2}}{\varepsilon_{S i}}\right)\right) \tag{5.30}
\end{equation*}
$$

Typically the body doping $N_{A}$ is very low in a TFET and, hence, the term $q N_{A} L_{d}^{2} / \varepsilon_{S i}$ in Equation (5.30) becomes negligible as compared to the other terms. This gives us

$$
\begin{equation*}
C=\left(\psi_{C}-\psi_{G}\right) \tag{5.31}
\end{equation*}
$$

Using Equation (5.29) in Equation (5.24) we get

$$
\begin{equation*}
x_{i}=L_{d} \cosh ^{-1}\left[V_{b i} /\left(\psi_{C}-\psi_{G}\right)\right] \tag{5.32}
\end{equation*}
$$

Finally, we have the values of all three unknown parameters ( $C, D$ and $x_{i}$ ). We can now write the final expression for the surface potential of the TFET, as follows:

$$
\begin{equation*}
\psi_{S}(x)=\left(\psi_{C}-\psi_{G}\right) \cosh \left(\frac{\left.x-\left(L_{d} \cosh ^{-1}\left(V_{b i} / \psi_{C}-\psi_{G}\right)\right)\right)}{L_{d}}\right)+\psi_{G} \text { in region R1 } \tag{5.33}
\end{equation*}
$$

$$
\begin{equation*}
\psi_{S}(x)=\psi_{C} \text { in region } \mathrm{R} 2 \tag{5.34}
\end{equation*}
$$

Equations (5.33) and (5.34) give the final expression for the surface potential of a TFET using the pseudo-2D model. It includes the dependence of gate and drain bias voltages through the factors $\psi_{G}$ and $\psi_{C}$, respectively.

The approach that we have discussed in this section does not include the effect of inversion charges formed in the channel region at high $V_{G S}$. We will now discuss a different method, known as the variational approach, which modifies the results obtained in this section to include the effect of inversion charges on the surface potential of a TFET.

### 5.2 The variational approach

The variational approach is a method for modelling the surface potential of a TFET, which incorporates the effect of the inversion charge unlike the pseudo2D approach. It uses the calculus of variations for solving the 2D Poisson equation, which transforms the process of solving the 2D Poisson equation (5.1) into the process of finding the extremum of the Lagrangian function [20, 21] given below:

$$
\begin{equation*}
L_{\psi}=\iint\left(\frac{\varepsilon}{2}\left[\left(\frac{\partial \psi}{\partial x}\right)^{2}+\left(\frac{\partial \psi}{\partial y}\right)^{2}\right]-\rho \psi\right) d x d y \tag{5.35}
\end{equation*}
$$

which gives us the solution for $\psi$.
This method starts with the basic solution for the surface potential obtained in Section 5.1.3 and incorporates the effect of the inversion charge on the surface potential. This is done by modelling the change in the characteristic length $\left(L_{d}\right)$ (5.17) by considering its dependence on $V_{G S}$. We will now describe this method in detail.

### 5.2.1 The variational form of Poisson's equation

In this section, we will illustrate how the problem of solving the 2D Poisson equation gets transformed into finding the extremum of the Lagrangian (5.35), using the calculus of variations and vector calculus.

Let us consider a differential equation of the following form:

$$
\begin{equation*}
A u=f ;(x, y) \in D \tag{5.36}
\end{equation*}
$$

where $A$ is an operator, $u$ is a variable, $f$ is a function and $D$ is the solution space.
The theory of calculus of variations states that [21] if the differential equation (5.36) has a solution for $u$, then it corresponds to finding the extremum (minimum or maximum) of the functional $I(u)$ :

$$
\begin{equation*}
I(u)=(A u, u)+2(u, f) \tag{5.37a}
\end{equation*}
$$

where the parenthesis operator is the scalar product of the two quantities defined as in the solution space of the functional $I(u)$. For two general functions $f$ and $g$, this operator is defined as

$$
\begin{equation*}
(f, g)=\iint_{D} f g d x d y \tag{5.37~b}
\end{equation*}
$$

Now let us consider the 2D Poisson equation:

$$
\begin{equation*}
\frac{\partial^{2} \psi(x, y)}{\partial x^{2}}+\frac{\partial^{2} \psi(x, y)}{\partial y^{2}}=\frac{-\rho(x, y)}{\varepsilon} ; \quad(x, y) \in D \tag{5.38}
\end{equation*}
$$

In this case we have

$$
\begin{gather*}
A=\frac{\partial^{2}}{\partial x^{2}}+\frac{\partial^{2}}{\partial y^{2}}  \tag{5.39a}\\
u=\psi  \tag{5.39b}\\
f=-\frac{\rho}{\epsilon} \tag{5.39c}
\end{gather*}
$$

Therefore,

$$
\begin{equation*}
I(u)=\iint \psi\left(\frac{\partial^{2} \psi}{\partial x^{2}}+\frac{\partial^{2} \psi}{\partial y^{2}}\right) d x d y+2 \iint \frac{\psi \rho}{\epsilon} d x d y=I_{1}+I_{2} \tag{5.40}
\end{equation*}
$$

Let us now use the 2D divergence theorem to simplify the integral $I_{1}$. The 2D divergence theorem states that

$$
\begin{equation*}
\oint F_{y} d x-F_{x} d y=\iint \frac{\partial F_{x}}{\partial x}+\frac{\partial F_{y}}{\partial y} d x d y \tag{5.41}
\end{equation*}
$$

where the contour of integration on the left-hand side is the surface of the domain where the problem is being solved. If we choose then we get

$$
\begin{gather*}
F_{x}=\psi \frac{\partial \psi}{\partial y} ; F_{y}=\psi \frac{\partial \psi}{\partial y}  \tag{5.42}\\
\frac{\partial F_{x}}{\partial x}=\left(\frac{\partial \psi^{2}}{\partial x^{2}}\right)+\psi \frac{\partial^{2} \psi}{\partial x^{2}}  \tag{5.43a}\\
\frac{\partial F_{y}}{\partial y}=\left(\frac{\partial \psi^{2}}{\partial y^{2}}\right)+\psi \frac{\partial^{2} \psi}{\partial y^{2}} \tag{5.43b}
\end{gather*}
$$

Substituting these values on the right-hand side of the 2D divergence theorem, we can write

$$
\begin{equation*}
\iint \psi\left(\frac{\partial^{2} \psi}{\partial x^{2}}+\frac{\partial^{2} \psi}{\partial y^{2}}\right) d x d y=\oint\left(\psi \frac{\partial \psi}{\partial y} d x-\psi \frac{\partial \psi}{\partial y} d y\right)-\iint\left(\left(\frac{\partial \psi}{\partial x}\right)^{2}+\left(\frac{\partial \psi}{\partial y}\right)^{2}\right) d x d y \tag{5.44}
\end{equation*}
$$

If we impose the boundary condition that, on the surface of our domain,

$$
\begin{equation*}
\frac{\partial \psi}{\partial y}=0=\frac{\partial \psi}{\partial x} \tag{5.45}
\end{equation*}
$$

then the contour integral in Equation (5.44) reduces to zero. In this case, we get

$$
\begin{equation*}
I_{1}=-\iint\left(\left(\frac{\partial \psi}{\partial x}\right)^{2}+\left(\frac{\partial \psi}{\partial y}\right)^{2}\right) d x d y \tag{5.46}
\end{equation*}
$$

which, if substituted in Equation (5.40), gives

$$
\begin{array}{r}
I(u)=\iint\left[2 \frac{\rho \psi}{\epsilon}-\left[\left(\frac{\partial \psi}{\partial x}\right)^{2}+\left(\frac{\partial \psi}{\partial y}\right)^{2}\right]\right] \\
d x d y=\iint\left[2 \frac{\rho \psi(x, y)}{\epsilon}-\left[\left(\frac{\partial \psi(x, y)}{\partial x}\right)^{2}+\left(\frac{\partial \psi(x, y)}{\partial y}\right)^{2}\right]\right] d x d y \tag{5.47}
\end{array}
$$

Finding the extremum of the integral in Equation (5.47) gives us the solution for $\psi(x, y)$. Hence, solving the 2D Poisson equation becomes equivalent to finding the extremum of Equation (5.47). Now we will use the variational form of Poisson's equation (5.47) to solve for the surface potential of a TFET.

### 5.2.2 Solution of the variational form of Poisson's equation in a TFET

In this section, we will solve the surface potential of a TFET using the variational form of Poisson's equation (5.47) [20]. Let us consider an n-channel SOI TFET as shown in Figure 5.6. The device has a channel length of $L$, silicon film thickness of $T_{S i}$, oxide thickness $T_{o x}$ and a body doping of $N_{A}$. The device has a thin silicon film ( $T_{S i} \sim 10 \mathrm{~nm}$ ) such that the body is fully depleted under the influence of the gate voltage. As seen in the previous section, a TFET has two regions, the tunnelling region R1 and the channel region R2. Also, in Section 5.1.2, we have seen that the surface potential in a TFET in the tunnelling region R1 can be expressed in the following form:

$$
\begin{equation*}
\psi(x, 0)=\psi_{C}+\left(\psi_{s r c}-\psi_{C}\right) e^{-\frac{x}{x}} \tag{5.48}
\end{equation*}
$$

where

Characteristic length $L_{d}=\lambda=\sqrt{t_{S i} \varepsilon_{S i} / C_{o x}}=\sqrt{t_{S i} t_{o x} \varepsilon_{S i} / \varepsilon_{o x}}$ and $\psi_{s r c}=$ source potential

According to Equation (5.49), $\lambda$ is not a function of $V_{G S}$. However, as we shall discuss now, this is not valid when an inversion layer is formed in the channel. To understand this, let us observe the surface potential variation with increasing $V_{G S}$ at a fixed $V_{D S}$, as shown in Figure 5.7 We can observe that the surface potential in


Figure 5.6 Schematic of an n-channel TFET.


Figure 5.7 Surface potential along the channel of the TFET for different gate voltages.
the channel (region R2) becomes pinned after a certain threshold $V_{G S}$, at which the inversion layer is formed. Hence, using Equation (5.48), the electric field in the $x$-direction $\left(E_{x}\right)$ in region R1 should become constant after the formation of the inversion layer. However, as can be seen in Figure 5.7, even after the formation of the inversion layer, $E_{x}$ in region R1 continues to increase. This is possible only if the factor $\lambda$ decreases with increasing $V_{G S}$. Hence, it becomes important to incorporate the effect of $V_{G S}$ on $\lambda$ for accurately modelling the surface potential in region R1. Note that in the pseudo-2D method, $\lambda$ or $L_{d}$ was independent of $V_{G S}$, as can be seen in Equation (5.17). Figure 5.8 shows the potential profile along the $x$-direction for different values of $y$ in the body of the TFET. Here we can observe that the potential profile for any value of $y$ can be expressed by an equation similar to (5.48). Hence, we can write:

$$
\begin{equation*}
\psi(x, y)=\psi_{C}(y)+\left(\psi_{s r c}-\psi_{C}(y)\right) e^{-x / \lambda(y)} \tag{5.50}
\end{equation*}
$$

where the characteristic length $\lambda$ is now a function of $y$. Now we will evaluate the Lagrangian (5.47), using Equation (5.50).

### 5.2.2.1 Evaluating the Lagrangian

In this section, we will solve for $\psi(x, y)$ using Equation (5.50) in the Lagrangian for Poisson's equation (5.47) and minimise it. However, let us first make a


Figure 5.8 Potential along the channel of the TFET at different depths (y) from the surface.
simplification in Equation (5.50). As Poisson's equation is linear, using the principle of superposition, let us define the following two parameters:

$$
\begin{equation*}
\phi(x, y)=\psi(x, y)-\psi_{C}(y) \tag{5.51}
\end{equation*}
$$

and

$$
\begin{equation*}
P(x, y)=\rho(x, y)-N_{A} \tag{5.52a}
\end{equation*}
$$

Using Equation (5.52a) we have subtracted the effect of depletion charge $\left(N_{A}\right)$ in Poisson's equation (5.38). Equation (4.51) gives us a simplified form of Equation (5.50):

$$
\begin{equation*}
\phi(x, y)=\left(\psi_{s r c}-\psi_{C}(y)\right) e^{-x / \lambda(y)} \tag{5.52b}
\end{equation*}
$$

Note that our ultimate aim is to find the tunnelling current, for which we have to find the $x$-direction electric field $\left(E_{x}\right)$, and note that $\partial \phi(x, y) / \partial x$ in Equation (5.52b) is equal to $\partial \psi(x, y) / \partial x$ in Equation (5.50). Hence, we get the same $E_{x}$ using Equations (5.51) and (5.52b). Substituting Equation (5.52b) in Equation (5.47), we get the first term of the Lagrangian (5.47) as

$$
\begin{align*}
L_{\psi 1} & =\iint_{D} \frac{\varepsilon}{2}\left[\left(\frac{\partial \phi(x, y)}{\partial x}\right)^{2}+\left(\frac{\partial \phi(x, y)}{\partial y}\right)^{2}\right] d x d y  \tag{5.53}\\
& =\int \frac{\varepsilon}{4}\left\{\frac{1+0.5\left(\lambda^{\prime}(y)\right)^{2}}{\lambda(y)} \psi_{0}^{2}(y)+\left(\psi_{0}(y)^{\prime}\right)^{2} \lambda+\psi_{0}(y) \psi_{0}(y)^{\prime} \lambda^{\prime}\right\} d y
\end{align*}
$$

where

$$
\psi_{0}(y)=\psi(x=0)=\psi_{s c r}(y)-\psi_{c}(y) ; \psi_{0}^{\prime}(y)=\frac{\partial \psi_{0}(y)}{\partial y} \text { and } \lambda^{\prime}(y)=\frac{\partial \lambda(y)}{\partial y}
$$

Next we will calculate the second term of the Lagrangian (5.47):

$$
\begin{equation*}
L_{\psi 2}=\iint_{D}[\phi(x, y) P(x, y)] d x d y \tag{5.54}
\end{equation*}
$$

For small $V_{G S}$ (i.e. before the formation of the inversion layer), there exists a uniform depletion charge $\left(N_{A}\right)$ in the Si body. Therefore, $P(x, y)$ is zero, leading to $L_{\psi 2}=0$.

However, when the gate voltage ( $V_{G S}$ ) increases, an inversion layer forms at the surface in the channel, which makes $P(x, y)$ non-zero. Using the charge sheet approximation (i.e. $P(x, y)$ is non-zero only at the surface $y=0$ ) and assuming the electron quasi-Fermi level to be uniform in the $x$-direction, we get

$$
\begin{equation*}
L_{\psi 2}=-\int_{0}^{\infty} \int_{0}^{T_{s i}} \psi(x, y) P(x, y) d y d x=-\int_{0}^{\infty} q N_{i n v}\left(1-e^{q / k T \psi(x, 0)}\right) \psi(x, 0) d x \tag{5.55}
\end{equation*}
$$

using the fact that, for a potential distribution given in Equation (5.50),

$$
\begin{equation*}
\frac{\partial \psi}{\partial x}=-\frac{\psi}{\lambda} \tag{5.56}
\end{equation*}
$$

we get

$$
\begin{equation*}
L_{\psi 2}=q N_{i n v} \lambda(0) \int_{\psi_{0}(0)}^{0}\left(1-e^{q \psi / k T}\right) d \psi \approx q N_{i n v} \lambda(0)\left[-\psi_{0}(0)\right] \tag{5.57}
\end{equation*}
$$

where $\psi_{0}(y)=\psi(0, y)$.
Now adding Equations (5.47) and (5.57) we get

$$
\begin{align*}
L_{\psi} \int_{-T_{o x}}^{T_{S i}} & \frac{\varepsilon}{4}\left\{\frac{1+0.5\left(\lambda^{\prime}(y)\right)^{2}}{\lambda(y)} \psi_{0}^{2}(y)+\left(\psi_{0}(y)^{\prime}\right)^{2} \lambda+\psi_{0}(y) \psi_{0}(y)^{\prime} \lambda^{\prime}\right\} d y  \tag{5.58}\\
& +q N_{i n v} \lambda(0)\left[-\psi_{0}(0)\right]
\end{align*}
$$

### 5.2.2.2 Minimising the Lagrangian

The integrant equation (5.58) cannot be analytically integrated unless some assumptions are made. We drop the $\psi_{0}(y)^{\prime}$ term in the silicon body because the vertical electric field in the Si body is small. Thus, we integrate the term $\left(\psi_{0}(y)^{\prime}\right)^{2} \lambda$ only in the oxide region. Second, we drop the term $\psi_{0}(y) \psi_{0}(y)^{\prime} \lambda^{\prime}$ in both the silicon and the oxide regions as $\lambda$ is not expected to change much in the thin gate oxide. Finally, we make an assumption that $\lambda$ is constant, giving $\lambda^{\prime}=0$. This assumption is in contradiction with our original premise that $\lambda$ is a function of $y$ as given in Equation (5.50). However, for the time being let us use this assumption, which gives us the following expression for $L_{\mu}$ :

$$
\begin{equation*}
L_{\psi}=\frac{\varepsilon_{S i}}{4} \frac{T_{S i}}{\lambda} \psi_{0}^{2}(0)+\frac{\varepsilon_{o x}}{4} \frac{\lambda}{T_{o x}} \psi_{0}^{2}(0)-q N_{i n v} \lambda \psi_{0}(0) \tag{5.59}
\end{equation*}
$$

Solving for $d L_{\psi} / d \lambda=0$, we obtain

$$
\begin{equation*}
\frac{1}{\lambda^{2}}=\frac{\varepsilon_{o x}}{\varepsilon_{S i} T_{S i} T_{o x}}-\frac{4 q N_{i n v}}{\varepsilon_{S i} T_{S i} \psi_{0}(0)} \tag{5.60}
\end{equation*}
$$

Equation (5.60) shows that $\lambda$ decreases with an increase in $N_{\text {inv }}\left(\right.$ as $\left.\psi_{0}(0)<0\right)$. From Equation (5.60), we can observe that in the absence of the inversion layer (i.e. when $V_{G S}$ is below the threshold), $N_{i n v}=0$, which gives us the same expression for $\lambda$ as that for $L_{d}$ in Equation (5.49). When $N_{\text {inv }}$ is very large, asymptotically we have $\lambda \sim N_{i n v}^{-0.5}$ from Equation (5.60).

### 5.2.2.3 Empirical formulation of characteristic length

However, in the presence of the inversion charge, it is not a good practice to assume $\lambda$ to be constant in the $y$-direction. This is because the inversion charge is only present at the surface and $\lambda$ reduces aggressively at the surface and remains constant deep inside the Si body. Therefore $\lambda(y=0)$ or $\lambda(0)$ should reduce faster with $N_{\text {inv }}$ than predicted by Equation (5.60). Hence, we will adopt an empirical form for $\lambda(y)$ based on observations made from TCAD simulations.

Figure 5.9 shows the TCAD simulation results for variation in $\lambda(y)$ with $y$ for different values of $T_{S i}$. By observing these curves, we adopt the following empirical expression for $\lambda(y)$ :

$$
\begin{equation*}
\lambda(y)=\Lambda \sin \left(\frac{\pi}{2} \frac{y+y_{0}}{T_{S i}+y_{0}}\right) \tag{5.61}
\end{equation*}
$$

where $y_{0}$ is a constant parameter. Equation (5.61) in Equation (5.58) gives


Figure 5.9 Variation in $\lambda(y)$ with y for different values of $T_{S i}$ [20].

$$
\begin{equation*}
L_{\psi}=\frac{\varepsilon_{S i}}{4} \psi_{0}^{2}(0) \int_{0}^{T_{S i}} \frac{1+0.5\left(\lambda^{\prime}\right)^{2}}{\lambda} d y+\frac{\varepsilon_{o x}}{4} \frac{\lambda(0)}{T_{o x}} \psi_{0}^{2}(0)-q N_{i n v} \lambda(0) \psi_{0}(0) \tag{5.62}
\end{equation*}
$$

Now we take (another empirical formulation)

$$
\begin{equation*}
\Lambda=\frac{2}{\pi}\left(T_{S i}+y_{0}\right) \tag{5.63}
\end{equation*}
$$

which gives

$$
\begin{equation*}
A=\int_{0}^{T_{S i}} \frac{1+0.5\left(\lambda^{\prime}\right)^{2}}{\lambda} d y=\frac{3}{2} \ln \left(\cot \frac{\pi}{4} \frac{y_{0}}{T_{S i}+y_{0}}\right)-\frac{1}{2} \cos \frac{\pi}{2} \frac{y_{0}}{T_{S i}+y_{0}} \tag{5.64}
\end{equation*}
$$

Using the Taylor series expansion of Equation (5.64) and using the most significant term, we can show that

$$
\begin{equation*}
\frac{d A}{d y_{0}} \approx \frac{3}{4} \frac{T_{S i}}{y_{0}} \frac{1}{T_{S i}+y_{0}} \tag{5.65}
\end{equation*}
$$

In a strong inversion the value of $\lambda(0)$ is very small as compared to $T_{S i}$, i.e. $\lambda(0) \ll T_{S i}$. Hence, using $\sin x \approx x$ (for small $x$ ) in Equation (5.61), we get

$$
\begin{equation*}
\lambda(0) \approx y_{0} \tag{5.66}
\end{equation*}
$$

Using Equation (5.65) in Equation (5.63) and solving for $d L_{\psi} / d y_{0}=0$ we get

$$
\begin{equation*}
\frac{3}{2} \frac{T_{S i}}{\lambda(0)\left[T_{S i}+\lambda(0)\right]}=\frac{\varepsilon_{o x}}{\varepsilon_{S i} T_{o x}}-\frac{4 q N_{i n v}}{\varepsilon_{S i} \psi_{0}(0)} \tag{5.67}
\end{equation*}
$$

From Equation (5.67), we can see that $\lambda \sim N_{i n v}^{-1}$ when $N_{i n v}$ is large and $\lambda(0)$ is small. Hence, $\lambda(0)$ varies faster with $N_{i n v}$ in Equation (5.67) than in Equation (5.60).

Hence, using the value of $\lambda(0)$ obtained from Equation (5.67) in Equation (5.48), we get a model for the surface potential of a TFET that incorporates the change in the characteristic length $(\lambda)$ due to the build-up of inversion charge in the channel, enabling us to capture the change in the potential and the electric fields in the tunnelling region R1 with varying gate bias more accurately.

### 5.3 The infinite series solution

In this section, we will describe a method for finding the surface potential of a TFET using the separation of variables technique. This method solves the 2D Poisson equation using separation of variables [22], giving us an infinite series solution for the potential. The infinite series obtained is the exact solution of the 2D Poisson equation. However, an infinite series does not give a closed-form solution for the surface potential and hence we take the first term of the infinite series to find the surface potential of the TFET [23-28]. The method is described in detail in the following sections.

### 5.3.1 Solving the 2D Poisson equation using separation of variables

Let us consider the homogeneous form of the 2D Poisson equation, also known as Laplace's equation:

$$
\begin{equation*}
\frac{\partial \psi^{2}}{\partial x^{2}}+\frac{\partial \psi^{2}}{\partial y^{2}}=0 \tag{5.68}
\end{equation*}
$$

Let us assume the above equation has the following form of solution:

$$
\begin{equation*}
\psi(x, y)=X(x) Y(y) \tag{5.69}
\end{equation*}
$$

where $X$ is a function of $x$ and $Y$ is a function $y$. Using Equation (5.69) in (5.68) gives

$$
X^{\prime \prime}(x) Y(y)+X(x) Y^{\prime \prime}(y)=0
$$

which can be written as

$$
\begin{equation*}
\frac{X^{\prime \prime}(x)}{X(x)}=-\frac{Y^{\prime \prime}(y)}{Y(y)}= \pm \lambda^{2} \tag{5.70}
\end{equation*}
$$

where the term $\pm \lambda^{2}$ is a constant.
The above equation is an eigenvalue problem, with $\lambda$ as the eigenvalue. For a positive value of the constant term $+\lambda^{2}$ in Equation (5.70), we get the following set of equations:

$$
\begin{equation*}
X^{\prime \prime}(x)-\lambda^{2} X(x)=0 \tag{5.71}
\end{equation*}
$$

and

$$
\begin{equation*}
Y^{\prime \prime}(y)+\lambda^{2} Y(y)=0 \tag{5.72}
\end{equation*}
$$

Equations (5.71) and (5.72) have the following general form solution:

$$
\begin{equation*}
X(x)=C \cosh (\lambda x)+D \sinh (\lambda x) \tag{5.73}
\end{equation*}
$$

and

$$
\begin{equation*}
Y(y)=A \cos (\lambda y)+B \sin (\lambda y) \tag{5.74}
\end{equation*}
$$

For a negative value of the constant term $-\lambda^{2}$ in Equation (5.70), we get the following set of equations:

$$
\begin{equation*}
X^{\prime \prime}(x)+\lambda^{2} X(x)=0 \tag{5.75}
\end{equation*}
$$

and

$$
\begin{equation*}
Y^{\prime \prime}(y)+\lambda^{2} Y(y)=0 \tag{5.76}
\end{equation*}
$$

Equations (5.75) and (5.76) have the following general form solution:

$$
\begin{equation*}
X(x)=A \cos (\lambda x)+B \sin (\lambda x) \tag{5.77}
\end{equation*}
$$

and

$$
\begin{equation*}
Y(y)=C \cos (\lambda x)+D \sinh (\lambda y) \tag{5.78}
\end{equation*}
$$

Using the solution for $X(x)$ and $Y(y)$ in Equation (5.69), we get the general form solution to the Laplace equation (5.68). However, there are many unknown


Figure 5.10 Boundary conditions in a $2 D$ solution space for the Laplace equation.
quantities in this solution. The first amongst these is the value of the constant term $\left(+\lambda^{2}\right.$ or $\left.-\lambda^{2}\right)$. The sign and the value of $\lambda$ can be found if we have homogeneous boundary conditions in one direction. Then we have the unknown constants $A, B$, $C$ and $D$, which can also be determined by using the boundary conditions. Let us now demonstrate the solution of the 2D Laplace equation for a homogeneous boundary value problem.

### 5.3.2 Solution of the homogeneous boundary value problem

Let us consider the 2D Laplace equation (5.68) with the following boundary conditions, as shown in Figure 5.10:

$$
\begin{gather*}
\psi(x, 0)=0  \tag{5.79}\\
\psi\left(x, L_{y}\right)=0  \tag{5.80}\\
\psi(0, y)=0  \tag{5.81}\\
\psi\left(L_{x}, y\right)=f(y) \tag{5.82}
\end{gather*}
$$

Only one boundary condition in the $x$ direction (5.82) is non-homogeneous and is a function of $y$; the rest of the boundary conditions are homogeneous.

Since both the boundary conditions in the $y$ direction are homogeneous, choosing $-\lambda^{2}$ as the constant term of the eigenvalue problem (5.70) gives us a trivial solution for $\psi(x, y)$, as shown below:

$$
\begin{equation*}
\psi(x, 0)=X(x) Y(0)=0 \Rightarrow Y(0)=0 \tag{5.83}
\end{equation*}
$$

This gives

$$
\begin{equation*}
Y(0)=C \cosh (0)+D \sinh (0)=0 \Rightarrow C=0 \tag{5.84}
\end{equation*}
$$

Now

$$
\begin{equation*}
\psi\left(x, L_{y}\right)=X(x) Y\left(L_{y}\right)=0 \Rightarrow Y\left(L_{y}\right)=0 \tag{5.85}
\end{equation*}
$$

which gives

$$
\begin{equation*}
Y(0)=D \sinh \left(\lambda L_{y}\right)=0 \Rightarrow D=0 \tag{5.86}
\end{equation*}
$$

Hence

$$
\begin{equation*}
Y(y)=0 \Rightarrow \psi(x, y)=X(x) Y(y)=0 \tag{5.87}
\end{equation*}
$$

Thus, we get a trivial solution for $\psi(x, y)$.
Hence, we will choose $+\lambda^{2}$ as the constant term of the eigenvalue problem (5.70), which gives us the general form solution for $X(x)$ and $Y(y)$ as given in Equations (5.73) and (5.74). Using the boundary conditions in the $y$ direction (5.79) and (5.80) gives

$$
\begin{align*}
& \psi(x, 0)=X(x) Y(0)=0 \Rightarrow Y(0)=0  \tag{5.88}\\
& Y(0)=A \cos (0)+B \sin (0) \Rightarrow A=0 \tag{5.89}
\end{align*}
$$

and

$$
\begin{gather*}
\psi\left(x, L_{y}\right)=X(x) Y\left(L_{y}\right)=0 \Rightarrow Y\left(L_{y}\right)=0  \tag{5.90}\\
Y\left(L_{y}\right)=B \sin \left(\lambda L_{y}\right)=0 \Rightarrow \lambda L_{y}=n \pi ; n=1,2,3,4, \ldots \tag{5.91}
\end{gather*}
$$

For each value of $n$, we will have a different value of $B$ and $\lambda$ represented by $B_{n}$ and $\lambda_{n}$, respectively. The value of $\lambda_{n}$ is given by

$$
\begin{equation*}
\lambda_{n}=\frac{n \pi}{L_{y}} ; n=1,2,3,4, \ldots \tag{5.92}
\end{equation*}
$$

The above equation gives us the eigenvalues $\lambda_{n}$ and we get the following expression for $Y(y)$ :

$$
\begin{equation*}
Y(y)=B_{n} \sin \left(\lambda_{n} y\right) \tag{5.93}
\end{equation*}
$$

The expression for $\psi(x, y)$ now becomes

$$
\begin{equation*}
\psi(x, y)=X(x) Y(y)=\left[C_{n} \cosh \left(\lambda_{n} x\right)+D_{n} \sinh \left(\lambda_{n} x\right)\right] B_{n} \sin \left(\lambda_{n} y\right) \tag{5.94}
\end{equation*}
$$

We will now apply the boundary condition (5.81), which gives

$$
\begin{gather*}
\psi(0, y)=X(0) Y(y)=0 \Rightarrow X(0)=0  \tag{5.95}\\
X(0)=C_{n} \cosh (0)+D_{n} \sinh (0)=0 \Rightarrow C_{n}=0 \tag{5.96}
\end{gather*}
$$

Therefore,

$$
\begin{equation*}
X(x)=D_{n} \sinh \left(\lambda_{n} x\right) \tag{5.97}
\end{equation*}
$$

and

$$
\begin{equation*}
X(x) Y(y)=C_{n} \sinh \left(\lambda_{n} x\right) B_{n} \sin \left(\lambda_{n} y\right) \tag{5.98}
\end{equation*}
$$

The solution for $\psi(x, y)$ is the linear combination of $X(x) Y(y)$ for all values of $\lambda_{n}$ :

$$
\begin{equation*}
\psi(x, y)=\sum_{n=0}^{\infty} C_{n} \sinh \left(\lambda_{n} x\right) B_{n} \sin \left(\lambda_{n} y\right) \tag{5.99}
\end{equation*}
$$

Combining the coefficients we get:

$$
\begin{equation*}
\psi(x, y)=\sum_{n=0}^{\infty} b_{n} \sinh \left(\lambda_{n} x\right) \sin \left(\lambda_{n} y\right) \tag{5.100}
\end{equation*}
$$

where

$$
\begin{equation*}
b_{n}=C_{n} B_{n} \tag{5.101}
\end{equation*}
$$

Now we are left with only one unknown $b_{n}$, which can be found using the nonhomogeneous boundary condition (5.82):

$$
\begin{equation*}
\psi\left(L_{x}, y\right)=\sum_{n=0}^{\infty} b_{n} \sinh \left(\lambda_{n} L_{x}\right) \sin \left(\lambda_{n} y\right) \tag{5.102}
\end{equation*}
$$

The above equation is a Fourier series with $b_{n} \sinh \left(\lambda_{n} L_{x}\right)$ as the Fourier series coefficient. Hence, $b_{n}$ can be found by the standard method for finding Fourier coefficients:

$$
\begin{equation*}
\int_{0}^{L y} \psi\left(L_{x}, y\right) \sin \left(\lambda_{n} y\right) d y=\frac{L_{y}}{2} b_{n} \sinh \left(\lambda_{n} L_{x}\right) \tag{5.103}
\end{equation*}
$$

$$
\begin{equation*}
b_{n}=\frac{2}{L_{y} \sinh \left(\lambda_{n} L_{x}\right)} \int_{0}^{L_{y}} \psi\left(L_{x}, y\right) \sin \left(\lambda_{n} y\right) d y \tag{5.104}
\end{equation*}
$$

Equation (5.102) can now be written as

$$
\begin{equation*}
\psi(x, y)=\sum_{n=0}^{\infty} c_{n} \frac{\sinh \left(\lambda_{n} x\right)}{\sinh \left(\lambda_{n} L_{x}\right)} \sin \left(\lambda_{n} y\right) \tag{5.105}
\end{equation*}
$$

Where

$$
\begin{equation*}
c_{n}=\frac{2}{L_{y}} \int_{0}^{L_{y}} \psi\left(L_{x}, y\right) \sin \left(\lambda_{n} y\right) d y \tag{5.106}
\end{equation*}
$$

Equation (5.105) is the solution for the 2D Laplace equation with boundary conditions as given by Equations (5.79) to (5.82). Considering this solution, we will now move on to solving the 2D Poisson equation in a TFET. This will be done by splitting our problem into a linear combination of different boundary value problems, similar to the boundary value problem described in Figure 5.10.

### 5.3.3 The solution to the 2D Poisson equation in a TFET

Let us now look at the problem of solving the 2D Poisson equation (5.1) in a TFET, which would have the boundary conditions as depicted in Figure 5.11 (a). This problem consists of an inhomogeneous equation (2D Poisson equation) with inhomogeneous boundary conditions. Hence, the method of separation of variables cannot be directly applied to solve this problem. However, since the 2D Poisson equation consists only of linear terms, the problem in hand can be split into a linear combination of simpler problems. Let us now see how this is done.

The problem shown in Figure 5.11(a) can be split into three parts as depicted by Figure 5.9 (b) to (d): (i) solving the 1D Poisson's equation in the $y$-direction satisfying the boundary conditions at $y=0$ and $y=t_{S i}$ (giving $v(y)$ ), (ii) solving the 2D Laplace equation satisfying the boundary condition at $x=0$ and assuming all other boundary conditions to be homogeneous (giving $\left.u_{L}(x, y)\right)$ and (iii) solving the 2D Laplace equation satisfying the boundary condition at $x=L_{x}$ and assuming all other boundary conditions to be homogeneous (giving $u_{R}(x, y)$ ).

A linear combination of all these three solutions satisfies the 2D Poisson equation and all the boundary conditions. Hence, we get the following form of solution:

$$
\begin{equation*}
\psi(x, y)=v(y)+u_{L}(x, y)+u_{R}(x, y) \tag{5.107}
\end{equation*}
$$



Figure 5.11 Boundary conditions for the potential distribution in a TFET.

Using the above solution in the 2D Poisson equation (5.1), we get

$$
\begin{equation*}
\nabla^{2} \psi(x, y)=\nabla^{2} v(y)+\nabla^{2} u_{L}(x, y)+\nabla^{2} u_{R}(x, y)=\frac{q N_{A}}{\varepsilon_{S i}}+0+0=\frac{q N_{A}}{\varepsilon_{S i}} \tag{5.108}
\end{equation*}
$$

Hence, the linear combination of the terms $v(y), u_{L}(x, y)$ and $u_{R}(x, y)$ satisfies the 2D Poisson equation. This solution (5.107) also satisfies all the boundary conditions of the problem shown in Figure 5.11(a):

$$
\begin{gather*}
\psi(x, 0)=v(0)+u_{L}(x, 0)+u_{R}(x, 0)=\psi_{G}-V_{F B}+0+0=\psi_{G}-V_{F B}  \tag{5.109}\\
\psi\left(x, T_{S i}\right)=v\left(T_{S i}\right)+u_{L}\left(x, T_{S i}\right)+u_{R}\left(x, T_{S i}\right)=\psi_{S u b}+0+0=\psi_{S u b} \tag{5.110}
\end{gather*}
$$

$$
\begin{gather*}
\psi(0, y)=v(y)+u_{L}(x, 0)+u_{R}(x, 0)=v(y)+\psi_{S r c}-v(y)+0=\psi_{S r c}  \tag{5.111}\\
\psi(0, y)=v(y)+u_{L}(x, 0)+u_{R}(x, 0)=v(y)+0+\psi_{\text {Drain }}-v(y)=\psi_{\text {Drain }} \tag{5.112}
\end{gather*}
$$

In the next section, we will solve for the potential in a TFET using the approach outlined here.

### 5.3.4 The infinite series solution to Poisson's equation in a TFET

Let us now solve for the potential in a TFET using the approach described in the previous section. Figure 5.12 shows the cross-section of an SOI TFET with all its boundary conditions. As given in Equation (5.107), the potential distribution in a TFET is the linear combination of three terms $\left(v(y), u_{L}(x, y)\right.$ and $\left.u_{R}(x, y)\right)$. We will begin by solving for the first term $v(y)$, which is the solution to the 1D Poisson's equation (4.1) satisfying the boundary conditions at $y=0$ and $y=t_{s i}$ :

$$
\begin{align*}
& \frac{\partial^{2} v(y)}{\partial y^{2}}=\frac{q N_{A}}{\varepsilon_{S i}} \text { in the silicon body }  \tag{5.113}\\
& \frac{\partial^{2} v(y)}{\partial y^{2}}=0 \text { inside the gate oxid } \tag{5.114}
\end{align*}
$$

The solution of Equation (5.113) is

$$
\begin{equation*}
v(y)=\frac{q N_{A}}{\varepsilon_{S i}} y^{2}+\alpha_{1} y+\beta_{1} \tag{5.115}
\end{equation*}
$$

and the solution of Equation (5.114) is

$$
\begin{equation*}
v(y)=\alpha_{1} y+\beta_{2} \tag{5.116}
\end{equation*}
$$



Figure 5.12 Boundary conditions for the potential distribution in an SOI TFET.

Using the boundary conditions shown in (Figure 5.12), we get the following values for $\alpha_{1}, \beta_{1}, \alpha_{2}$ and $\beta_{2}$ :

$$
\begin{gather*}
\alpha_{1}=\frac{\varepsilon_{o x} \psi_{G}+\frac{q N_{A} \varepsilon_{o x}}{2 \varepsilon_{S i}} t_{S i}^{2}}{\varepsilon_{S i} t_{o x}-\varepsilon_{o x} t_{S i}}  \tag{5.117}\\
\beta_{1}=\beta_{2}=\frac{\frac{q N_{A}}{2} t_{S i}^{2} t_{o x}-\psi_{G} t_{S i} \varepsilon_{o x}}{\varepsilon_{S i} t_{o x}-t_{S i} \varepsilon_{o x}}  \tag{5.118}\\
\alpha_{2}=\frac{\varepsilon_{S i}}{\varepsilon_{o x}} \alpha_{1}=\frac{\varepsilon_{S i} \psi_{G}+\frac{q N_{A}}{2} t_{S i}^{2}}{\varepsilon_{S i} t_{o x}-\varepsilon_{o x} t_{S i}} \tag{5.119}
\end{gather*}
$$

Now we will solve for the second term in Equation (5.107), $u_{L}(x, y)$, which is the solution for the 2D Laplace equation (4.68) with the boundary conditions given in (Figure 5.12). From the separation of variables method described in Section 5.3, we get the following form of solution for the 2D Laplace equation for the boundary value problem described in Figure 5.11(d):

$$
\begin{equation*}
\psi(x, y)=\sum_{n=0}^{\infty} c_{n} \frac{\sinh \left(\lambda_{n} x\right)}{\sinh \left(\lambda_{n} L_{x}\right)} \sin \left(\lambda_{n} y\right) \tag{5.120}
\end{equation*}
$$

Let us now write a more general form of this equation where the positions of the origin of the sin and sinh functions are also variable:

$$
\begin{equation*}
\psi(x, y)=\sum_{n=0}^{\infty} c_{n} \frac{\sinh \left(\lambda_{n}\left(x-x_{i}\right)\right)}{\sinh \left(\lambda_{n} L_{x}\right)} \sin \left(\lambda_{n}\left(y-y_{i}\right)\right) \tag{5.121}
\end{equation*}
$$

where $x_{0}$ and $y_{0}$ are the positions of the origin of the sinh and $\sin$ functions, respectively.

Here we can observe that the position of $y_{0}$ and the value of $\lambda_{n}$ satisfy the $y$-direction boundary conditions and the position of $x_{0}$ satisfy the $x$-direction homogeneous boundary condition. The value of $c_{n}$ is calculated using the nonhomogeneous $x$-direction boundary condition. We will now find the expression for $u_{L}(x, y)$ using Equation (5.121) and the boundary conditions given in Figure 5.12. Note that similar to the solution of $v(y)$, we will have different expressions for $u_{L}(x, y)$ for the silicon body and for the oxide regions. Using the boundary conditions $u_{L}\left(x, t_{s i}\right)=0$ and $u_{L}(L, y)=0$, we get the values for $y_{0}$ and $x_{0}$, respectively. This will give

$$
\begin{equation*}
u_{L}(x, y)=\sum_{n=0}^{\infty} c_{n} \frac{\sinh \left(\lambda_{n}(x-L)\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n}\left(y+t_{s i}\right)\right) \text { in the silicon body } \tag{5.122}
\end{equation*}
$$

Similarly, using the boundary conditions for the oxide regions $u_{L}\left(x, t_{o x}\right)=0$ and $u_{L}(L, y)=0$, we get

$$
\begin{equation*}
u_{L}(x, y)=\sum_{n=0}^{\infty} c_{T n} \frac{\sinh \left(\lambda_{n}(x-L)\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n}\left(y+t_{o x}\right)\right) \text { in the oxide region } \tag{5.123}
\end{equation*}
$$

Now, as given in Figure 5.11(a), $u_{L}(x, y)$ and $\varepsilon \frac{\partial u_{L}(x, y)}{\partial y}$ should be continuous at $y=0$. Using these boundary conditions and equating the $n$th terms, we get the following set of equations:

$$
\begin{gather*}
c_{n}=\frac{\sinh \left(\lambda_{n}(x-L)\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n} t_{S i}\right)=-c_{T n} \frac{\sinh \left(\lambda_{n}(x-L)\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n} t_{o x}\right)  \tag{5.124}\\
\lambda_{n} \varepsilon_{S i} c_{n}=\frac{\sinh \left(\lambda_{n}(x-L)\right)}{\sinh \left(\lambda_{n} L\right)} \cos \left(\lambda_{n} t_{S i}\right)=\lambda_{n} \varepsilon_{o x} c_{T n} \frac{\sinh \left(\lambda_{n}(x-L)\right)}{\sinh \left(\lambda_{n} L\right)} \cos \left(\lambda_{n} t_{o x}\right) \tag{5.125}
\end{gather*}
$$

Dividing Equation (5.124) by Equation (5.125), we get

$$
\begin{gather*}
\frac{\tan \left(\lambda_{n} t_{S i}\right)}{\varepsilon_{S i}}=-\frac{\tan \left(\lambda_{n} t_{o x}\right)}{\varepsilon_{o x}}  \tag{5.126}\\
\Rightarrow \varepsilon_{o x} \tan \left(\lambda_{n} t_{S i}\right)+\varepsilon_{S i} \tan \left(\lambda_{n} t_{o x}\right)=0 \tag{5.127}
\end{gather*}
$$

By solving Equation (5.127), we get the eigenvalues $\lambda_{n}$.
We will now solve for $u_{R}(x, y)$, which is the solution for the 2D Laplace equation (5.68) with the boundary conditions given in Figure 5.12. Similar to the solution for $u_{L}(x, y)$ outlined above, we get $u_{R}(x, y)$ as

$$
\begin{align*}
& u_{R}(x, y) \sum_{n=0}^{\infty} d_{n} \frac{\sinh \left(\lambda_{n}(x)\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n}\left(y+t_{s i}\right)\right) \text { in the silicon region }  \tag{5.128}\\
& u_{R}(x, y) \sum_{n=0}^{\infty} d_{T n} \frac{\sinh \left(\lambda_{n}(x)\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n}\left(y-t_{o x}\right)\right) \text { in the oxide region } \tag{5.129}
\end{align*}
$$

As the $y$-direction boundary conditions are the same for $u_{R}(x, y)$ and $u_{L}(x, y)$, Equations (5.124) and (5.125) will be the same for $u_{R}(x, y)$ and for $u_{L}(x, y)$ :

$$
\begin{array}{r}
d_{n} \frac{\sinh \left(\lambda_{n} x\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n} t_{S i}\right)=-d_{T n} \frac{\sinh \left(\lambda_{n} x\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n} t_{o x}\right) \\
\lambda_{n} \varepsilon_{S i} d_{n} \frac{\sinh \left(\lambda_{n} x\right)}{\sinh \left(\lambda_{n} L\right)} \cos \left(\lambda_{n} t_{S i}\right)=\lambda_{n} \varepsilon_{o x} d_{T n} \frac{\sinh \left(\lambda_{n} x\right)}{\sinh \left(\lambda_{n} L\right)} \cos \left(\lambda_{n} t_{o x}\right) \tag{5.131}
\end{array}
$$

Equations (5.130) and (5.131) give

$$
\begin{equation*}
\varepsilon_{o x} \tan \left(\lambda_{n} t_{S i}\right)+\varepsilon_{S i} \tan \left(\lambda_{n} t_{o x}\right)=0 \tag{5.132}
\end{equation*}
$$

The next task is to find the values of $c_{n}$ and $c_{T n}$ in Equations (5.122) and (5.123), respectively. We will first express $c_{T n}$ in terms of $c_{n}$ using Equation (5.124):

$$
\begin{equation*}
c_{T n}=-c_{n} \frac{\sin \left(\lambda_{n} t_{S i}\right)}{\sin \left(\lambda_{n} t_{o x}\right)} \tag{5.133}
\end{equation*}
$$

Now we can write

$$
\begin{equation*}
u_{L}(x, y)=\sum_{n=0}^{\infty} c_{n} u_{L n}(x, y) \tag{5.134}
\end{equation*}
$$

Where

$$
\begin{array}{r}
u_{L n}(x, y)=\frac{\sinh \left(\lambda_{n}(x-L)\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n}\left(y+t_{s i}\right)\right) \\
u_{L n}(x, y)=-\frac{\sin \left(\lambda_{n} t_{S i}\right)}{\sin \left(\lambda_{n} t_{o x}\right)} \frac{\sinh \left(\lambda_{n}(x-L)\right)}{\sinh \left(\lambda_{n} L\right)} \sin \left(\lambda_{n}\left(y-t_{o x}\right)\right) \tag{5.136}
\end{array}
$$

The value of $c_{n}$ can now be found using the non-homogeneous $x$-direction boundary condition and the method for finding Fourier coefficients (as shown in Equation 5.103), as given below:

$$
\begin{equation*}
c_{n}=\frac{\int_{-t_{S i}}^{t_{o x}}\left[V_{\text {source }}-v(y)\right] g_{n}(y) d y}{\int_{-t_{S i}}^{t_{o x}} u_{L n}(0, y) g_{n}(y) d y} \tag{5.137}
\end{equation*}
$$

where $g_{n}$ is an orthogonal function of $u_{L n}(0, y)$, such that

$$
\begin{equation*}
\int_{-t_{S i}}^{t_{o x}} u_{L n}(0, y) g_{m}(y) d y=0 ; n \neq m \tag{5.138}
\end{equation*}
$$

The function $g_{1}$ is given by

$$
\begin{gather*}
g_{1}(y)=\sin \left(\lambda_{n}\left(y+t_{s i}\right)\right) \text { in the silicon region }  \tag{5.139}\\
g_{1}(y)=\frac{\cos \left(\lambda_{n} t_{S i}\right)}{\cos \left(\lambda_{n} t_{o x}\right)} \sin \left(\lambda_{n}\left(y-t_{o x}\right)\right) \text { in the oxide region } \tag{5.140}
\end{gather*}
$$

Hence, we get:

$$
\begin{equation*}
c_{1}=\frac{\int_{-t_{S i}}^{t_{o x}}\left[V_{\text {source }}-v(y)\right] g_{1}(y) d y}{\int_{-t_{S i}}^{t_{o x}} u_{L n}(0, y) g_{1}(y) d y} \tag{5.141}
\end{equation*}
$$

Similarly,

$$
\begin{equation*}
d_{1}=\frac{\int_{-t_{S i}}^{t_{o x}}\left[V_{d r a i n}-v(y)\right] g_{1}(y) d y}{\int_{-t_{s i}}^{t_{o x}} u_{R n}(0, y) g_{1}(y) d y} \tag{5.142}
\end{equation*}
$$

By evaluating the above integrals we get the coefficients for the first terms of Equation (5.134). We neglect the higher-order terms (i.e. $n=2,3,4, \ldots$ ) and get the following expression for $\psi(x, y)$ :

$$
\begin{equation*}
\psi(x, y)=v(y)+c_{1} u_{L 1}(x, y)+d_{1} u_{R 1}(x, y) \tag{5.143}
\end{equation*}
$$

The value of $\psi(x, y)$ at $y=0$ gives us the surface potential:

$$
\begin{equation*}
\psi_{S}(x)=\psi(x, 0)=v(0)+c_{1} u_{L 1}(x, 0)+d_{1} u_{R 1}(x, 0) \tag{5.144}
\end{equation*}
$$

Hence, exploiting the linearity of Poisson's equation and using the method of separation of variables, we get a solution for the surface potential in a TFET. This method has been extensively used in various analytical models reported to date for the drain current of a TFET. This method, however, assumes the entire channel to be depleted, which may not be the case with long channel devices. In such a case, we can solve for the 2D Poisson equation only in the tunnelling region with the appropriate boundary conditions.

### 5.4 Extension of surface potential models to different TFET structures

All the methods for finding the surface potential discussed so far have been derived for a single gate SOI TFET. In this section, we will discuss extending these models to double gate (DG) and gate all around (GAA) TFET structures. The advantages of these structures have been discussed previously in Section 3.3.

### 5.4.1 DG TFET

Let us consider a double gate (DG) TFET structure as shown in Figure 5.13. The device has a channel length of $L$, silicon film thickness of $T_{S i}$, oxide thickness $T_{o x}$ and a body doping of $N_{A}$. The device has a long channel (i.e. channel length $>50$ $\mathrm{nm})$ and a thin silicon film ( $T_{S i} \sim 10 \mathrm{~nm}$ ) such that the body is fully depleted under the influence of a gate voltage. The top and the bottom gates are of the same material and hence the structure is symmetric along the $a-a^{\prime}$ axis. This device has two channels, one at the top silicon-oxide interface and other at the bottom siliconoxide interface. Since the device is symmetric, we will only study one of the two channels and use the same results for the other channel. However, the reader should note that the electrostatics of a single gate and a double gate TFET are not the same. Hence, the model results derived for a single gate TFET in the previous sections cannot be directly applied to a DG TFET.

Let us first discuss the pseudo-2D method for finding the surface potential in a DG TFET [15, 17]. We start by using the parabolic potential approximation (5.3) in a DG TFET. Figure 5.14 shows the potential along the $y$ direction in a DG TFET. From this figure, we can observe that the potential distribution in the $y$-direction in a DG TFET has the following boundary conditions:
(i) The electric field at the centre of the body (i.e. $y=0$ ) is equal to zero, which gives

$$
\begin{equation*}
\left.\frac{\partial \psi(x, y)}{\partial y}\right|_{y=0}=0 \tag{5.145}
\end{equation*}
$$



Figure 5.13 Schematic of a p-channel double gate (DG) TFET.


Figure 5.14 Potential distribution along the y-direction in a DG TFET.
(ii) The potential at the $\mathrm{Si}-\mathrm{SiO}_{2}$ interface (i.e. $y=t_{S i} / 2$ ) is equal to the surface potential $\left(\psi_{S}(x)\right)$, which gives

$$
\begin{equation*}
\psi\left(x, t_{s i} / 2\right)=\psi_{S}(x) \tag{5.146}
\end{equation*}
$$

(iii) The electric field displacement is continuous across the $\mathrm{Si}_{-}-\mathrm{SiO}_{2}$ interface (i.e. $y=t_{S i} / 2$ ), which gives

$$
\begin{equation*}
\left.\frac{\partial \psi(x, y)}{\partial y}\right|_{y=t_{S I} / 2}=\frac{C_{o x}\left(\psi_{G}-\psi_{S}(x)\right)}{\varepsilon_{S i}} \tag{5.147}
\end{equation*}
$$

where $C_{o x}$ is the gate oxide capacitance per unit area and $\psi_{G}$ is the gate potential. The gate potential $\psi_{G}$ is given by

$$
\begin{equation*}
\psi_{G}=V_{G S}-V_{F B} \tag{5.148}
\end{equation*}
$$

where $\mathrm{V}_{\mathrm{FB}}$ is the flat-band voltage and is dependent on the work function of the gate. Note that the above boundary conditions are different from the boundary conditions given by Equations (5.4) to (5.6) for a single gate TFET.

Using Equations (5.145) to (5.147), we get the following values for the coefficients $a_{0}(x), a_{1}(x)$ and $a_{2}(x)$ (Equation (5.3)):

$$
\begin{equation*}
a_{0}(x)=\psi_{S}(x)\left(1+\frac{C_{o x} t_{S i}}{4 \varepsilon_{S i}}\right)-\frac{\psi_{G} t_{o x}}{4 t_{S i}} \tag{5.149}
\end{equation*}
$$

$$
\begin{gather*}
a_{1}(x)=0  \tag{5.150}\\
a_{2}(x)=\frac{C_{o x}\left(\psi_{G}-\psi_{S}(x)\right)}{t_{S i} \varepsilon_{S i}} \tag{5.151}
\end{gather*}
$$

Using the above coefficients in the 2D Poisson equation (5.1), gives us the following second-order differential equation in the surface potential:

$$
\begin{equation*}
\frac{\partial^{2} \psi_{S}(x)}{\partial x^{2}}-\frac{2 C_{o x}}{t_{S i} \varepsilon_{S i}} \psi_{S}(x)=\frac{q N_{A}}{\varepsilon_{S i}}-\frac{2 C_{o x}}{t_{S i} \varepsilon_{S i}} \psi_{G} \tag{5.152}
\end{equation*}
$$

This equation is of the same form as Equation (5.14) derived in Section 5.1.2 for a single gate TFET, but with a difference in the coefficients of $\psi_{S}(x)$ and $\psi_{G}$ terms. Hence, the final solution for the surface potential of a DG TFET will have the same form as that of a single gate TFET as given by Equations (5.33) and (5.34) with a change in the value of the parameter $L_{d}$ (i.e. the characteristic length), which is

$$
\begin{equation*}
L_{d}=\sqrt{t_{S i} \varepsilon_{S i} / 2 C_{o x}}=\sqrt{t_{S i} t_{o x} \varepsilon_{S i} / 2 \varepsilon_{o x}} \text { for a DG TFET } \tag{5.153}
\end{equation*}
$$

The increase in the drain in a DG TFET as compared to a single gate TFET is due to two factors, the first due to the fact that we have two parallel channels in a DG TFET and the second due to a decrease in the value of $L_{d}$ for a double gate TFET as compared to a single gate TFET. Hence, the drain current of a DG TFET will be more than twice of that of a single gate TFET, with the same device parameters.

Let us now recall the other approaches for finding the surface potential discussed in the previous sections.

The variational approach discussed in Section 5.2 can be extended for finding the surface potential of a DG TFET by using the symmetry of the device along the $a-a^{\prime}$ axis and transforming the problem into a single TFET with appropriate integration limits, as depicted in Figure 5.15. As outlined in Section 5.2, the use of this


Figure 5.15 Schematic of a p-channel double gate (DG) TFET showing the axis of symmetry.


Figure 5.16 Boundary conditions for the potential distribution of a DG TFET.
method helps in incorporating the effect of inversion charge in the surface potential of the TFET.

The infinite series method discussed in Section 5.3 can also be extended to a DG TFET. In this case, we need to follow the same procedure as outlined in Section 5.3 on the boundary value problem depicted in Figure 5.16. As discussed in Section 5.3, the infinite series method is the exact solution to the 2D Poisson equation, giving us a more complex but accurate model for the surface potential of the TFET.

We will now discuss how to find the surface potential for a gate all around (GAA) nanowire TFET.

### 5.4.2 GAA TFET

A GAA TFET as described in Section 3.3.2 is a three-dimensional structure with a gate wrapped all around a piece of silicon nanowire as shown in Figure 5.17. Since, the device has a cylindrical structure, we need to solve for the 2D Poisson equation in cylindrical coordinates, as given below:

$$
\begin{equation*}
\frac{1}{r} \frac{\partial}{\partial r}\left(r \frac{\partial \psi(r, z)}{\partial r}\right)+\frac{\partial^{2} \psi(r, z)}{\partial z^{2}}=\frac{q N_{A}}{\varepsilon_{S i}} \tag{5.154}
\end{equation*}
$$

As the device has a gate wrapped all around the silicon nanowire uniformly, the electrostatics in the device is invariant along the angular direction $(\phi)$. Hence, we consider only the 2D form of Poisson's equation in cylindrical coordinates (5.154), neglecting the angular dimension. If we try to draw an analogy between a planar and a cylindrical structure then the $r$-direction corresponds to the $y$-direction, the $z$-direction corresponds to the $x$-direction and the angular direction corresponds to the $z$-direction.

Gate ( $L$ )


Figure 5.17 Schematic of a p-channel gate all around (GAA) nanowire TFET.


Figure 5.18 Potential distribution along the radius of a GAA nanowire TFET.

Now let us look at the potential distribution of a GAA nanowire TFET in the $r$-direction (Figure 5.18). The shape of the potential distribution of a GAA nanowire TFET in the $r$-direction is exactly the same as that of a DG TEFT in the $y$-direction. Hence, the $r$-direction boundary conditions for a GAA nanowire TFET are exactly the same as those given by Equations (5.145) to (5.147). Hence, using the pseudo-2D method in a GAA nanowire TFET gives us the following second-order differential equation in the surface potential:

$$
\begin{equation*}
\frac{\partial^{2} \psi_{S}(z)}{\partial z^{2}}-\frac{2 C_{o x}}{T_{S i} \varepsilon_{S i}} \psi_{S}(z)=\frac{q N_{A}}{\varepsilon_{S i}}-\frac{2 C_{o x}}{T_{S i} \varepsilon_{S i}} \psi_{G} \tag{5.155}
\end{equation*}
$$

Equation (5.155) is exactly the same as Equation (5.152), with only a change in the coordinates. However, for a GAA nanowire TFET the expression for $C_{o x}$ will be different from that of a DG TFET. The parameter $C_{o x}$ for a GAA nanowire TFET is the gate capacitance per unit area at the inner surface of the oxide (i.e. at $\left.r=T_{S i} / 2\right)$ and is given by

$$
\begin{equation*}
C_{o x}=\varepsilon_{o x} /\left(\left(T_{S i} / 2\right) \ln \left(1+\frac{T_{o x}}{T_{S i} / 2}\right)\right) \tag{5.156}
\end{equation*}
$$

This gives the value of characteristic length $\left(L_{d}\right)$ for the GAA TFET as

$$
\begin{equation*}
L_{d}=\sqrt{T_{S i}^{2} \ln \left(1+\frac{2 T_{o x}}{T_{S i}}\right) \varepsilon_{S i} /\left(4 \varepsilon_{o x}\right)} \tag{5.157}
\end{equation*}
$$

Hence the solution for the surface potential of the GAA TFET will also be the same as that outlined in Section 5.1 with a characteristic length as given by Equation (5.157) [10, 11, 14]. Therefore, we can conclude that the pseudo-2D method for finding the surface potential yields the same results for the single gate, DG and GAA TFET structures, with only a difference in the value of the characteristic length $\left(L_{d}\right)$.

From the expressions for $L_{d}$ for a single gate, DG and GAA TFET, we can see that for the same values of $T_{S i}$ and $T_{o x}$, the value of the characteristic length $L_{d}$ is largest for a single gate TFET, followed by a DG TFET and is smallest for a GAA TFET. A smaller value of $L_{d}$ in the DG and GAA structures means that for the same value of $V_{G S}$, these structures have an increased amount of inversion layer charge in the channel region (region R2, Figure 5.4) as compared to that in a single gate structure. This shows that we get an enhanced electrostatic control of the gate over the channel as we move from a single gate structure to DG and GAA structures. Thus, DG and GAA structures exhibit lesser short channel effects (SCEs) as compared to a single gate device. In a TFET, a smaller value of $L_{d}$ also means that there will be a higher $x$-direction electric field in the tunnelling region in these device structures leading to an enhanced drain current.

We will now briefly outline how the other approaches discussed in the previous sections can be applied for finding the surface potential of a GAA TFET.

The variational approach discussed in Section 5.2 can be used in a GAA nanowire TFET, by first formulating the variational form of the 2D cylindrical Poisson's equation and then by following a procedure similar to that outlined in Section 5.2. The use of this method helps in incorporating the effect of inversion charge in the surface potential of the GAA TFET.

For using the infinite series method to find the surface potential in a GAA nanowire TFET, one has to first find the general form solution for the 2D Poisson equation in cylindrical coordinates using the method of separation of variables and then use a boundary value problem as outlined for the DG TFET in the previous section. As discussed in Section 5.3, the infinite series method is the exact solution to the 2D Poisson's equation, giving us a more complex but accurate model for the surface potential of the TFET.

We will now discuss the surface potential for a dual material gate (DMG) TFET, using the pseudo-2D method.

### 5.4.3 Dual material gate TFET

### 5.4.3.1 Pseudo-2D model for a dual material gate TFET

As discussed in Section 3.3.1, a dual material gate (DMG) TFET is an important TFET architecture and provides many potential advantages. In this section, we will discuss about modelling the surface potential in a DMG TFET using the pseudo-2D model [9, 10]. Figure 5.19 shows the structure of a DMG TFET and also a simulated surface potential curve. As can be seen in the figure, the DMG TFET has three depletion regions in the body, the source-channel depletion region (region RD1) and two depletion regions at the interface of the two gates,

(b)


Figure 5.19 (a) Schematic of a p-channel dual material gate (DMG) TFET. (b) Surface potential along the channel of a DMG TFET.
one under the tunnelling gate (RD2) and one under the auxiliary gate (RD3). Also there are two channel regions, one under the tunnelling gate ( RC 1 ) and one under the auxiliary gate ( RC 2 ). Similar to Equations (5.20 and 5.21), the surface potential in regions RC1 and RC2 can be written as:

$$
\begin{gather*}
\psi_{C t}=V_{D}+\psi_{B t} \text { in the saturation region }  \tag{5.158}\\
\psi_{C t}=V_{G S}-V_{t h t}+\psi_{B t} \text { in the linear region }  \tag{5.159}\\
\psi_{C a}=V_{D}+\psi_{B a} \text { in the saturation region }  \tag{5.160}\\
\psi_{C a}=V_{G S}-V_{t h a}+\psi_{B a} \text { in the linear region } \tag{5.161}
\end{gather*}
$$

where in the symbol subscripts $t$ is for the region under the tunnelling gate (RC1) and $a$ is for the region under the auxiliary gate (RC2); $\psi_{B}$ is the built-in potential of the channel, which is the sum of the band bending in the body and drop across the buried oxide, and $V_{t h}$ is the threshold voltage of a MOSFET of an equivalent structure.

Since this is a single gate SOI structure we get the following general form solution for the surface potential in the depletion regions, as discussed in Section 5.1:

$$
\begin{equation*}
\psi_{s i}(x)=C_{i} \exp \left(\frac{x-L_{i}}{L_{d}}\right)+D_{i} \exp \left(\frac{-\left(x-L_{i}\right)}{L_{d}}\right)+\psi_{G i}-\frac{q N_{i} L_{d}^{2}}{\varepsilon_{S i}} \tag{5.162}
\end{equation*}
$$

where the characteristic length $L_{d}$ is

$$
\begin{equation*}
L_{d}=\sqrt{T_{S i} T_{o x} \varepsilon_{S i} / \varepsilon_{o x}} \tag{5.163}
\end{equation*}
$$

and $C_{i}$ and $D_{i}$ are coefficients, $L_{i}$ is equal to the length of the depletion region, $\psi_{G i}$ is equal to the gate potential above the depletion region and $N_{i}$ is equal to the background charge. The subscript $i$ is for the region number (e.g. $i=1$ for RD1). We will have three such equations, one for each depletion region. The solution for the surface potential in the source-channel depletion region is exactly the same as that outlined for the SOI TFET in Section 5.1. Therefore, $\psi_{s 1}(x)$ is given by Equation (5.33). Now we have to find the surface potential in the regions RD2 and RD3. One important thing to note here is that the value of $N_{i}$ will be different for R2 and R3. The channel at the boundary between RD2 and RD3 behaves like a $\mathrm{p}^{+}-\mathrm{p}$ junction and hence there will be complete depletion in RD2, but the mobile charges of the channel, which were earlier in region RD2, will now move into region RD3. We assume that the density of this charge in region RD3 to be $n \mathrm{~cm}^{-3}$. Hence, $N_{i}$ in RD2 will be the body doping $N_{A}$ and in RD3 it will be $N_{A}+n$. We will first define the position of $x=0$ at the interface of the two gates.

Note that this has been done to get simpler expressions for the coefficients of Equation (5.162). We will then use the following boundary conditions for finding the unknowns $C_{2}, D_{2}, C_{3}, D_{3}, L_{2}$ and $L_{3}$.
(i) The value of $\psi_{S}$ at $x=-L_{2}$ is equal to $\psi_{C t}$ and at $x=L_{3}$ is equal to $\psi_{C a}$ :

$$
\begin{align*}
& \psi_{s 2}\left(-L_{2}\right)=\psi_{C t}  \tag{5.164}\\
& \psi_{s 3}\left(-L_{3}\right)=\psi_{C a} \tag{5.165}
\end{align*}
$$

(ii) The electric field at $x=-L_{2}$ and at $x=L_{3}$ is zero:

$$
\begin{gather*}
\frac{\partial \psi_{s 2}\left(-L_{2}\right)}{\partial z}=0  \tag{5.166}\\
\frac{\partial \psi_{s 3}\left(L_{3}\right)}{\partial z}=0 \tag{5.167}
\end{gather*}
$$

(iii) The surface potential is continuous at $x=0$ :

$$
\begin{equation*}
\psi_{s 2}(0)=\psi_{s 3}(0) \tag{5.168}
\end{equation*}
$$

(iv) The electric field is continuous at $x=0$ :

$$
\begin{equation*}
\frac{\partial \psi_{s 2}(0)}{\partial z}=\frac{\partial \psi_{s 3}(0)}{\partial z} \tag{5.169}
\end{equation*}
$$

From Equations (5.164) and (5.166), we get

$$
\begin{equation*}
C_{2}=D_{2}=\frac{\psi_{C t}-\psi_{G t}+\frac{q N_{s} L_{d}^{2}}{\varepsilon_{S i}}}{2} \tag{5.170}
\end{equation*}
$$

From Equations (5.165) and (5.167), we get

$$
\begin{equation*}
C_{3}=D_{3}=\frac{\psi_{C a}-\psi_{G a}+\frac{q\left(N_{S}+n\right) L_{d}^{2}}{\varepsilon_{S i}}}{2} \tag{5.171}
\end{equation*}
$$

From Equation (5.168), we get

$$
\begin{gather*}
\frac{\psi_{C t}-\psi_{G t}+\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}}}{2}\left(e^{L_{2} / L_{d}}+e^{-L_{2} / L_{d}}\right)+\psi_{G t}-\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}} \\
=\frac{\psi_{C a}-\psi_{G a}+\frac{q\left(N_{S}+n\right) L_{d}^{2}}{\varepsilon_{S i}}}{2}\left(e^{L_{3} / L_{d}}+e^{-L_{3} / L_{d}}\right)+\psi_{G a}-\frac{q\left(N_{S}+n\right) L_{d}^{2}}{\varepsilon_{S i}} \tag{5.172}
\end{gather*}
$$

From Equation (5.169), we get
$\frac{\psi_{C t}-\psi_{G t}+\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}}}{2}\left(e^{L_{2} / L_{d}}-e^{-L_{2} / L_{d}}\right)=\frac{\psi_{C a}-\psi_{G a}+\frac{q\left(N_{S}+n\right) L_{d}^{2}}{\varepsilon_{S i}}}{2}\left(e^{-L_{3} / L_{d}}-e^{L_{3} / L_{d}}\right)$

The value of $n$ in region R 3 can be calculated by using the following condition, which comes from the conservation of charge across the $\mathrm{p}^{+}-\mathrm{p}$ junction at the boundary of the tunnelling gate and the auxiliary gate, that is

$$
\begin{equation*}
n L_{3}=n_{c h 2} L_{2} \tag{5.174}
\end{equation*}
$$

where $n_{c h 2}$ is the channel inversion charge concentration under the tunnelling gate. Simultaneously solving (5.172), (5.173) and (5.174) gives us the values of $L_{2}, L_{3}$ and $n$. These equations have to be solved numerically as they contain non-linear expressions. Since, after the onset of strong inversion, the channel charge does not vary greatly and $L_{2}$ and $L_{3}$ are of the same order, we can simplify our model by assuming $n$ to be constant. As the inversion charge in strong inversion mostly remains a constant with the applied $V_{G S}$, we can assume $n$ to be fixed at $10^{19} / \mathrm{cm}^{3}$ as $n_{c h 2}$ is typically $10^{19} / \mathrm{cm}^{3}$ in strong inversion. Now we only need to solve Equations (5.172) and (5.173) and find $L_{2}$ and $L_{3}$.

We can now write the final expression for the surface potential of the DMG TFET, as follows:
$\psi_{s}(x)=\left(\psi_{C t}-\psi_{G t}\right) \cosh \left(\frac{x-L_{d} \cosh ^{-1}\left(V_{b i} /\left(\psi_{G t}\right)\right)}{L_{d}}\right)+\psi_{G}-\frac{q N_{A} L_{d}^{2}}{\varepsilon_{S i}}$ in region RD1

$$
\begin{equation*}
\psi_{s}=\psi_{C t}, \quad \text { in region } \mathrm{RC} 1 \tag{5.176}
\end{equation*}
$$

$$
\begin{equation*}
\psi_{s}(x)=C_{2} \exp \left(\frac{x-L_{2}}{L_{d}}\right)+D_{2} \exp \left(\frac{-\left(x-L_{2}\right)}{L_{d}}\right)+\psi_{G t}-\frac{q N_{A} L_{d}^{2}}{\varepsilon_{S i}} \text { in region RD2 } \tag{5.177}
\end{equation*}
$$

$\psi_{s}(x)=C_{3} \exp \left(\frac{x-L_{3}}{L_{d}}\right)+D_{3} \exp \left(\frac{-\left(x-L_{3}\right)}{L_{d}}\right)+\psi_{G a}-\frac{q\left(N_{A}+n\right) L_{d}^{2}}{\varepsilon_{S i}}$ in region RD3

$$
\begin{equation*}
\psi_{s}=\psi_{C a} \text { in region } \mathrm{RC} 2 \tag{5.179}
\end{equation*}
$$

Most of the studies on DMG TFETs to date suggest that $L_{t}$ should be much smaller than $L_{a}$. Therefore, let us extend our model for a more general case where $L_{t}$ is much smaller than $L_{a}$. In such a case, the entire length of the channel under the tunnelling gate may be depleted, i.e. regions RD1 and RD2 merge into each other and region RC 1 does not exist. This will happen more so at low values of gate voltages where $L_{1}$ and $L_{2}$ are larger. In a structure where $L_{t}$ is small, typically below 20 nm , we will first solve Equations (5.164) to (5.174) and check if $L_{1}+L_{2}$ is larger than $L_{t}$. If it is true then we modify our surface potential models. Now we have only two depletion regions: RD1, which is the entire region under the tunnelling gate, and RD3, which is the same as earlier. We will now have two equations like (5.162) and six unknowns ( $C_{1}, D_{1}, L_{1}, C_{3}, D_{3}$ and $L_{3}$ ). We will again use six boundary conditions as earlier defining $x=0$ as the junction of the two gates.

The tunnelling gate length $L_{t}$ is the length of region RD1 now and not $L_{1}$. Since regions RD1 and RD2 have merged, we will get a point of minimum in the surface potential at $x=-L_{1}$. As a result, the condition given by (5.164) will be different now and will be as follows:

$$
\begin{equation*}
\psi_{S 1}\left(-L_{t}\right)=V_{b i} \tag{5.180}
\end{equation*}
$$

The other five boundary conditions given by Equations (5.165) to (5.169) remain the same but the variables and constants of region RD 2 are replaced by those of region RD1 (i.e. $\psi_{s 2}$ will become $\psi_{s 1}$ and so on). Solving as done earlier, we get the following:

$$
\begin{gather*}
C_{3}=D_{3}=\frac{\psi_{C a}-\psi_{G a}+\frac{q\left(N_{S}+n\right) L_{d}^{2}}{\varepsilon_{S i}}}{2}  \tag{5.181}\\
C_{1}=D_{1}=\left(V_{b i}-\psi_{G t}+\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}}\right) /\left(2 \cosh \left(\frac{-L_{t}+L_{1}}{L_{d}}\right)\right)  \tag{5.182}\\
=\frac{\psi_{b i}-\psi_{G t}+\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}}}{2 \cosh \left(\frac{-L_{t}+L_{1}}{L_{d}}\right)}\left(e^{L_{1} / L_{d}}+e^{-L_{1} / L_{d}}\right)+\psi_{G t}-\frac{q N_{S} L_{d}^{2}}{\varepsilon_{S i}}  \tag{5.183}\\
\frac{\psi_{G i}-\frac{q\left(N_{S}+n\right) L_{d}^{2}}{\varepsilon_{S i}}}{2}\left(e^{L_{3} / L_{d}}+e^{-L_{3} / L_{d}}\right)+\psi_{G a}-\frac{q\left(N_{S}+n\right) L_{d}^{2}}{\varepsilon_{S i}} \\
2 \cosh \left(\frac{-L_{t}+L_{1}}{L_{d}}\right)  \tag{5.184}\\
\varepsilon_{S i} \\
\left.2 N^{L_{1} / L_{d}}-e^{-L_{1} / L_{d}}\right)=\frac{\psi_{C a}^{2}-\psi_{G a}+\frac{q\left(N_{S}+n\right) L_{d}^{2}}{\varepsilon_{S i}}}{2}\left(e^{-L_{3} / L_{d}}-e^{L_{3} / L_{d}}\right)
\end{gather*}
$$

Simultaneously solving Equations (5.182) to (5.184) gives us $L_{1}, L_{3}$ and $C_{1}$. We can now write the final expression for the surface potential of the DMG TFET, in this case:

$$
\begin{equation*}
\psi_{s}(x)=C_{1} \exp \left(\frac{x-L_{1}}{L_{d}}\right)+D_{1} \exp \left(\frac{-\left(x-L_{1}\right)}{L_{d}}\right)+\psi_{G t}-\frac{q N_{A} L_{d}^{2}}{\varepsilon_{S i}} \text { in region RD1 } \tag{5.185}
\end{equation*}
$$

$\psi_{s}(x)=C_{3} \exp \left(\frac{x-L_{3}}{L_{d}}\right)+D_{3} \exp \left(\frac{-\left(x-L_{3}\right)}{L_{d}}\right)+\psi_{G a}-\frac{q\left(N_{A}+n\right) L_{d}^{2}}{\varepsilon_{S i}}$ in region RD3

$$
\begin{equation*}
\psi_{s}=\psi_{C a} \text { in region RC2 } \tag{5.186}
\end{equation*}
$$

We will now outline the infinite series method for finding the surface potential in a DMG TFET.

### 5.4.3.2 Infinite series method for DMG TFET

The infinite series method discussed in Section 5.3 can also be used for modelling the surface potential of a DMG TFET [24, 25]. In this section, we will provide a brief outline for using the infinite series method for modelling the surface potential of a DMG TFET. For this purpose, we will divide the structure of the DMG TFET into two parts along the boundary between the two gates and use the infinite series method individually in each part. This will give us two equations like Equation (5.107), as given below:

$$
\begin{align*}
& \psi_{t}(x, y)=v_{1}(y)+u_{L 1}(x, y)+u_{R 1}(x, y)  \tag{5.188}\\
& \psi_{a}(x, y)=v_{2}(y)+u_{L 2}(x, y)+u_{R 2}(x, y) \tag{5.189}
\end{align*}
$$

where $\psi_{t}(x, y)$ and $\psi_{a}(x, y)$ are the potential distributions under the tunnelling gate and the auxiliary gate, respectively.

We will first solve for the 1D Poisson equation in each of the two structures shown in Figure 5.20. This gives us $v_{1}(y)$ and $v_{2}(y)$, which are the solution to the 1D Poisson equation. We then solve for the 2D Laplace equation in each of the two structures to get $u_{L 1}(x, y), u_{R 1}(x, y), u_{L 2}(x, y)$ and $u_{R 2}(x, y)$. Their general form solutions are:

$$
\begin{equation*}
u_{L 1}(x, y) \sum_{n=0}^{\infty} c_{n 1} \frac{\sinh \left(\lambda_{n}\left(x-L_{1}\right)\right)}{\sinh \left(\lambda_{n} L_{1}\right)} \sin \left(\lambda_{n}\left(y+t_{S i}\right)\right) \tag{5.190}
\end{equation*}
$$



Figure 5.20 Boundary conditions for the potential distribution of a DMG TFET.

$$
\begin{gather*}
u_{R 1}(x, y) \sum_{n=0}^{\infty} d_{n 1} \frac{\sinh \left(\lambda_{n} x\right)}{\sinh \left(\lambda_{n} L_{1}\right)} \sin \left(\lambda_{n}\left(y+t_{S i}\right)\right)  \tag{5.191}\\
u_{L 2}(x, y) \sum_{n=0}^{\infty} c_{n 2} \frac{\sinh \left(\lambda_{n}\left(x-\left(L_{1}+L_{2}\right)\right)\right)}{\sinh \left(\lambda_{n}\left(L_{1}+L_{2}\right)\right)} \sin \left(\lambda_{n}\left(y+t_{S i}\right)\right)  \tag{5.192}\\
u_{R 2}(x, y) \sum_{n=0}^{\infty} d_{n 2} \frac{\sinh \left(\lambda_{n}\left(x-L_{1}\right)\right)}{\sinh \left(\lambda_{n}\left(L_{1}+L_{2}\right)\right)} \sin \left(\lambda_{n}\left(y+t_{S i}\right)\right) \tag{5.193}
\end{gather*}
$$

The values of the coefficients $c_{n 1}, d_{n 1}, c_{n 2}$ and $d_{n 2}$ can be found using source and drain side boundary conditions and the continuity of electric field and potential at the interface between the two gates. Exact working out of this model is beyond the scope of this book but readers are encouraged to further explore this model.

We will now move towards modelling the surface potential in a TFET in the presence of localised charges at the silicon-oxide interface.

### 5.5 The effect of localised charges on the surface potential

TFETs are highly prone to hot-carrier effects at the source-channel region due to high lateral electric fields. These hot carriers get injected into the gate creating permanent damage in the oxide, leading to the presence of localised charges at the silicon-oxide interface. This changes the flat-band voltage of the gate above the damaged region, which causes a change in the surface potential profile in the tunnelling of a TFET and a change in the drain current [12]. Hence, it is important


Figure 5.21 Schematic of a p-channel dual soi TFET showing the presence of localised charges at the si-sio ${ }_{2}$ interface near the source side.
to model the surface potential in the presence of localised charges at the siliconoxide interface near the source side of a TFET, as shown in Figure 5.21. The approach followed to model the surface potential in a DMG TFET can also be extended here $[16,19]$. Let us assume the density of the localised charges to be $N_{f}$ and the length of the affected region to be $L_{D}$. The flat-band voltage of the gate in the presence of localised charges is given by

$$
\begin{equation*}
V_{F B}=V_{F B 0}+\frac{q N_{f}}{C_{o x}} \tag{5.194}
\end{equation*}
$$

where $V_{F B O}$ is the flat-band voltage of the gate without any localised charge. Hence, the surface potential in a TFET in the presence of localised charges can be modelled as the surface potential in a DMG TFET having two gates with the following gate potentials:

$$
\begin{equation*}
\psi_{G 1}=V_{G S}-V_{F B}=V_{G S}-V_{F B 0}+\frac{q N_{f}}{C_{o x}} \text { equivalent to the tunnelling gate } \tag{5.195}
\end{equation*}
$$

and

$$
\begin{equation*}
\psi_{G 2}=V_{G S}-V_{F B}=V_{G S}-V_{F B 0} \text { equivalent to the auxiliary gate } \tag{5.196}
\end{equation*}
$$

### 5.6 Surface potential in the depletion regions

In the surface potential model described so far, we have neglected the depletion region in the source side at the source-body interface. We assumed the surface potential to be constant $\left(V_{b i}\right)$ throughout the source region. Such an assumption


Figure 5.22 Schematic of an n-channel TFET showing source and drain depletion regions.
holds very well for high source doping (which is generally the case with TFETs). However, as a more general case, let us now discuss modelling the surface potential inside the source depletion region.

We will now calculate the surface potential in the depletion regions in the device and observe the effects that inclusion of the depletion regions has on the surface potential within the channel.

Figure 5.22 shows the schematic of the device that is being modelled. The device is divided into three regions - the source side depletion region (R1), the channel (R2) and the drain side depletion region (R3). Due to a different doping in each region, Poisson's equation is written separately in each of the three regions:

$$
\begin{equation*}
\frac{\partial^{2} \psi_{s, i}}{\partial x^{2}}+\frac{\partial^{2} \psi_{s, i}}{\partial y^{2}}=\frac{-q N_{i}}{\epsilon_{S i}} \tag{5.197}
\end{equation*}
$$

where $\psi_{s, i}$ and $N_{i}$ are the surface potential and doping in the $i$ th region, respectively. Using the pseudo-2D method outlined in the preceding section, the surface potential is approximated as a parabola in the $y$-direction in each region. The boundary conditions used (Equations (5.4) and (5.5)) to determine the coefficients of this parabola will also be the same as described in the preceding section. However, the boundary condition relating to the continuity of the vertical electric displacement (Equation (5.6)) will be modified in the depletion regions. This is because the gate-body capacitance is different for the channel $\left(C_{o x}\right)$ and for the depletion regions. Therefore, in the depletion regions, we must use the fringing field capacitance $\left(C_{o x, f}=(2 / \pi) C_{o x}\right)$ of the gate [6,29]. For example, in the source depletion region (R1):

$$
\begin{equation*}
\left.\frac{\partial \psi_{s, 1}}{\partial y}\right|_{y=0}=\frac{-C_{o x, f}\left(\psi_{G}-\psi_{s, 1}\right)}{\epsilon_{S i}} \tag{5.198}
\end{equation*}
$$

Therefore, in the source depletion region, the above equation leads to the following expression for the surface potential:

$$
\begin{gather*}
\psi_{s, 1}=C_{1} \exp \left(\frac{x-x_{0}}{L_{d, 1}}\right)+D_{1} \exp \left(-\frac{x-x_{0}}{L_{d, 1}}\right)+\psi_{G}-\frac{2 q N_{1} L_{d, 1}^{2}}{\pi \epsilon_{s i}}  \tag{5.199}\\
L_{d, 1}=\sqrt{\frac{\pi t_{s i} t_{o x} \epsilon_{S i}}{2 \epsilon_{o x}}} \tag{5.200}
\end{gather*}
$$

Similar equations can be written in the drain depletion region, giving $\psi_{s, 3}$ in the drain depletion region (R3). The surface potential in the channel $\psi_{s, 2}$ will have the same form as previously discussed (Equation (5.26). Now that we have the surface potentials in all the three regions, we need to solve for the coefficients $C_{i}$ and $D_{i}$ and depletion region lengths $L_{1}$ and $L_{3}$ by writing the electrostatic boundary conditions in the horizontal direction. Beginning from the source side, the potential should be equal to the source potential at $x=x_{0}$ and the field at the depletion region edge should be zero:

$$
\begin{gather*}
\psi_{s, 1}\left(x_{0}\right)=V_{S}+V_{b i, 1}  \tag{5.201}\\
\left.\frac{\partial \psi_{s, 1}}{\partial x}\right|_{x=x_{0}}=0 \tag{5.202}
\end{gather*}
$$

Similar boundary conditions can be written on the drain side depletion region, at $x=x_{3}$ :

$$
\begin{gather*}
\psi_{s, 3}\left(x_{3}\right)=V_{D S}+V_{b i, 3}  \tag{5.203}\\
\left.\frac{\partial \psi_{s, 3}}{\partial x}\right|_{x=x_{3}}=0 \tag{5.204}
\end{gather*}
$$

At the source-channel $(i=2)$ and channel-drain boundaries $(i=3)$, we impose conditions of continuity of the surface potential and electric displacement:

$$
\begin{align*}
\psi_{s, i-1}\left(x_{i-1}\right) & =\psi_{s, i}\left(x_{i-1}\right)  \tag{5.205}\\
\left.\frac{\partial \psi_{s, i-1}}{\partial x}\right|_{x=x_{i-1}} & =\left.\frac{\partial \psi_{s, i}}{\partial x}\right|_{x=x_{i-1}} \tag{5.206}
\end{align*}
$$

Solution of these boundary conditions is not analytically possible, since Equations (5.201) to (5.206) lead to transcendental equations in the depletion region lengths. Therefore, we need to approximate the depletion region lengths.

This is done by applying the diode approximation to the source-channel and channel-drain depletion regions and by using the equations for the depletion region length in a diode:

$$
\begin{align*}
& L_{1}=\sqrt{\frac{2 \epsilon_{S i}\left|\psi_{G}-V_{S}-V_{b i, 1}\right|\left|N_{2}\right|}{q\left|N_{1}\right|\left(\left|N_{2}\right|+\left|N_{1}\right|\right)}}  \tag{5.207}\\
& L_{3}=\sqrt{\frac{2 \epsilon_{S i}\left|\psi_{G}-V_{D S}-V_{b i, 2}\right|\left|N_{2}\right|}{q\left|N_{3}\right|\left(\left|N_{2}\right|+\left|N_{3}\right|\right)}} \tag{5.208}
\end{align*}
$$

Using the above equations and those resulting from the boundary conditions (Equations (5.201) to (5.206), we get a system of linear equations in the coefficients $C_{i}$ and $D_{i}$ that we can solve to obtain the surface potential in each region. This procedure gives us the surface potential in the device while including the effects of the source and drain side depletion regions.

Note that while considering the source and drain side depletion regions, we did not include the effect of inversion charges. In the next section, we will study a method to include this effect by using smoothing functions.

### 5.7 Use of smoothing functions in the surface potential models

Smoothing functions are widely used in circuit simulations and are a method of mathematically switching from one regime of device operation to another.

Studies analysing the effect of inversion charges in TFETs have shown that the inversion charge concentration increases significantly after the gate potential $\psi_{G}$ either (i) increases above the drain potential $\Psi_{\text {drain }}=V_{D S}+V_{b i, 2}$ or (ii) decreases below the source potential $\Psi_{\text {source }}=V_{S}+V_{b i, 1}$. Figure 5.23 shows the variation in the mid-channel potential of a TFET with varying gate bias. It can be observed that when $\Psi_{\text {source }} \leq \psi_{G} \leq \Psi_{\text {drain }}$, the inversion charge concentration is negligible, the mid-channel potential increases linearly with gate voltage and the slope is unity. However, when $\psi_{G} \geq \Psi_{\text {drain }}$ or $\psi_{G} \leq \Psi_{\text {source }}$, the inversion layer charge is significant. In this case, the increase in the channel potential with respect to the gate voltage is still linear, but with a slope reduced from 1 to $(1-\eta)$, where $\eta$ is an empirical parameter [30].

To capture the surface potential variation as described above (Figure 5.23), a semi-empirical parameter called the "effective gate potential" $\psi_{G, e f f}$ is used in place of the gate potential $\psi_{G}$ to calculate the surface potential in the channel region. The behaviour of $\psi_{g, \text { eff }}$ is such that when $\Psi_{\text {source }} \leq \psi_{G} \leq \Psi_{\text {drain }}$,


Figure 5.23 Variation in the mid-channel potential of an n-channel TFET with varying gate voltage $\left(V_{G S}\right)$ [30]. Source: Reproduced with permission of IEEE.

$$
\begin{equation*}
\psi_{g, e f f}=\psi_{g} \tag{5.209}
\end{equation*}
$$

that is $\psi_{g, e f f}$ increases linearly with $\psi_{g}$ with a slope of 1 . When $\psi_{g} \geq \Psi_{\text {drain }}$, pinning of the surface potential occurs on the drain side and the increase in $\psi_{g, e f f}$ is now linear with a reduced slope of $(1-\eta)$ :

$$
\begin{equation*}
\psi_{g, e f f}=\eta \Psi_{\text {drain }}+(1-\eta) \psi_{g} \tag{5.210}
\end{equation*}
$$

Similarly, when $\psi_{g} \leq \Psi_{\text {source }}$, pinning of the channel potential occurs on the source side and the increase in $\psi_{g, e f f}$ is linear with a reduced slope $(1-\eta)$ :

$$
\begin{equation*}
\psi_{g, e f f}=\eta \Psi_{\text {source }}+(1-\eta) \psi_{g} \tag{5.211}
\end{equation*}
$$

To model the transitions from Equations (5.210) to (5.211), a semi-empirical approach is adopted by using the following smoothing function:

$$
\begin{equation*}
\psi_{g, e f f}=\psi_{g}-\eta \phi_{t} \ln \left(1+e^{\left(\psi_{g}-\Psi_{\text {drain }}\right) / \phi_{t}}\right)+\eta \phi_{t} \ln \left(1+e^{\left(\psi_{\text {suurce }}-\psi_{g}\right) / \phi_{t}}\right) \tag{5.212}
\end{equation*}
$$

where $\phi_{t}$ is a dimensionless empirical smoothing parameter obtained by fitting the simulated and the modelled transfer characteristics [15]. Physically, when the gate potential increases to a point where $\left(\psi_{g}-\Psi_{\text {drain }}\right)$ is of the order of $\phi_{t}$ or more, there
is an appreciable change in the slope of the surface potential, as shown in Figure 5.23. The exponential nature of the above function ensures the continuity and infinite differentiability of the surface potential, leading to the continuity and infinite differentiability of all the obtained characteristics. While the pseudo-2D method gives us results with reasonable accuracy and also provides analytical expressions for the surface potential, it is not able to predict the effects of inversion charges in a rigorous manner. In the next section, we will study the variational approach towards modelling the surface potential of a TFET, which predicts the effect of inversion charges in a TFET from ab initio theoretical principles rather than semi-empirical formulations.

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## 6

## Modelling the drain current

The problem of modelling the drain current in a TFET can be divided into two major parts - finding the surface potential and finding the tunnelling rate. The former has been discussed in the preceding chapter. In this chapter, we will outline the common methods used to find the tunnelling rate in a TFET and thus obtain the drain current in the device.

The band diagram of a TFET in the ON-state is shown in Figure 6.1(a) and the band diagram of the source-channel junction is shown in greater detail in Figure 6.1(b). In the ON-state of the TFET, most of the current is due to tunnelling across the source-channel junction, depicted by the horizontal arrow in Figure 6.1(b). We can consider this tunnelling from two perspectives - electrons at different energies $E_{i}$ tunnelling from the point $P_{i}$ in the source valence band to the point $Q_{i}$ in the channel valence band, and particles at different positions $x_{i}$ tunnelling from $P_{i}$ to $Q_{i}$. The former approach usually corresponds to a non-local perspective of tunnelling, in which case the tunnelling probability is dependent on the potential in the entire region from the point $P_{i}$ to the point $Q_{i}$. The latter approach usually applies a local perspective to tunnelling, with the tunnelling probability dependent only on the electric field at the initial position $x_{i}$.

It should also be noted that the tunnelling probability is different for every position $x_{i}$ (or energy $E_{i}$, depending on our perspective). Therefore, to find the tunnelling rate throughout the device, we need to integrate the individual tunnelling probabilities obtained for every initial position (or energy). Since the functions describing the tunnelling probability are quite complicated, this integration

[^5]

Distance along the channel

Figure 6.1 (a) Band diagram of an n-channel TFET at the surface in the ON state. (b) Band diagram in the source-channel junction.
may not have a closed form solution, and evaluating it often requires various approximations.

In this chapter, we will discuss the local and non-local methods used to find the tunnelling probability in TFETs. Corresponding to each method, we will first obtain an expression for the tunnelling probability from the surface potential.

We will next study various techniques used to integrate the tunnelling probabilities. We would then be able to calculate the current for a given biasing and obtain the transfer and output characteristics of the device. Finally, using the device characteristics thus obtained, we will discuss different definitions of the threshold voltage of a TFET.

### 6.1 Non-local methods

### 6.1.1 Landauer's tunnelling formula in TFETs

The non-local method considers tunnelling to occur at an energy level $E_{i}$, from the point $P_{i}$ in the source valence band to the point $Q_{i}$ in the channel valence band, as shown in Figure 6.1(b). Using Landauer's tunnelling formula [1-6] (Equation (2.44)), we can write the drain current $I_{D}$ as

$$
\begin{equation*}
I_{D}=\frac{2 q}{\hbar} \int_{E_{C, \text { Channel }}}^{E_{V, \text { Source }}}\left(f_{P_{i}}\left(E_{i}\right)-f_{Q_{i}}\left(E_{i}\right)\right) T_{B T B T}\left(E_{i}\right) d E_{i} \tag{6.1}
\end{equation*}
$$

where $f_{P_{i}}\left(E_{i}\right)$ and $f_{Q_{i}}\left(E_{i}\right)$ are the values of the Fermi functions for the energy level $E_{i}$ at the point $P_{i}$ in the source valence band and $Q_{i}$ in the channel valence band, respectively; $T_{B T B T}\left(E_{i}\right)$ is the probability of the electron tunnelling from $P_{i}$ to $Q_{i}$.

The integral for the device current (Equation (6.1)) involves the product of two terms - the difference in occupancy and the tunnelling probability. Recalling our discussion on Landauer's tunnelling formula (Section 2.3), we note that tunnelling occurs only when there are (i) occupied energy levels in the source valence band from where electrons can tunnel and (ii) unoccupied energy levels in the channel conduction band to which electrons can tunnel. This is captured by the term $\left(f_{P_{i}}\left(E_{i}\right)-f_{Q_{i}}\left(E_{i}\right)\right)$, signifying the difference in occupancy levels at energy $E_{i}$ between the source valence band and the channel conduction band. These occupancies can be given by the Fermi-Dirac distribution as

$$
\begin{align*}
f_{P_{i}}\left(E_{i}\right) & =\frac{1}{1+\exp \left(\left(E_{i}-E_{F, \text { Source }}\right) / k T\right)}  \tag{6.2}\\
f_{Q_{i}}\left(E_{i}\right) & =\frac{1}{1+\exp \left(\left(E_{i}-E_{F, \text { Channel }}\right) / k T\right)} \tag{6.3}
\end{align*}
$$

where $E_{F, \text { Source }}$ and $E_{F, \text { Channel }}$ are the Fermi energy levels in the source and drain, respectively. The integration (6.1) is carried out for all energy levels from which tunnelling can take place, starting from the valence band edge of the source $E_{V, \text { Source }}$ to the conduction band edge of the channel $E_{C, \text { Channel }}$.

To evaluate the integral (6.1), we need to find the tunnelling probability $T_{B T B T}\left(E_{i}\right)$. In non-local models, the most widely used method to calculate the tunnelling probability is the WKB approximation, which we will study in the following section.

### 6.1.2 WKB approximation in TFETs

In Section 2.2, we learnt the WKB approximation, which enabled us to calculate the tunnelling probability for a general potential barrier. We will now use the WKB approximation to find the probability of an electron in the valence band of the source tunnelling to the conduction band of the channel. From Equations (2.28) and (2.30), we can write the tunnelling rate $T_{B T B T}$ given by the WKB approximation as

$$
\begin{gather*}
T_{B T B T}=e^{-2 \gamma}  \tag{6.4}\\
\gamma=\int_{x_{P_{i}}}^{x_{Q_{i}}}\left|\sqrt{\frac{2 m(V(x)-E)}{\hbar^{2}}}\right| d x \tag{6.5}
\end{gather*}
$$

In the preceding chapter, we studied different methods used to obtain the surface potential in the device if the biasing is known. We will now study how the surface potential $\psi_{s}(x)$ can be converted into the potential barrier $V(x)$ of the tunnelling problem.

Let us examine the band diagram of the device in the ON-state, focusing our attention to the region in which tunnelling is taking place, as shown in Figure 6.2. Since $E / q=\psi$, the shape of the surface potential gives us the shape of the energy band edges of the material. Hence, if we consider $q \psi_{s}(x)$ to be the valence band energy, the plot of the conduction band would be given by $E_{g}+q \psi_{s}(x)$, where $E_{g}$ is the bandgap of the material. Examining the tunnelling path taken by an electron at an arbitrary position $P_{i}$ and energy $E_{i}$ in the valence band of the source, the potential barrier that the electron encounters (the shaded shape in Figure 6.2) is given as

$$
\begin{equation*}
V(x)=E_{g}+q \psi_{s}(x) \tag{6.6}
\end{equation*}
$$

and this barrier $V(x)$ extends from $P_{i}$ to $Q_{i}$. Therefore, the term $(V(x)-E)$ in the WKB approximation (Equation (6.5)) is

$$
\begin{equation*}
V(x)-E=V(x)-E_{i}=E_{g}+q \psi_{s}(x)-E_{i} \tag{6.7}
\end{equation*}
$$

Our problem is not completely solved yet - while the integrand of Equation (6.5) is known, we need to obtain the limits of integration $x_{P_{i}}$ and


Distance along the channle

Figure 6.2 Band diagram in the source-channel junction of the TFET in the ON-state showing the tunnelling of an electron across the bandgap.
$x_{Q_{i}}$. Since we have considered $q \psi_{s}(x)$ to represent the valence band energy, at the position $x_{P_{i}}$, we must have $q \psi_{s}\left(x_{P_{i}}\right)=E_{i}$. At the final point $Q_{i}$, the conduction band energy equals the initial energy $E_{i}$. Since the conduction band energy is given as $E_{g}+q \psi_{s}(x)$, we can find the position $x_{Q_{i}}$ by solving

$$
\begin{equation*}
q \psi_{S}\left(x_{Q_{i}}\right)+E_{g}=E_{i}=q \psi_{s}\left(x_{P_{i}}\right) \tag{6.8}
\end{equation*}
$$

We now have the shape and width of the potential barrier for an electron at any arbitrary energy $E_{i}$ and we can find the tunnelling probability $T\left(E_{i}\right)$ for this energy if the surface potential in the device $\psi_{s}(x)$ is known. To better understand this process, let us now find the current in the device after assuming a particular form of the surface potential.

### 6.1.3 Obtaining the drain current

Till this point, we have written expressions to find the tunnelling probability and the current for a general surface potential $\psi_{s}(x)$. However, due to the complicated integrals involved (Equations (6.1) and (6.5)), evaluation of the current is very difficult without using approximations.

A widely used method to solve this problem involves two approximations the potential in the tunnelling region is linear and the tunnelling probability $T\left(E_{i}\right)$ in Equation (6.1) is constant. The first approximation simplifies the process of obtaining the tunnelling probability in Equation (6.4). The second approximation


Figure 6.3 Tunnelling of an electron across in a linear potential profile.
brings the term $T\left(E_{i}\right)$ out of the integral in Equation (6.1), making it easier to evaluate the integral and obtain the current.

Let us first find the tunnelling probability $T\left(E_{i}\right)$ when the potential in the tunnelling region is linear, as shown in Figure 6.3. In this case, the tunnelling barrier is the triangle $P_{i} Q_{i} R_{i}$. The side $R_{i} Q_{i}$ of this triangle represents the term $(V(x)-E)$ of Equation (6.5). Since the potential is linear, the longitudinal electric field $E_{\perp}$ in the tunnelling region is constant. Taking the origin at the initial position $P_{i}$ (i.e. $x_{P_{i}}=0$ ), and using the fact that the surface potential is linear with a slope $-q E_{\perp}$, we can rewrite Equation (6.7) as

$$
\begin{equation*}
V(x)-E=E_{g}-q E_{\perp} x \tag{6.9}
\end{equation*}
$$

Using Equation (6.9) in Equation (6.8), we get the position $x_{Q_{i}}$ of the final point $Q_{i}$ as

$$
\begin{equation*}
x_{Q_{i}}=E_{g} / q E_{\perp} \tag{6.10}
\end{equation*}
$$

We can now rewrite the WKB integral (Equation (6.5)) using the above Equations (6.9) and (6.10):

$$
\begin{equation*}
\gamma=\int_{0}^{E_{g} / q E_{\perp}}\left|\sqrt{\frac{2 m\left(E_{g}-q E_{\perp} x\right)}{\hbar^{2}}}\right| d x \tag{6.11}
\end{equation*}
$$

Evaluating Equation (6.11) gives

$$
\begin{equation*}
\gamma=\frac{2 \sqrt{2 m E_{g}^{3}}}{3 q \hbar E_{\perp}} \tag{6.12}
\end{equation*}
$$

Substituting the value of $\gamma$ in Equation (6.4), the tunnelling probability can be written as

$$
\begin{equation*}
T_{B T B T}=\exp \left(-\frac{4 \sqrt{2 m E_{g}^{3}}}{3 q \hbar E_{\perp}}\right) \tag{6.13}
\end{equation*}
$$

Equation (6.13) is obtained by approximating potential in the tunnelling region to be linear. Let us now use the approximation of a constant tunnelling probability to evaluate the integral in Landauer's tunnelling formula (Equation (6.1)). The variable of integration is the energy $E_{i}$, and the only functions of energy within the integral correspond to the occupancy levels in the source $\left(f_{P_{i}}\left(E_{i}\right)\right)$ and in the channel $\left(f_{Q_{i}}\left(E_{i}\right)\right.$ ) given by Equations (6.2) and (6.3). Substituting Equations (6.2), (6.3) and (6.13) in Equation (6.1) and evaluating the resulting integral, we get the drain current $I_{D}$ as
$I_{D}=\frac{2 q k T}{\hbar} T_{B T B T} \ln \left|\frac{\left(1+\exp \left(E_{F, \text { Source }}-E_{C, \text { Channel }} / k T\right)\right)\left(1+\exp \left(E_{F, \text { Channel }}-E_{V, \text { Source }} / k T\right)\right)}{\left(1+\exp \left(E_{F, \text { Source }}-E_{V, \text { Source }} / k T\right)\right)\left(1+\exp \left(E_{F, \text { Channel }}-E_{C, \text { Channel }} / k T\right)\right)}\right|$

It should be noted that while developing this drain current model, the longitudinal electric field $E_{\perp}$ is considered to be constant. However, the surface potential model would usually result in a varying electric field in the tunnelling region. Therefore, many different approximations are used to find the appropriate value of $E_{\perp}$ in Equation (6.13). Some methods take the maximum electric field in the tunnelling region to be $E_{\perp}$, but this leads to overestimation of the drain current. This problem can be solved by taking the average of the minimum and maximum electric field or by calculating the average electric field in the tunnelling region and taking it to be $E_{\perp}$. This approach also has a drawback, since the contribution of a higher electric field to the tunnelling current is far greater than that of a lower electric field due to the exponential dependence of the band-to-band generation rate on the electric field (Equation (6.13)). Another commonly used approach is to write $E_{\perp}$ in terms of the screening length $\lambda$ of the device:

$$
\begin{equation*}
E_{\perp}=\frac{E_{G}+\left(E_{V, \text { Source }}-E_{C, \text { Channel }}\right)}{q \lambda} \tag{6.15}
\end{equation*}
$$

As described in Section 2.4, a completely non-local approach is more accurate than a local approach to find the drain current of a TFET. However, the approximations that we have made in order to obtain an analytical result for the drain current lead to certain disadvantages. The non-local method is able to effectively capture the effect of the density of states and occupancy levels in the source and the channel; it will be clear from the next section that local methods are not able to accomplish this. On the other hand, due to the inclusion of terms relating to the density of states, the non-local methods predict a zero current before there is any overlap between the source and channel bands. Experimental results show that this is not the case and TFETs do have a non-zero subthreshold current. Finally, due to the approximation of a constant electric field (linear potential), this method is not able to incorporate the effects of the varying electric field within the channel. The local methods to calculate the current in a TFET that we will study in the next section provide a suitable alternative - they are able to predict the subthreshold current and can incorporate the effects of the varying electric field throughout the device.

### 6.2 Local methods

In the previous section, we studied the non-local approach to tunnelling, which considers electrons at different energies $E_{i}$ tunnelling from the source valence band to the channel conduction band. Due to this perspective, the variable of integration in the case of a non-local approach (Equation (6.1)) was energy. We will now study the local approach for modelling the drain current in a TFET, which considers electrons at different points $x_{i}$ tunnelling from the source valence band to the channel conduction band. In this case, to obtain the drain current $I_{D}$, the variable of integration is volume:

$$
\begin{equation*}
I_{D}=q \int G_{b t b} d V \tag{6.16}
\end{equation*}
$$

where $G_{b t b}$ is the band-to-band generation rate at the position of the volume element $d V$ and is usually obtained by using Kane's model [7] (Section 2.4.2.1):

$$
\begin{equation*}
G_{b t b}=\frac{E^{2.5} m_{r}^{1 / 2}}{18 \pi \hbar^{2} E_{G}^{1 / 2}} \exp \left\{\frac{-\pi m_{r}^{1 / 2} E_{G}^{3 / 2}}{2 \hbar|E|}\right\}=A \frac{E^{2.5}}{E_{G}^{1 / 2}} \exp \left\{-B \frac{E_{G}^{3 / 2}}{|E|}\right\} \tag{6.17}
\end{equation*}
$$

The above expression gives the band-to-band generation rate at any point in the device in terms of the local electric field $E$ at that point. Integrating this generation rate over the entire device volume gives the total number of carriers
generated due to tunnelling. As explained in (Section 2.4.2.1), it is assumed that the drain bias sweeps all the generated carriers towards the drain. Therefore, the drain current is equal to the rate of the total number of carriers generated due to tunnelling, and is given by integrating $G_{b t b}$ throughout the device volume.

However, integrating $G_{b t b}$ throughout the device may not be the most computationally efficient method to obtain the drain current. Observing the band diagram along the surface of a TFET in the ON -state (Figure 6.1(a)), we can see that electrons are tunnelling only in the depletion region of the source-channel junction (which is termed the tunnelling region). Hence, to obtain the drain current, it is sufficient to integrate $G_{b t b}$ only in the tunnelling region.

The expression for $G_{b t b}$ (Equation (6.17)) shows that it is a function of the electric field at a given point. Therefore, if the potential in the device is known, its derivative will give us the field, which can be subsequently used in Equation (6.17) to calculate $G_{b t b}$ at every point. We can then integrate $G_{b t b}$ over the tunnelling region to obtain the drain current. However, the expression for $G_{b t b}$ has polynomial as well as exponential dependency on the electric field. Due to this, in most cases there is no closed form integration of $G_{b t b}$. Therefore, if an analytical expression for the drain current is required, we need to explore different approximations to evaluate this integral. We will now discuss different methods for finding the integral of $G_{b t b}$ over the volume of the tunnelling region.

### 6.2.1 Numerical integration

The most straightforward way of finding the integral given in Equation (6.16) is to evaluate it numerically [8-11]. We divide the length of the tunnelling region into extremely small steps $(\Delta x)$ of $x$ and take the summation of $G_{b t b}(x) \Delta x$ :

$$
\begin{equation*}
I_{D}=q \sum_{n=0}^{0} G_{b t b}\left(x_{n}\right) \Delta x ; n=1,2,3, \ldots, N \tag{6.18}
\end{equation*}
$$

where $N$ is a large integer. This gives us the most accurate value of the total tunnelling generation rate $\left(G_{b t b}\right)$ in the tunnelling region. However, this summation involves a large number of steps and requires a huge amount of computational resources, making it highly inefficient for circuit simulation. Moreover, this method does not give us an analytical expression for the drain current of a TFET. We will now move to different methods that approximate the integration of $G_{b t b}$ and give us an analytical expression for the drain current.

### 6.2.2 Shortest tunnelling length

In the previous section, the tunnelling generation rate was integrated numerically, which is an accurate but computationally inefficient method of obtaining the drain
current. More importantly, as it does not give an analytical expression, it provides little insight into the process of tunnelling in the device. Hence, it is very important to develop methods that are compact and analytical, so as to provide faster computation and better insights into the device.

To get a closed-form expression of the tunnelling generation rate $\left(G_{b t b}\right)$, we need to approximate the integration given in Equation (6.16). One way to approximate the integral is to exploit the exponential dependence of $G_{b t b}$ on the electric field. As $G_{b t b}$ is an exponential function of the electric field, it dies out very quickly with a decrease in the electric field. Hence, we can calculate $G_{b t b}$ at the region of maximum electric field and neglect the $G_{b t b}$ in the rest of the tunnelling region [12]. Let us now have a look at Figure 6.4, which shows the surface potential in a TFET in the ON-state. As can be seen, the surface potential curve is steepest at the source-channel junction and hence has the maximum electric field at the source-channel junction. However, since tunnelling is a process that takes place across a tunnelling width, we use the average electric field $\left(E_{T W}\right)$ over the shortest tunnelling length $\left(L_{T W}\right)$ to find $G_{b t b}$ from Equation (6.15) and multiply it with a constant so as to find the drain current.

The shortest tunnelling length $\left(L_{T W}\right)$ is the distance from the source-channel junction over which the surface potential drops by $E_{g} / q$ in a p-TFET and vice versa in an n-TFET, as shown in Figure 6.4. In other words, the shortest tunnelling length is the shortest distance over which we get an alignment between the conduction band in the source and the valence band in the channel. Hence, $L_{T W}$ can be written as


Figure 6.4 Surface potential of a p-channel TFET in the ON-state.

$$
\begin{equation*}
L_{T W}=x\left[\psi\left(\text { source }-E_{G} / q\right)\right]-x[\psi(\text { source })] \tag{6.19}
\end{equation*}
$$

The average electric field $\left(E_{T W}\right)$ over the shortest tunnelling length $\left(L_{T W}\right)$ is given by

$$
\begin{equation*}
E_{T W}=\frac{E_{G}}{q L_{T W}} \tag{6.20}
\end{equation*}
$$

Now the drain current can be approximated to be proportional to $G_{b t b}$ given by Equation (6.15) with $E=E_{T W}$ :

$$
\begin{equation*}
I_{D} \propto A \times \frac{E_{T W}^{2.5}}{E_{G}^{0.5}} \times \exp \left[-B \frac{E_{G}^{1.5}}{E_{T W}}\right] \tag{6.21}
\end{equation*}
$$

Since the tunnelling of carriers is taking place in the entire thickness of the inversion layer, as shown in Figure 4.1, we have to multiply the above expression by a constant factor $A_{1}$, which incorporates the thickness of the inversion layer:

$$
\begin{equation*}
I_{D}=A_{1} \times A \times \frac{E_{T W}^{2.5}}{E_{G}^{0.5}} \times \exp \left[-B \frac{E_{G}^{1.5}}{E_{T W}}\right] \tag{6.22}
\end{equation*}
$$

The above model (6.20), is a closed-form expression for the drain current of the TFET. It can use any of the surface potential models discussed in Chapter 5 to find $L_{T W}$ and then use it in Equations (6.17) to (6.20) to give us the drain current. The above model, however, is not very accurate, especially for low gate voltages when the shortest tunnelling length is not very small. At small gate voltages, the surface potential profile at the source-channel junction is not very steep, and hence the distance over which the surface potential drops by $E_{g} / q$ is large. Therefore, the shortest tunnelling length $L_{T W}$ has a large value for small gate voltages. For large values of $L_{T W}$, the expression given by Equation (6.19) is not dominant as compared to the tunnelling generation rate $\left(G_{b t b}\right)$ at other areas in the tunnelling region. In other words, the above model is accurate only when $L_{T W}$ is small so that the value of $G_{b t b}$ given by Equation (6.16) due to its exponential dependence on $E_{T W}$ becomes large as compared to $G_{b t b}$ at other areas in the tunnelling region, making it possible for us to neglect $G_{b t b}$ in those areas.

We will now move on to a method where the approximation of the integral of $G_{b t b}$ over the volume of the tunnelling region is done in a different way altogether.

### 6.2.3 Constant polynomial term assumption

In the previous section, we discussed a method of finding the integration of $G_{b t b}$, which used the exponential dependence of $G_{b t b}$ on the electric field for
approximating the integration. In this section, however, we will discuss a different approach for approximating the integral given by Equation (6.16) [13-15].

Let us rewrite Equation (6.17) as

$$
\begin{equation*}
G_{b t b}=\frac{E^{2.5} m_{r}^{1 / 2}}{18 \pi \hbar^{2} E_{G}^{1 / 2}} \exp \left\{\frac{-\pi m_{r}^{1 / 2} E_{G}^{3 / 2}}{2 \hbar|E|}\right\}=A \frac{E^{2.5}}{E_{G}^{1 / 2}} \exp \left\{-B \frac{E_{G}^{3 / 2}}{|E|}\right\} \tag{6.23}
\end{equation*}
$$

Here we can see that $G_{b t b}$ has both exponential and polynomial terms in the electric field, which makes it impossible to get a closed-form expression for the integration of $G_{b t b}$. However, as the polynomial term is expected to change more slowly with the electric field, as compared to the exponential term, we can assume the polynomial term $E(x)^{2.5}$ to be constant in the tunnelling region and integrate only in the exponential term. This gives us the following equation:

$$
\begin{equation*}
I_{D}=\int G_{b t b} d x=\int A \frac{E(x)^{2.5}}{E_{G}^{1 / 2}} \exp \left\{-B \frac{E_{G}^{3 / 2}}{|E(x)|}\right\} d x=A \frac{E\left(x_{0}\right)^{2.5}}{E_{G}^{1 / 2}} \int \exp \left\{-B \frac{E_{G}^{3 / 2}}{|E(x)|}\right\} d x \tag{6.24}
\end{equation*}
$$

where the integration is carried out over the length of the tunnelling region. The position $x=x_{0}$ at which the constant value of the polynomial term is chosen can either be at the source-channel interface (position of the maximum electric field) or at any suitable average value. The expression for $E(x)$ is given by using any of the surface potential models discussed in Chapter 5, which would enable us to write the integral in terms of $x$. The final form of expression of Equation (6.24) will, therefore, depend on the surface potential model used. As this model takes into account the varying electric field by incorporating it into the integral, it intuitively appears to be more accurate than the shortest tunnelling length approach, where only a constant electric field was used in the expression (Equation (6.21)) to calculate the device current. However, for steeper potential profiles at a high gate bias, the variation in the polynomial term $E\left(x_{0}\right)^{2.5}$ in Equation (6.24) may also be significant, weakening the approximation we have made here and reducing the accuracy. Despite this shortcoming, this approach of approximation of the integral will be more accurate than the shortest tunnelling length method.

The approaches for approximating the integral of the tunnelling generation rate that we have discussed so far have limited accuracy. Even after proper calibration of the constant terms $A$ and $B(6.23)$, these models fail to predict the drain current accurately in the entire range of gate voltages. Hence, it is necessary to develop a unified approach that gives us a compact (or closed-form) expression for the drain current of a TEFT that is accurate in the entire range of gate voltages.

We will now move on to the next approach of approximating the integral of the tunnelling generation rate. This approach is graphical and makes use of the differential (or the slope) of the expression given in Equation (6.17) to evaluate the integral of $G_{b t b}$, which would give us the total tunnelling generation rate in the device.

### 6.2.4 Tangent line approximation

The methods for approximating the integral of $G_{b t b}$ discussed so far have limited accuracy. We will now discuss a method that, along with being compact and analytical, can also achieve a high level of accuracy in evaluating the integral of $G_{b t b}$ in the entire range of gate voltages. We will integrate the tunnelling generation rate $G_{b t b}$ for the TFET shown in Figure 4.1, which is in the ON-state ( $V_{G S}=-3 \mathrm{~V}$ and $\left.V_{D S}=-0.5 \mathrm{~V}\right)$. The variation of $G_{b t b}$ along the length of the channel in the tunnelling region is shown in Figure 6.5. As can be seen in the figure, $G_{b t b}$ is highest at the source-channel junction and decreases steeply to a negligible value within a few nanometres along the channel. The decrease is steepest at the source-channel junction and becomes less steep as we move along the channel. This nature of $G_{b t b}$ can be utilised to draw tangents at appropriate points on its graph. We then find the area under these tangents, giving us a good approximation of the integration of $G_{b t b}$. This method is therefore called the tangent line approximation method


Figure 6.5 Tunnelling generation rate $\left(G_{b t b}\right)$ at the surface along the channel of a TFET starting from the source-channel junction.


Figure 6.6 Tangent line $l_{l}$ to the $G_{b t b}$ curve. The shaded area is $G_{I}$.
[16, 17]. Let us now see how this method works. For simplicity, we will define the origin (i.e. $x=0$ ) at the source-channel junction.

We first start with drawing a tangent to the $G_{b t b}$ curve at $x=0$, that is the source-channel junction where $G_{b t b}$ has the maximum value, and call it line $l_{1}$, as shown in Figure 6.6. We extend this tangent line $\left(l_{1}\right)$ to the point where it intersects the $x$-axis $\left(x=L_{1}\right)$ and find the area $G_{1}$ under it (shaded region shown in Figure 6.6). Since the line $l_{1}$ is a tangent to the $G_{b t b}$ curve at $x=0$, its slope is equal to $G_{b t b}^{\prime}(0)$ and hence we get the following expressions for $L_{1}$ and $G_{1}$ :

$$
\begin{gather*}
L_{1}=G_{b i b}(0) / G_{b i b}^{\prime}(0)  \tag{6.25}\\
G_{1}=0.5 G_{b i b}^{\prime}(0) L_{1}^{2} \tag{6.26}
\end{gather*}
$$

At the $x$-intercept of the line $l_{1}$ (i.e. $x=L_{1}$ ), we draw another tangent line (line $l_{2}$ ) to the $G_{b t b}$ curve and extend it to the point where it intersects the $x$-axis $\left(x=L_{1}+L_{2}\right)$ and to the point where it meets the previous tangent line $l_{2}\left(x=L_{1}+L_{2}-L_{1 d}\right)$, as shown in Figure 6.7. We now find the area $G_{2}$ under this line $\left(l_{2}\right)$. We also find the area $G_{1 d}$ common under the lines $l_{1}$ and $l_{2}$ (shaded region shown in Figure 6.8). The slope of line $l_{2}$ is $G_{b t b}^{\prime}\left(L_{1}\right)$ and we get the following expressions:

$$
\begin{gather*}
L_{2}=G_{b i b}\left(L_{1}\right) / G_{b i b}^{\prime}\left(L_{1}\right)  \tag{6.27}\\
L_{1 d}=G_{b i b}^{\prime}(0) L_{2} /\left[G_{b i b}^{\prime}(0)-G_{b i b}^{\prime}\left(L_{1}\right)\right] \tag{6.28}
\end{gather*}
$$



Figure 6.7 Tangent line $l_{2}$ to the $G_{b t b}$ curve. The shaded area is $G_{2}$.


Figure 6.8 Tangent line $l_{3}$ to the $G_{b t b}$ curve. The shaded area is $G_{3}$.

$$
\begin{gather*}
G_{2}=0.5 G_{b i b}^{\prime}\left(L_{1}\right) L_{1 d}^{2}  \tag{6.29}\\
G_{1 d}=0.5 G_{b i b}^{\prime}(0)\left(L_{1 d}-L_{2}\right)^{2} \tag{6.30}
\end{gather*}
$$

We will now repeat the process outlined above once again. At the $x$-intercept of line $l_{2}$ (i.e. $x=L_{1}+L_{2}$ ), we draw a tangent line (line $l_{3}$ ) to the $G_{b t b}$


Figure 6.9 Area common to the tangent lines $l_{1}$ and $l_{2}$, that is $G_{1 d}$.
curve and extend it to the point where it intersects the $x$-axis ( $x=L_{1}+L_{2}+L_{3}$ ) and to the point where it meets line $l_{2}\left(x=L_{1}+L_{2}+L_{3}-L_{2 d}\right)$, as shown in Figure 6.9. We find the area $G_{3}$ under the line $l_{3}$ (shaded region shown in Figure 6.9) and also find the area $G_{2 d}$ common under the lines $l_{2}$ and $l_{3}$ (shaded region shown in Figure 6.10). The slope of line $l_{3}$ is $G_{b t b}^{\prime}\left(L_{1}+L_{2}\right)$ and we get the following expressions:

$$
\begin{gather*}
L_{3}=G_{b i b}\left(L_{1}+L_{2}\right) / G_{b i b}^{\prime}\left(L_{1}+L_{2}\right)  \tag{6.31}\\
L_{2 d}=\frac{G_{b i b}^{\prime}\left(L_{1}\right) L_{3}}{\left[G_{b i b}^{\prime}\left(L_{1}\right)-G_{b i b}^{\prime}\left(L_{1}+L_{2}\right)\right]}  \tag{6.32}\\
G_{3}=0.5 G_{b i b}^{\prime}\left(L_{1}+L_{2}\right) L_{2 d}^{2}  \tag{6.33}\\
G_{2 d}=0.5 G_{b i b}^{\prime}\left(L_{1}\right)\left(L_{2 d}-L_{3}\right)^{2} \tag{6.34}
\end{gather*}
$$

Finally, we add the areas under the three lines $l_{1}, l_{2}$ and $l_{3}$. Note that the areas common under any two lines have been added twice. Hence, we subtract the areas common between any two lines, giving us the total generation rate $G_{T}$ :

$$
\begin{equation*}
G_{T}=G_{1}+G_{2}+G_{3}-G_{1 d}-G_{2 d} \tag{6.35}
\end{equation*}
$$

$G_{T}$ gives us an approximate value of the integration of the tunnelling generation rate $G_{b t b}$. The accuracy of $G_{T}$ can be improved further by repeating the


Figure 6.10 Area common to the tangent lines $l_{2}$ and $l_{3}$, that is $G_{2 d}$.
process of drawing a tangent line and then finding the area under it, a greater number of times. For the $n$th repetition step, we will get the following set of equations:

$$
\begin{gather*}
L_{n}=\frac{G_{b t b}\left(L_{1}+L_{2}+L_{3}+\cdots+L_{n-1}\right)}{G_{b t b}^{\prime}\left(L_{1}+L_{2}+L_{3}+\cdots+L_{n-1}\right)}  \tag{6.36}\\
L_{n-1 d}=\frac{G_{b t b}^{\prime}\left(L_{1}+L_{2}+\cdots+L_{n-2}\right) L_{n}}{\left[G_{b t b}^{\prime}\left(L_{1}+L_{2}+\cdots+L_{n-2}\right)-G_{b t b}^{\prime}\left(L_{1}+L_{2}+\cdots+L_{n-1}\right)\right]}  \tag{6.37}\\
G_{n}=0.5 G_{b t b}^{\prime}\left(L_{1}+L_{2}+\cdots+L_{n-1}\right) L_{n-1 d}^{2}  \tag{6.38}\\
G_{n-1 d}=0.5 G_{b t b}^{\prime}\left(L_{1}+L_{2}+\cdots+L_{n-2}\right)\left(L_{n-1 d}-L_{n}\right)^{2} \tag{6.39}
\end{gather*}
$$

At the end of the $n$th step, the expression for $G_{T}$ is given as follows:

$$
\begin{equation*}
G_{T}=G_{1}+G_{2}+\cdots+G_{n}-G_{1 d}-G_{2 d}-\cdots-G_{n-1 d} \tag{6.40}
\end{equation*}
$$

The drain current $I_{d}$ can now be given as

$$
\begin{equation*}
I_{d}=q A_{K} G_{T} \tag{6.41}
\end{equation*}
$$

where $A_{k}$ is a constant, which incorporates the thickness $t_{i n v}$ of the inversion layer and is given by

$$
\begin{equation*}
A_{K}=\frac{A \times t_{i n v}}{E_{g}^{0.5}} \tag{6.42}
\end{equation*}
$$

where $A$ is the pre-exponential constant of Kane's model (Equation (6.17)).

Table 6.1 Accuracy of tangent line approximation

| Number of repetition steps | Accuracy |
| :--- | :---: |
| 1 | $53 \%$ |
| 2 | $80 \%$ |
| 3 | $89 \%$ |
| 4 | $92 \%$ |
| 5 | $93 \%$ |
| 6 | $93.6 \%$ |
| 7 | $93.7 \%$ |

We will now analyse the accuracy of the tangent line approximation method with a varying number of repetition steps. The accuracy of the tangent line approximation can be evaluated by dividing $G_{T}$ (Equation (6.40)) by the numerically evaluated value of the integration of $G_{b t b}(x)$, given in Figure 6.5. In Table 6.1, we show the accuracy of the tangent line approximation with the number of repetition steps $n$. As we can see here, the accuracy settles to around $93 \%$ after the 4th step. Hence, using 5 to 6 repetition steps gives us very good accuracy of the integration of $G_{b t b}$.

The method discussed in this section has closed-form equations and evaluates the integral of the tunnelling generation rate with more than $93 \%$ accuracy. Hence, by utilising the fact that any given function has a closed-form expression for its differential, we have developed a method for finding the drain current of a TFET that is computationally efficient and accurate at the same time. The final expressions for the drain current are closed-form equations and no iterations or numerical integrations are required at any step, making this method very useful for developing TFET models for circuit simulations.

### 6.3 Threshold voltage models

One of the most important parameters of a MOSFET is its threshold voltage. For a MOSFET, there is a clear physical definition of the threshold voltage - it is the gate voltage at which the magnitude of the conducting inversion layer charge in the channel is equal to the background concentration. However, since the current in a TFET is not controlled by the formation of a conducting inversion layer, but by modulation of the tunnelling barrier, this definition cannot be applied to TFETs.

Another point of dissimilarity from MOSFETs is that TFETs have two threshold voltages - the gate threshold voltage and the drain threshold voltage. The reason for this becomes clear if we remember our previous discussion
(Section 3.2.2.2) on the output characteristics of a TFET. We had studied that even if a high gate bias is applied, the TFET is in the OFF-state in the case of a low drain bias, as the channel potential is pinned to the drain potential. As the drain bias is increased, the TFET transitions from the OFF-state to the ON-state, thereby leading to a second threshold voltage, the drain threshold voltage.

Due to these reasons, the definitions of the threshold voltage used for a MOSFET could not be directly applied to TFETs. New definitions of the threshold voltage were needed for TFETs, some of which will be studied in this section.

### 6.3.1 Constant current method

This was one of the first methods used to extract the threshold voltage of a TFET and was based on practical considerations. The ON-state current of a TFET lies in the order of $110^{-6} \mathrm{~A} / \mu \mathrm{m}$. Drawing parallels with MOSFETs, the current at the threshold voltage should be an order of magnitude lower than the ON-state current. Therefore, in this method, the threshold voltage is defined as that gate (or drain) voltage at which the current in the device $I_{D S}=10^{-7} \mathrm{~A} / \mu \mathrm{m}$.

When extracted using this method, the gate threshold voltage displays negligible dependence on the drain voltage (if $V_{D S}>0.1 \mathrm{~V}$ ), as can be seen in Figure 6.11. Apart from this, as shown in Figure 6.12, there is a small amount of threshold voltage roll-off at low gate lengths.

The drain threshold voltage extracted by this method decreases with increasing gate voltage (Figure 6.13). This is because a higher gate voltage would lead to a greater electric field at the source-channel junction, even in case of pinning, leading to a higher current.

One variation of this method used in analytical modelling is the constant tunnelling length method, which we will now study.


Figure 6.11 Dependence of the gate threshold voltage on the drain voltage for an n-channel TFET.


Figure 6.12 Variation in the gate threshold voltage with the gate length for an n-channel TFET.


Figure 6.13 Dependence of the drain threshold voltage on the gate voltage for an n-channel TFET.

### 6.3.2 Constant tunnelling length

As described in Section 6.2.3, the drain current in a TFET is often written as a function of the tunnelling length (Equation (6.20)). For a particular device structure, the tunnelling length at which the current $I_{D S}=10^{-7}(\mathrm{~W} / \mathrm{L})$ A would be constant. This property is used to model the threshold voltage, study its dependence on the device biasing, to investigate effects of localised charges, etc.

In this method, the tunnelling length at which the device current $I_{D S}=10^{-7} \mathrm{~A}$ is calculated using either analytical or numerical methods [18-20]. Once this
threshold tunnelling length is known, it is equated to the tunnelling length as a function of the bias obtained from the analytical model. The gate (or drain) bias at which the tunnelling length equals the threshold tunnelling length is the threshold voltage of the device.

In the constant current (or tunnelling length) method, the threshold voltage is extracted by equating the current with an arbitrarily defined (albeit practically useful) value. The values of the threshold voltage extracted using this method give little insight into the physical functioning of the TFET, as they have no physical basis. Due to this, the transconductance change (TC) method, which is widely used in MOSFETs, was proposed to extract the threshold voltage in TFETs.

### 6.3.3 Transconductance change (TC) method

The transconductance change method defines the threshold voltage for any nonlinear device as the gate voltage at which the derivative of the transconductance $d g_{m} / d V_{G S}$ is maximum [21]. This definition has been validated for MOSFETs and shown to give the same results as the physical definition of the threshold voltage. This method was subsequently extended [22] to extract the threshold voltage for TFETs.

Observing the transfer characteristics of the TFETs, we find that the dependence of the current on the gate bias changes from quasi-exponential to linear with increasing gate bias. Similarly, from the output characteristics of the TFET, we find that the dependence of the current on the drain bias changes from quasiexponential to linear with increasing drain bias. This behaviour is the basis of threshold voltage extraction in the transconductance change method.

To calculate the gate threshold voltage, the transconductance $g_{m}$ is obtained as a function of the gate bias $V_{G S}$. The gate threshold voltage is defined as the gate bias at which $d g_{m} / d V_{G S}$ is maximum. Similarly, to calculate the drain threshold voltage, the conductance $g_{d s}$ is obtained as a function of the drain bias $V_{D S}$. The threshold voltage is defined as the drain bias at which $d g_{d s} / d V_{D S}$ is maximum.

The threshold voltages extracted using this method exhibit very different behaviour when compared to those extracted by the constant current method. The gate threshold voltage shows negligible roll-off with decreasing gate length (Figure 6.12), since the change in gate control from quasi-exponential to linear is independent of the gate length. Furthermore, the gate threshold voltage increases almost linearly with increasing drain voltage. The reason for this is that at a higher drain voltage, the channel potential would be pinned at a higher gate voltage, thereby causing a shift in gate control from quasi-exponential to linear at a higher gate voltage. Similarly, the drain threshold voltage also increases with increasing gate voltage, albeit in a non-linear manner (Figure 6.13).

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## 7

## Device simulation using ATLAS

Technology computer aided design (TCAD) simulations are indispensable tools for any device modeller. They enable us to observe 1D, 2D and 3D variations in different physical quantities such as surface potential, electric field, band-toband generation rate, electron/hole concentration, etc., that cannot be measured experimentally. Simulations can give us a clear insight into the functioning of the device, as the measurements here are not limited to the device terminals and can be done anywhere throughout the structure of the device. This can help a device modeller or a device designer to understand the effects of different parameters on the complete functioning of the device. However, simulations have their own limitations. One has to make sure that the simulation captures all the physical effects in the device accurately before putting them to any use.

Once the accuracy of the simulations is ensured, then simulations can be really useful for device modelling and for investigating new device designs. A simulation setup is inexpensive as compared to an experimental setup and also gives quick results. Therefore, one should formalise their ideas using simulations and then proceed towards fabricating their device.

In the present day, many commercial TCAD tools are available in the market. The leading ones are Silvaco ATLAS and Synopsys Sentaurus. Also, many organisations, universities and research centres have their own TCAD tools.

[^6]In this chapter, we will primarily focus on TFET device simulations using Silvaco ATLAS [1]. At any point in the text, the reader can refer to the ATLAS user manual available at http://www.silvaco.com/.

In TFET simulations, accuracy is a major challenge. Most of the tunnelling models are solutions to the quantum mechanical phenomenon of tunnelling and use numerous approximations. Therefore, TFET simulation results can differ from the experimental results in orders of magnitude. Hence, it is always necessary to calibrate the simulations with experimental data before using the simulation results for formulating any new idea on TFETs.

In the rest of the chapter we focus on explaining how the ATLAS simulations works and the challenges faced in running the simulations.

### 7.1 Simulations using ATLAS

ATLAS is a physics-based device simulator that predicts the electrical behaviour of a defined semiconductor structure and gives insights into the physical mechanism operating inside the device. The structure of the semiconductor device is divided into 2D or 3D grids (or mesh), as shown in Figure 7.1, and a set of five basic physics-based semiconductor equations is solved in a coupled manner at each mesh point. These equations are Poisson's equation, the electron and hole continuity equation and the electron and hole current equations. Using these equations, the electron and hold density and their transport through the device structure is simulated. In other words, using numerical methods, a set of basic equations in


Figure 7.1 Example of a $2 D$ mesh grid in ATLAS.
semiconductor physics and other user-specified physical models are solved in order to predict the operation of the device.

### 7.1.1 Inputs and outputs

Figure 7.2 shows the type of information flow in an ATLAS device simulation. It consists of two types of input files: a structure file, which defines the device structure and the mesh points, and a command file, which contains all the commands that ATLAS has to execute in order to perform the simulation. The simulation results are given in the form of three files. The first is the runtime output file, which tracks the progress of the simulation as it proceeds. The second is the log file, which saves all the terminal characteristics (currents and voltages) from the simulation. The third is the solution file, which saves the data of the 2D or 3D structure mesh points relating to the solution variables of the physical models solved for in the simulation at a given bias point.

The input file in ATLAS has a [.in] extension and contains both the structure and command inputs. An ATLAS input file is opened using DECKBUILD, an ATLAS interactive tool that provides an interactive runtime environment. Each line in the input file contains a statement. Each statement consists of a keyword (<STATEMENT>), which defines the statement and a set of parameters associated with the statement (<PARAMETER >). The general format of the statement is as follows:
<STATEMENT> <PARAMETER> <PARAMETER>=<VALUE>

For example:
DOPING UNIFORM N.TYPE CONCENTRATION=1.0e16 REGION=1
where DOPING is the keyword of the statement and UNIFORM, N.TYPE, CONCENTRATION, REGION are parameters. Any statement in ATLAS is case-invariant.


Figure 7.2 Flow of information in ATLAS.

There are five groups of statements that an ATLAS input file must contain in the correct order (as given below). These groups are:

1. Structure specification. This group of statements defines the structure of the model. This includes statements for defining the mesh, regions, electrodes, material, doping, etc.
2. Materials and models specification. This group of statements specifies the material parameters and physical models to be included in the simulations. This includes statements for defining material, models, contact properties, interface charges, etc.
3. Numerical method specification. This statement specifies the numerical methods to be used for solving the set of equations associated with the simulation.
4. Solution specification. This group of statements specifies the bias conditions for which the simulations have to be run. It includes statements that command the simulator to solve for the given bias conditions, log the solution and save it in a solution file.
5. Result analysis. This group of statements consists of commands to open and analyse the solution saved in a solution file.

Let us look briefly into each of the above five group of statements.

### 7.1.2 Structure specification

The first step of structure specification is defining the mesh. A mesh is a set of horizontal and vertical lines at the intersection of which the simulation equations are solved. The entire structure of the device has to be defined within the region covered by the mesh. A mesh is defined as follows:

MESH SPACE.MULT=<VALUE>

The above statement defines the scaling factor of the mesh. By default the value of the scaling factor is 1 . This is followed by a series of X.MESH and Y. MESH statements, to define the position and the spacing of the mesh lines:

```
X.MESH LOCATION=<VALUE> SPACING=<VALUE>
Y.MESH LOCATION=<VALUE> SPACING=<VALUE>
```

The location and spacing between the mesh lines are specified in $\mu \mathrm{m}$. ATLAS automatically inserts mesh lines between two specified X.MESH or Y.MESH
locations according to the given spacing. An example of defining a 2D mesh is as follows:

```
x.mesh loc=0.00 spac=0.50
x.mesh loc=1.15 spac=0.02
x.mesh loc=1.5 spac=0.1
x.mesh loc=1.85 spac=0.02
x.mesh loc=3 spac=0.5
Y.mesh loc=-0.017 spac=0.02
Y.mesh loc=0.00 spac=0.005
Y.mesh loc=0.1 spac=0.02
Y.mesh loc=0.2 spac=0.01
Y.mesh loc=0.6 spac=0.25
```

Note that any parameter term can be shortened. For example, LOCATION can be written as loc and SPACING as spac.

The second step is defining regions. Once the mesh is defined, each part of the mesh must be assigned a region and a material. This is done using the region statement:

REGION number=<integer> <position parameters> <material_type>

For example:

```
region num=1 y.max=0 oxide
region num=2 y.min=0 y.max=0.2 silicon
region num=3 y.min=0.2 oxide
```

This gives the regions shown in Figure 7.3.
While defining a mesh, it is always good practice to define the $x$-mesh and $y$-mesh locations at each line of interface between two regions. Otherwise, if a mesh line does not exist at the boundary of a region, then the nearest mesh line will be taken as the boundary. This may not give you the structure that you desired. Also, mesh lines should be made finer at the boundaries between two regions and in any other area where we expect a parameter to change rapidly, and the mesh can be made coarser in other areas. Making the mesh too fine can make the simulation runtime large. Hence, it is good practice to make the mesh as coarse as possible, without losing the capability of capturing the characteristics correctly.


Figure 7.3 Example of regions of a device structure in ATLAS.

The third step is defining the electrodes. Using the electrode statement does this:

ELECTRODE NAME=<electrode name> <position_parameters>

It is required to define at least one electrode contacting a semiconductor material. For example:

```
electrode name=gate x.min=1x.max=2 y.min=-0.017 y.max=-0.017
electrode name=source x.max=0.5 y.min=0 y.max=0
electrode name=drain x.min=2.5 y.min=0 y.max=0
electrode substrate
```

The substrate by default is taken at the highest position of $y$ defined in the $y$-mesh statements. A very critical point here is to make sure that a mesh line is present along the boundary of the electrode, especially when we are defining a 1D electrode like the electrodes in the above example. Otherwise the entire electrode itself may get defined at the nearest mesh line, completely changing the effect of the electrode in the simulations.

The final part is to define the doping of the semiconductor regions. This is done with the doping statement:

```
DOPING <distribution_type> <dopant_type>
```

<position_parameters>.

For example:

```
doping uniform conc=2e17 p.type reg=2
doping gaussn.type conc=1e20 char=0.2 lat.char=0.05 reg=2 x.r=1.0
doping gauss n.type conc=1e20 char=0.2 lat.char=0.05 reg=2 x.l=2.0
```

Doping profiles can be of different types and each type has its own set of parameters. In the above example, in the first statement, we define uniform doping in which we have to define two parameters: concentration (conc=2e17) and type (p.type). In the second and third statements, we define Gauss doping, which has the following parameters: type (n.type), peak concentration (conc=1e20), $y$-direction characteristic length (char=0.2) and lateral characteristic length (lat.char=0.05). The Gaussian doping profile is defined by the following expression:

$$
\begin{equation*}
N(y)=\operatorname{conc} \times \exp \left(-\left(\frac{Y}{\text { char }}\right)^{2}\right) \tag{7.1}
\end{equation*}
$$

### 7.1.3 Material parameters and model specification

After defining regions and materials in the structure specification section, in this section the first part is to specify the parameters of the materials (like bandgap, mobility, permittivity, etc.) defined in the structure specification section. This is done using the material statement. For example:

MATERIAL MATERIAL=Silicon EG300=1.12 MUN=1100

This sets the bandgap (EG300) and low field mobility (MUN) of silicon regions defined in the structure. If this part is skipped, then default values of material parameters are taken.

The second part is to define the contacts. This is done using the contact statement. For each electrode defined in the structure specification section, there is a contact statement where the work function or the material of the electrode is defined. For example:

```
contact name=gate n.poly;
CONTACT NAME=gate WORKFUNCTION=4.8;
```

Here we have two choices. We can either specify the material of the electrode, which will include all the default parameter values associated with the specified material in the simulation, or we can specify specific properties of the electrode material like work function, resistance, capacitance, inductance, etc. If no work function or material type is specified then the electrode is assumed to be an ohmic contact.

The third part is defining the physical models to be included in the simulations. Models of basic semiconductor physics (like Boltzmann carrier statistics, driftdiffusion, etc.) are defined by default and need not be defined in this section.

The physical models can be distributed into five categories: mobility, recombination, carrier statistics, impact ionisation and tunnelling. All models are defined using the model statement. Only the impact ionisation models are defined using the impact statement. For example:

MODELS CONMOB FLDMOB SRH FERMIDIRAC;
IMPACT SELB;

The above statement specifies standard concentration-dependent mobility, parallel field mobility, Shockley-Read-Hall recombination with fixed carrier lifetimes and Fermi-Dirac statistics. The Selberherr impact ionisation model is also specified here.

### 7.1.4 Numerical method specification

After defining the structure and the models, we need to define the numerical methods that ATLAS has to use in order to solve for the specified physical model equations and place on to the defined structure. This is done using the model statement. For example:

METHOD GUMMEL NEWTON;

This statement specifies that the Gummel method will be used to solve the physical model equations followed by the Newton method, in case the solution does not converge using the Gummel method.

### 7.1.5 Solution specification

After setting up the structure, models and methods of solution, we have to now obtain the solutions or, in other words, run the simulation. This is done by using the solve statement followed by specifying the bias. The syntax of this statement is as follows:

```
SOLVE VGATE=1.0;
```

There are two important rules here. First, any electrode for which the bias voltage is not defined in the given solve statement needs to have its value specified in the previous solve statement taken. Second, if the bias voltage of an electrode is never defined in a solve statement then its value is taken to be zero. For most of the analysis, sweeping one or more bias voltages is required. This is done as follows:

```
solve vgate=0.1 vstep=0.1 name=gate vfinal=1.5
```

This sweeps the voltage at the gate electrode from 0.1 to 1.5 V in steps of 0.1 V. A typical example of a solution specification is:

```
solve init
solve prev
solve vgate=-0.2
solve vdrain=0.05
solve vdrain=0.1
# ramp gate voltage
solve vgate=0.1 vstep=0.1 name=gate vfinal=1.5
```

As shown above using the symbol \#, we can specify a comment.

### 7.2 Analysis of simulation results

As shown in Figure 7.2, there are three types of outputs from an ATLAS simulation. The first type is the runtime output, which is displayed in a window at the bottom of DECKBUILD (Figure 7.4). This shows the status of the simulation as each statement of the input file is executed. The second type of output is the log file. To save information into a log file, we use the log statement. Its syntax is as follows:

```
LOG OUTF=<FILENAME>
```

To save the data into a log file, one has to open a $\log$ file using the log statement given above. Terminal characteristics of all solve statements following the $\log$ statement are stored in the log file. To stop the log file from storing more data, we have to close the log file using the logoff statement.

For example:

```
log outf=mosfet.log
solve vgate=0.1 vstep=0.1 name=gate vfinal=1.5
log off
```

The above statements store the terminal characteristics of the gate voltage sweep into the log file named mosfet.log and then close the log file.

The third type of output is the solution file (or the structure file). This is generated using the save statement. Its syntax is
save outf=<filename>.str


Figure 7.4 The DECKBUILD window.

The above save statement saves the solution data corresponding to each mesh point defined in the structure at the last specified bias point into the structure (.str) file. For example:
save outf=mosfet.str

The next part is viewing the $\log$ and the structure files stored in the simulation. This is done by opening the files in TONYPLOT, which is an ATLAS interactive tool like DECKBUILD and is used for plotting data. The following statement is used in the input file or separately in the command terminal to open the log and structure files using TONYPLOT:

```
tonyplot <filename>.log or tonyplot <filename>.str
```

Figure 7.5 shows a $\log$ file and Figure 7.6 shows a structure file opened in TONYPLOT, respectively. The log file opens as a 2D plot, where we can choose the quantities to display on the $x$ - and $y$-axes using the graphical user interface (GUI) of TONYPLOT. A structure file opens as a 2D schematic of the structure of the device showing different regions and electrodes of the device (such as source, drain, gate oxide, etc.). To view the spatial distribution of different physical quantities (potential, electric field, doping, etc.) we have to make a horizontal


Figure 7.5 Example of a log file in ATLAS.


Figure 7.6 Example of a structure file in ATLAS.
or vertical cutline using the cutline option. This opens a 2D plot where we can choose to plot different physical quantities along the cutline. In the 2 D schematic of the structure itself, we can also see the mesh lines and contours of different physical quantities.

After understanding the basics of ATLAS simulation, in the following section we will illustrate how to run an ATLAS simulation using an example of a basic device structure.

### 7.3 SOI MOSFET example

In this section, we will illustrate how to run an ATLAS simulation, with the example of a SOI MOSFET. The structure of the SOI MOSFET is shown in Figure 7.7 To simulate this structure the following parameters are chosen: channel length $(L)=1 \mu \mathrm{~m}$, length of source and drain regions $=1 \mu \mathrm{~m}$, source/drain doping (Gaussian profile) peak concentration $=10^{20} / \mathrm{cm}^{3}$, body doping $\left(N_{A}\right)=2 \times$ $10^{17} / \mathrm{cm}^{3}$, gate oxide thickness $\left(T_{o x}\right)=17 \mathrm{~nm}$, silicon film thickness $\left(T_{S i}\right)$ and buried oxide thickness $=400 \mathrm{~nm}$. The gate is of n-type poly-silicon and has a length $=$ $1 \mu \mathrm{~m}$. The ATLAS input file for the simulation is given in Table 7.1.

In Table 7.1, go at las (Line 1) specifies that ATLAS has to be used for the simulation and TITLE (Line 2) is the title of the simulation and is always mentioned at the second line of the input file, but this is optional and may not always be specified. Then mesh space.mult $=1.0$ (Line 3) specifies the scaling factor of the mesh which is defined equal to 1 . The statement x . mesh (Lines 4 to 8 ) defines the $x$-mesh. Here we start with defining a mesh point at $x=0$, which is the starting point of the structure. Since $x=0$ is the beginning of the source region, not much of a spatial variation in physical quantities is expected here and hence we define a coarse mesh spacing of $0.5 \mu \mathrm{~m}$. Then we define a mesh point at the source-body junction $(x=1.15)$. This is the boundary between two regions and steep variations are expected here in different physical quantities. Therefore we define a fine mesh


Figure 7.7 Schematic of the SOI MOSFET under study.

## Table 7.1 Example code in ATLAS for SOI MOSFET simulation

| Line1: | go atlas |
| :---: | :---: |
| Line2: | TITLE SOI device simulation |
| \# SILVACO International 1992, 1993, 1994, 1995, 1996 |  |
| \# |  |
| \# 0.2um of silicon on 0.4 um oxide substrate |  |
| \# |  |
| Line3: | mesh space.mult=1.0 |
| \# |  |
| Line4: | $\mathrm{x} . \mathrm{mesh} \mathrm{loc}=0.00$ spac $=0.50$ |
| Line5: | x. mesh loc=1.15 spac=0.02 |
| Line6: |  |
| Line7: | $\mathrm{x} . \mathrm{mesh} \mathrm{loc}=1.85$ spac=0.02 |
| Line8: | $x . m e s h l o c=3 \quad$ spac $=0.5$ |
| \# |  |
| Line9: | Y.mesh loc $=-0.017$ spac $=0.02$ |
| Line10: | $y$. mesh loc $=0.00 \quad$ spac $=0.005$ |
| Line11: | y.mesh loc=0.1 spac=0.02 |
| Line12: | y.mesh loc=0.2 spac=0.01 |
| Line13: | $y . m e s h l o c=0.6 \quad$ spac $=0.25$ |
| \# |  |
| Line14: | region num=1 y.max=0 oxide |
| Line15: | region num=2 y.min=0 y.max=0.2 silicon |
| Line16: | region num=3y.min=0.2 oxide |
| \# |  |
| \#*********** define the electrodes ************ |  |
| \# \#1-GATE \#2-SOURCE \#3-DRAIN \#4-SUBSTRATE (below oxide) |  |
| \# |  |
| $\begin{aligned} & \text { Line17: electrode name=gate } x . m i n=1 x . m a x=2 y . m i n= \\ & -0.017 y . m a x=-0.017 \end{aligned}$ |  |
| Line18: electrode name=sourcex.max=0.5y.min=0y.max=0 |  |
| Line19: electrode name=drain $x . m i n=2.5 y . m i n=0 y . m a x=0$ |  |
| Line20: electrode substrate |  |
| \# |  |
| \#*********** define the doping concentrations ***** |  |
| \# |  |
| Line21: doping uniform conc=2e17 p.type reg=2 |  |
| Line22: doping gaussn.type conc=1e20 char=0.2 lat. char=0.05 reg=2 x.r=1. 0 |  |
| Line23: doping gaussn.type conc=1e20 char=0.2 lat. char=0.05 reg=2 $\mathrm{x} . \mathrm{l}=2.0$ |  |
| \# |  |

Table 7.1 (continued)

```
# set interface charge separately on front and back oxide
    interfaces
Line24: interf qf=3e10 y.max=0.1
Line25: interf qf=1e11 y.min=0.1
#
# set workfunction of gate
Line26: contact name=gate n.poly
#
# select models
Line27: models conmob srh auger bgn fldmob print
#
Line28: solve init
#
# do IDVG characteristic
#
Line29: method newton trap
Line30: solve vdrain=0
Line31: solve vdrain=0.05
Line32: solve vdrain=0.1
#
# ramp gate voltage
Line33: log outf=soiex.log
Line34: solve vgate=0.1 vstep=0.1 name=gate vfinal=1.5
Line35: save outf=test.str
#
#
Line36: quit
```

spacing of $0.02 \mu \mathrm{~m}$ here. Now, in the centre of the channel, we expect a gradual spatial variation in physical quantities and hence we can afford a coarser mesh here. Then we define a mesh point at $x=1.5$ with a spacing of $0.1 \mu \mathrm{~m}$. Similar to the source-body junction we define a finer mesh point at the drain body junction, that is $x=1.85$, with a spacing of $0.01 \mu \mathrm{~m}$. Finally, we define the end point of the device at $x=3$ with a spacing of $0.5 \mu \mathrm{~m}$, similar to that at the starting point.

The statement $y$. mesh (Lines 9 to 13) is used to specify the mesh lines of the structure in the $y$-direction. It is common practice to place the silicon-dielectric interface at $y=0$. Since the gate oxide thickness in this example is 17 nm , we start by defining our starting mesh point at $y=-0.017$. This is the position of the gate.

Note that although the gate is shown to have a finite thickness in Figure 7.7, in an input file we can define the gate with zero thickness and a finite length. Between $y=-0.017$ and $y=0$, we need a fine meshing (e.g. $0.02 \mu \mathrm{~m}$ ) because these mesh lines are inside the thin dielectric where we expect to observe strong variation in physical quantities as we move along the $y$-direction. Now we define the next mesh point at $y=0$, that is the silicon-dielectric interface with a very fine mesh spacing of $0.005 \mu \mathrm{~m}$. These mesh lines would be inside the channel and hence we require a very fine mesh in this region. As we move away from the silicon-silicon dioxide interface in the $y$-direction, we do not need such a fine mesh away from the inversion layer. Therefore, we define the next mesh point at $y=0.1$ with a spacing of $0.02 \mu \mathrm{~m}$. Now at the silicon-buried oxide interface, that is at $y=0.2$, we define the next mesh point with a spacing of $0.01 \mu \mathrm{~m}$. Next we define the end point of the mesh at $y=0.6$, with a coarse spacing of $0.25 \mu \mathrm{~m}$. These mesh lines are inside the buried oxide, where we expect a very gradual spatial variation in the physical quantities. Figure 7.8 shows the mesh grid of the structure generated after running the mesh statements of the above file.

Having defined the mesh of the structure, we now need to define the different regions of the structure. The statement region (Lines 13 to 15 ) is used for this purpose. The device has three regions. Each region is identified by a number (e.g. num=1). Number 1 in the region statement is the gate oxide region, which starts form the first $y$-mesh line and ends at $y=0$ and extends throughout the device in the $x$-direction, that is from $x=0 \mu \mathrm{~m}$ to $x=0.3 \mu \mathrm{~m}$. Number 2 is the silicon region, which starts from $y=0$ to $y=0.02$ and extends throughout the device in the


Figure 7.8 Mesh grid of the SOI MOSFET example.
$x$-direction. Number 3 is the buried oxide region, which starts at $y=0.02$ and ends at the last $y$-mesh point and extends throughout the device in the $x$-direction.

The statement electrode (Lines 16 to 19) is used to define different electrodes in the device structure. First we define the gate electrode, which is a 1D electrode at the top of the structure, that is at $y=-0.017$, and extends from $x=$ $1 \mu \mathrm{~m}$ to $x=2 \mu \mathrm{~m}$. Next, we define the source electrode, which is a 1D electrode at $y=0$ and extends from $x=0 \mu \mathrm{~m}$ to $x=0.5 \mu \mathrm{~m}$. The source electrode is, generally, separated by the gate electrode using a sidewall oxide. Therefore, it is preferable to define the source electrode not covering the entire source region. This is followed by the drain electrode, which is a 1D electrode at $y=0$ and extends from $x=0.25 \mu \mathrm{~m}$ to $x=3 \mu \mathrm{~m}$. Lastly, we define the substrate. If the coordinates of the substrate electrode are not defined, the substrate electrode is taken to be at the maximum $y$-mesh point extending all over the device structure in the $x$-direction. Figure 7.9 shows the electrodes of our device.

The statement doping (Lines 20 to 22) is used to define the type and the profile of doping in the silicon region of the device. We begin with defining a uniform doping of $2 \times 10^{17} \mathrm{~cm}^{-3}$ (p-type), in the entire silicon region of the device (Line 20). Next (Lines 21 and 22) we define a Gaussian doping profile in the source and drain regions of the device of n-type with peak concentration $=1 \mathrm{e} 20 / \mathrm{cm}^{3}, y$-direction characteristic length $=0.2 \mu \mathrm{~m}$ and lateral characteristic length $=0.05 \mu \mathrm{~m}$. The statement interf (Lines 23 and 24) is used to define the interface charges at the gate oxide-silicon interface and buried oxide-silicon interface. The statement contact defines the gate electrode to be n-type polysilicon (Line 25). This defines the properties (work function, resistivity, inductance, capacitance, etc.) of


Figure 7.9 Electrodes of the SOI MOSFET example.
the gate material. The model statement (Line 26) describes the types of model to be used in the simulation. In this simulation, we include the models for concen-tration-dependent mobility (conmob), srh recombination (srh), auger recombination (auger), bandgap narrowing (bgn) and lateral electric field dependent mobility (fldmob). The parameter print prints the status of all models (i.e. coefficients and constants) in the runtime output. The statement solve init (Line 27) runs the simulation for finding the initial solution, that is with all terminals at zero bias. In the method statement (Line 28) we define that Newton and trap methods will be used for solving the physics-based semiconductor equations and the specified model (in Line 26) equations. Now we run the simulation for finding the $I_{D}-V_{G S}$ characteristics of the device in the linear region. Using the solve statement (Lines 29 to 31), we solve the simulation for the desired bias points. Using the log statement (Line 32), we open a log file named soiex.log, which is followed by ramping the gate voltage from 0.1 to 1.5 V in steps of 0.1 V (Line 33), and the terminal characteristics of this voltage sweep get stored in the $\log$ file soiex.log. Finally, we save the structure file of the device using the save statement (Line 35).

Figure 7.10 shows the structure file (soiex.str) of the SOI MOSFET opened in TONYPLOT and the doping contours and different regions of the device. Figure 7.11 shows the log file (soiex.log) opened in TONYPLOT. It displays the transfer characteristics ( $I_{D}$ versus $V_{G S}$ ) of the device, corresponding to the gate voltage sweep in Line 34 of Table 7.1 at a drain voltage of 0.1 V .


Figure 7.10 The structure file of the SOI MOSFET example showing the different regions and the doping profile (in log scale) in the device.


Figure 7.11 The log file of the SOI MOSFET example showing the drain current $\left(I_{D}\right)$ versus the gate voltage $\left(V_{G S}\right)$ characteristics.

## Reference

[1] ATLAS Device Simulation Software, Silvaco Int., Santa Clara, CA, USA, 2015.

## 8

## Simulation of TFETs

In the previous chapter, we started with basic device simulation using ATLAS [1]. We will now move towards the simulation of TFETs for 2D as well as 3D device structures. In the simulation of TFETs, we use different tunnelling models that require calibration with experimental data. In this chapter, we will discuss the simulation of a 2D SOI TFET followed by the simulation of a 3D gate all around nanowire TFET.

### 8.1 SOI TFET

A TFET has a structure similar to that of a MOSFET with only a difference in the type of source doping. Most of the tunnelling in a TFET occurs at the sourcechannel junction and hence this region of the device becomes important in simulation for predicting the results accurately. The two key points in the simulation of a TFET are defining an appropriate mesh and choosing the right tunnelling model parameters.

In this section, we will describe the simulation of an SOI TFET. Figure 8.1 shows the schematic of an SOI TFET. It has a channel length ( $L$ ) of 200 nm and a gate oxide thickness $\left(T_{o x}\right)$ of 2 nm . The thickness of the silicon film $\left(T_{S i}\right)$ is 10 nm . The source/drain regions have a doping of $10^{21} / \mathrm{cm}^{3}$ and a length of 50 nm . This is a p-channel TFET and hence the source doping is of n-type and the drain doping is of p-type. The buried oxide thickness is 180 nm . The gate metal

[^7]

Figure 8.1 Schematic of the SOI TFET under study.
wok function is 4.8 eV in order to enhance band-to-band tunnelling at the source channel junction. The input file for the TFET simulation is shown in Table 8.1.

Section 1 of the file given in Table 8.1 defines the mesh of the structure. In a TFET, most of the tunnelling occurs at the source-channel junction and hence it is necessary to define a finer mesh at this junction. Due to a low ON-state current, there is less variation in physical quantities in the rest of the channel and hence we can afford to have a coarser mesh in the channel. Drain side tunnelling is an important phenomenon in the TFET and hence we should define a finer mesh at the drain-channel junction. In the $y$-direction, the electrostatics is mostly like a MOSET unless we are defining a vertical tunnelling structure and hence $y$-meshing is similar to that in the SOI MOSFET example described previously (Section 7.3). Sections 2 to 5 of the file are similar to the SOI MOSFET except that the source doping is n-type in the SOI TFET. Section 6 is the most important part of the simulation. Here we have to choose an appropriate tunnelling model and the correct values of the parameters of the tunnelling model. Although all the default parameter values in ATLAS are defined for silicon-based devices, using these default parameters for tunnelling can give an orders of magnitude difference between the simulations and the experimental results. Hence, it is very important to calibrate the simulation results with the characteristics of an experimental device before using the simulation for further study of the TFET. In this example, we have used Kane's model for the band-to-band tunnelling model, which is a local model used for tunnelling. The tunnelling parameters given in Section 6 (a.btbt, b.btbt and bbt . gamma) of the file are for Kane's model equation:

$$
\begin{equation*}
G_{B B T}=\frac{D \times B B T . A_{-} K A N E}{E_{g}^{0.5}} F^{B B T . G A M M A} \exp \left(-B B T . B_{-} K A N E \frac{E_{g}^{1.5}}{F}\right) \tag{8.1}
\end{equation*}
$$

where $G_{B B T}$ is the tunnelling generation rate, $F$ is the local electric field, $E_{g}$ is material bandgap, $D$ is the statistical factor and BBT.A_KANE (a.btbt), $B B T . G A M M A$ (bbt.gamma) and BBT.B_KANE (b.btbt) are the tunnelling

Table 8.1 Example code in ATLAS for an SOI TFET simulation

```
go atlas
TITLE SOI TFET device simulation
# SILVACO International 1992, 1993, 1994, 1995, 1996
#
# 10 nm of silicon on 180 nm oxide substrate
#
#Section 1: Defining the Mesh
mesh space.mult=1.0
#
x.mesh loc=0.00 spac=0.025
x.mesh loc=0.05 spac=0.0002
x.mesh loc=0.08 spac=0.0002
x.mesh loc=0.15 spac=0.001
x.mesh loc=0.25 spac=0.002
x.mesh loc=0.3 spac=0.025
#
y.mesh loc=-0.002 spac=0.001
y.mesh loc=0.00 spac=0.0001
y.mesh loc=0.005 spac=0.001
y.mesh loc=0.01 spac=0.001
y.mesh loc=0.06 spac=0.025
#
#Section 2: Defining the regions
region num=1 y.max=0 oxide
region num=2 x.min=0.25 x.max=0.3 y.min=0 y.max=0.01
    silicon
region num=3 x.min=0.05 x.max=0.25 y.min=0 y.max=0.01
    silicon
egion num=4 x.min=0.00 x.max=0.05 y.min=0 y.max=0.01
    silicon
region num=5 y.min=0.01 oxide
#
#Section 3: Defining the electrodes
# #1-GATE #2-SOURCE #3-DRAIN #4-SUBSTRATE (below the
    buried oxide)
#
electrode name=gate x.min=0.05 x.max=0.251 y.min=
    -0.002 y.max=-0.002
electrode name=source x.max=0.05 y.min=0 y.max=0
electrode name=drain x.min=0.251 y.min=0 y.max=0
electrode substrate
#
```

Table 8.1 (continued)

```
#Section 4: Defining the doping concentrations
#
doping uniform conc=1e21 p.type reg=2
doping uniform conc=1e21 n.type reg=4
doping uniform conc=6.5e15 n.type reg=3
#
#Section 5: Defining material properties
contact name=gate workfunction=4.8
#
# Section 6: Defining the models
models conmob srh auger bgn fldmob print bbt.kane a.
    btbt=4e19 b.btbt=41e6 bbt.gamma=2
#
#Section 7: Solving the simulation
solve init
#
method gummel newton
#
solve vsource=0.0
solve vdrain=0.0
solve vdrain=0
solve vdrain=-0.5
solve vdrain=-1
#for output characteristics uncomment lines below
#solve vgate=-0
#solve vgate=-1
#solve vgate=-1.5
#solve vgate=-2
#
# ramp the gate voltage
log outf=test.log master
solve vgate=0 vstep=-0.1 name=gate vfinal=-3
#for output characteristics uncomment line below
# solve vdrain=0 vstep=-0.1 name=drain vfinal=-3
output val.band con.band u.bbt charge e.lines band.param
save outf=test.str
#Section 8: Reading the results
tonyplot test.log
tonyplot test.str
#
quit
```

parameters. These parameters are calibrated against experimental results [2, Figure 6(a)], as shown in Figure 8.2 [3]. The simulator calculates the tunnelling rate $\left(G_{B B T}\right)$ and integrates it in the entire silicon film to find the drain current. As can be seen from Equation (8.1), tunnelling is an exponential function of the electric field and hence the tunnelling rate is maximum at the source-channel junction, where the electric field is maximum. At all other locations the tunnelling generation rate is negligible, as can be seen in Figure 8.3. According to Kane's paper [4] the tunnelling parameter $B B T . G A M M A$ is equal to 2 for direct bandgap materials like Ge and 2.5 for indirect bandgap materials like Si . However, BBT.GAMMA can be fine-tuned between 2 and 2.5. The statistical factor $D$ is by default taken to be 1 , but for a TFET, this may give non-zero current at zero drain voltage.

Hence, while simulating output characteristics, one should include either bbt. hurkx, bbt. dehurkx or bbt. djhurkx models for finding the value of $D$. All the other models defined here are similar to that in the SOI MOSFET example described in Section 7.3. In Section 7 of Table 8.1, we specify both the gummel and the newt on methods, which are the numerical methods for solving the simulation equations. This specification starts the process of finding the solution with the Gummel method, which solves the equations in a fully decoupled way and makes an initial guess. This is followed by the use of the Newton method, which solves the equations in a fully coupled way to find the final solution. This ensures better and faster convergence of the simulation. Also, in the output statement, we specify statements for including valance band energy,


Figure 8.2 Reproduction of experimental results [2, Figure 6(a)] to calibrate the tunnelling parameters [3]. Source: Reproduced with permission of IEEE.


Figure 8.3 Tunnelling generation rate at the silicon-oxide interface (i.e. at $y=$ $0)$ along the channel (i.e. $x$-direction).


Figure 8.4 The structure file of the SOI TFET example showing the different regions and the doping profile (in log scale) in the device.
conduction band energy, tunnelling generation rate, net charge, electric field lines and band parameters in the standard structure (test.str) file.

Figure 8.4 shows the structure file (test . str) open in TONYPLOT. It displays the doping profile and different regions of the device. Figure 8.5 show a


Figure 8.5 The structure file of the SOI TFET showing the potential plotted along a horizontal cutline made at the surface of the device (i.e. at $y=0$ ).


Figure 8.6 The log file of the SOI TFET example showing the drain current ( $I_{D}$ ) versus gate voltage ( $V_{G S}$ ) characteristics.
horizontal cutline made at the surface in the structure file (test.str) and the potential profile plotted along the cutline. Figure 8.6 shows the $\log$ file (test.log) open in TONYPLOT. It displays the transfer characteristics
( $I_{D}$ versus $V_{G S}$ ) of the device, corresponding to the gate voltage sweep in Section 7 of Table 8.1 at a drain voltage of -1 V .

We will now discuss different tunnelling models that can be used for the simulation of a TFET.

### 8.2 Other tunnelling models

In the SOI TFET example described in the previous section, we have used Kane's model to find the generation rate of the carriers due to band-to-band tunnelling. However, there are some other models that can be used to find the carrier generation rate due to band-to-band tunnelling. In this section, we will describe a couple of commonly used models.

### 8.2.1 Schenk band-to-band tunnelling model

A model that includes a comprehensive study of phonon-assisted tunnelling or indirect tunnelling, is the Schenk band-to-band tunnelling model. This model neglects direct band-to-band tunnelling and hence is only good for indirect tunnelling materials such as silicon. It is also a local model like Kane's model and can be used by specifying SCHENK. BBT in the model statement. The band-to-band generation rate in the Schenk model is given by

$$
\begin{align*}
G_{B B T}= & \text { A.BBT.SCHENK } \times F^{7 / 2} \times S \\
& \times\left(\frac{\left(A^{\mp}\right)^{-3 / 2} \exp \left(\frac{A^{\mp}}{F}\right)}{\exp \frac{(H W \cdot B B T \cdot S C H E N K)}{k T}-1}+\frac{\left(A^{ \pm}\right)^{-3 / 2} \exp \left(\frac{A^{ \pm}}{F}\right)}{1-\exp \frac{(-H W \cdot B B T \cdot S C H E N K)}{k T}}\right) \tag{8.2}
\end{align*}
$$

where $A^{ \pm}=B \cdot B B T . S C H E N K(\hbar \omega \pm H W \cdot B B T . S C H E N K)^{3 / 2}, S$ is the statistical factor dependent on carrier concentration and $\hbar \omega$ is the energy of phonon. This model has three parameters, A.BBT.SCHENK, B.BBT.SCHENK and HW.BBT. SCHENK. These three parameters have to be calibrated with experimental results. As compared to Kane's model, this model is more accurate for silicon as it is an indirect bandgap semiconductor, but cannot be used for direct bandgap materials [5].

### 8.2.2 Non-local band-to-band tunnelling

Both Kane's and Schenk's models are local tunnelling models, which find the tunnelling generation rate at a given point based on the electric field at that point.

A non-local band-to-band tunnelling model (bbt.nonlocal), on the other hand, finds the tunnelling generation rate across a tunnelling length and incorporates the change in the electric field along the tunnelling length. This model is more accurate for reverse biased tunnelling junctions with high doping. To use this model, we need to define a special mesh called the qt mesh in the tunnelling region. Also, the tunnelling direction has to be defined separately using qtunnel. dir in the model statement. This model finds the tunnelling rate in parallel slices (which are defined by the qt mesh) along the tunnelling direction. The qt mesh interpolates data between underlying device mesh points (defined by the initial mesh statements) and performs tunnelling calculations. The qt mesh should contain only a single $\mathrm{p}-\mathrm{n}$ junction and should cover it entirely. To begin with, a coarser qt mesh can be defined, which can then be made finer till the point where no change in simulation results is observed. The following describes the qt meshing for the SOI TFET given in Figure 8.1:
qtx.mesh loc $=0.04$ spac $=0.0001$
qtx.mesh loc=0.1 spac=0.0002
qty.mesh loc $=0.000$ spac $=0.00001$
qty.mesh loc=0.002 spac=0.00005

There are other ways for defining the qt mesh as well, which can be found in the ATLAS manual. The non-local band-to-band tunnelling model integrates the tunnelling of carriers across the bandgap at all the energies that lie between the valence band in the source and the conduction band in the channel. The model uses material parameters me.tunnel (tunnelling mass of electron) and mh.tunnel (tunnelling mass of hole) that can be defined in the material statements. The tunnelling current is exponentially dependent on the tunnelling mass and, therefore, me.tunnel and mh. tunnel should be used as parameters to calibrate the model with experimental results. For example:
material material=Silicon me.tunnel=0. 14 region=2 models bbt.nonlocal qtunn. dir=1 bgn consrh conmob print

Here the electron effective tunnelling mass is defined to be 0.14 . The model's statement defines the use of the non-local band-to-band tunnelling model (bbt . nonlocal) with the tunnelling direction $x$ (qtunn. dir=1). The non-local tunnelling model works only with the newton method. Hence, we cannot use the gummel model together with the non-local tunnelling model.

We will now move on to the simulation of 3D TFETs with the example of a gate all around (GAA) nanowire TFET.

### 8.3 Gate all around nanowire TFET

In this section, we describe the simulation of a 3D TFET structure using ATLAS 3D. The most popular 3D structure studied is the nanowire transistor. Hence, we will study the simulation of a gate all around (GAA) nanowire TFET. The structure of the nanowire TFET is shown in Figure 8.7 The device is a p-channel TFET. The radius of the silicon nanowire $\left(T_{S i}\right)$ is 10 nm and the gate oxide thickness $\left(T_{o x}\right)$ is 2 nm . The length of the channel region is 200 nm and of the source/drain region is 50 nm . Hence, the total length of the silicon nanowire is 300 nm . The source/ drain doping is $10^{21} / \mathrm{cm}^{3}$ and the gate work function is 4.8 eV . Table 8.2 gives the input file for the simulation of the GAA nanowire TFET.

The first and the most important step in any 3D simulation is defining a 3D mesh. Section 1 in Table 8.2 defines a 3D cylindrical mesh. The first statement (mesh three.d cylindrical) instructs the ATLAS to create a fully 3D cylindrical mesh. A cylindrical mesh is specified in terms of radius ( $r$ ), angle (a) and $z$ coordinate. In our nanowire TFET structure, the radius $(r)$ is analogous to the $y$-direction of the 2D SOI TFET, the $z$-direction is analogous to the $x$-direction of the 2D SOI TFET and the angle (a) adds the third dimension. As done previously in the 2D SOI TFET (Section 8.1), the $z$-mesh should be finer at the source-channel junction and coarser at other positions. The $r$ mesh should be finer at the silicon oxide interface and coarser at the centre. As shown in Figure 8.7, the 3D TFET has gate wrapped all around the structure and hence the electrostatics are invariant as we move along the angular (a) direction. Hence, we take a coarse mesh with equal spacing of $45^{\circ}$ in the $a$-direction. In general, the mesh points in a 3D structure can be several orders larger in number compared to a corresponding 2D structure. Hence, it is necessary to define a coarser mesh for a 3 D device structure as compared to a 2D device structure. A good practice is to start with a coarse mesh and then make it finer till the point where no change in


Figure 8.7 Schematic view of the GAA nanowire TFET.

## Table 8.2 Example code in ATLAS for GAA TFET simulation

go atlas
TITLE GAA device simulation
\#Section 1: Defining the 3D cylindrical mesh
mesh three.d cylindrical
R.MESH LOCATION=0.0 SPACING=0.002
R.MESH LOCATION=0.010 SPACING=0.0001
R.MESH LOCATION=0.012 SPACING=0.0005
R.MESH LOCATION=0.014 SPACING=0.001
A. MESH LOCATION=0 SPACING=45
A.MESH LOCATION=360 SPACING=45
Z.MESH LOCATION=-0.15 SPACING=0.025
Z.MESH LOCATION=-0.10 SPACING=0.002
Z.MESH LOCATION=0.0 SPACING=0. 02
Z.MESH LOCATION=0.10 SPACING=0.002
Z.MESH LOCATION=0.15 SPACING=0.025
\#Section 2: Defining the regions
REGION NUM=1 MATERIAL=silicon $\mathrm{Z} . \mathrm{MIN}=-0.15 \mathrm{Z} . \mathrm{MAX}=0.15 \mathrm{~A}$. $\mathrm{MIN}=0 \mathrm{~A} . \mathrm{MAX}=360.0 \mathrm{R} . \mathrm{MAX}=0.010$
REGION NUM=2 MATERIAL=OXide R.MIN=0.01 R.MAX=0.014 Z. $\mathrm{MIN}=-0.15 \mathrm{Z} . \mathrm{MAX}=0.15 \mathrm{~A} . \mathrm{MIN}=0 \mathrm{~A} . \mathrm{MAX}=360.0$

## \#Section 3: Defining the electrodes

ELECTRODE NAME=source Z.MIN=-0.15 Z.MAX=-0.10R.MIN=0.01 R.MAX=0. 011

ELECTRODE NAME=drain Z.MIN=0. $10 \mathrm{Z} . \mathrm{MAX}=0.15 \mathrm{R} . \mathrm{MIN}=0.01 \mathrm{R}$. MAX=0. 011
ELECTRODE NAME=gate Z.MIN=-0. 10 Z. MAX $=0.102$ R.MIN $=0.012$
\#Section 4: Defining the doping
doping uniformp.type conc=1e20 reg=1r.min=0r.max=0.01 a.min=0 a.max $=360 \mathrm{z} \cdot \min =0.10 \mathrm{z} . \max =0.15$
doping uniformn.type conc=1e16 reg=1r.min=0r.max=0.01 a.min=0 a.max=360 z.min=-0.10 z.max=0.10
doping uniformn.type conc=1e20 reg=1r.min=0r.max=0.01 $a . \min =0 \mathrm{a} . \max =360 \mathrm{z} \cdot \mathrm{min}=-0.15 \mathrm{z} \cdot \max =-0.10$

Table 8.2 (continued)

```
#Section 5: Defining the material properties
contact name=gate workfunction=4.8
```

```
#Section 6: Defining the models
models conmob srh auger bgn fldmob print bbt.kane a.
    btbt=4e19 b.btbt=41e6 bbt.gamma=2
```

\#Section 7: Solving the simulation
solve init
\# do IDVG characteristic
method gummel newton maxtrap=10
solve vdrain=0
solve vdrain=-0.05
solve vdrain=-0.1
solve vdrain=-0.2
solve vdrain=-0.5
\# ramp gate voltage
log outf=testi.log
solve vgate=0 vstep=-0.1 name=gate vfinal=-3
save outf=testl.str
\#Section 8: Reading the results
tonyplot testi.log
tonyplot -3d nohw test.str
quit
simulation results are observed. The regions, electrodes and doping all have to be defined in cylindrical coordinates. In a 3D structure, all electrodes are required to have a finite thickness. Electrodes with zero thickness (i.e. 2D electrodes) will give a runtime error. To ensure convergence it is very important to include the Gummel method before the Newton method so that it makes a good initial guess of the solution. Without the Gummel method, the Newton method alone may take a long time to converge or may not converge at all. Also, if the simulations do not converge, it is advisable to take smaller bias steps and to define the maxtrap parameter, a value greater than its default value, which is 4. Maxtrap defines the maximum number of steps by which ATLAS will reduce the bias steps by itself (when the simulation is not converging) before exiting the simulation. Hence, by increasing the maxtrap value, ATLAS goes to smaller bias step values, which may make the simulation converge. The structure file of 3D simulations


Figure 8.8 The structure file of the GAA nanowire TFET opened in TONYPLOT3D.
opens in TONYPLOT3D, which needs a graphics card above a certain configuration and may not open in all computers. The one in which it opens looks like that shown in Figure 8.8. To study the spatial variation in any physical quantity, we have to draw a cutplane in the structure, which will export all the data corresponding to that cutplane into a regular 2D TONYPLOT.

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